## Yuan Wen Hau

List of Publications by Year in descending order

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		1684188	1372567
33	152	5	10
papers	citations	h-index	g-index
33	33	33	147
all docs	docs citations	times ranked	citing authors

#	Article	IF	CITATIONS
1	Use of learning approaches to predict clinical deterioration in patients based on various variables: a review of the literature. Artificial Intelligence Review, 2022, 55, 1055-1084.	15.7	5
2	A Novel and Reliable Framework of Patient Deterioration Prediction in Intensive Care Unit Based on Long Short-Term Memory-Recurrent Neural Network. IEEE Access, 2021, 9, 3894-3918.	4.2	10
3	A review of breast boundary and pectoral muscle segmentation methods in computer-aided detection/diagnosis of breast mammography. Artificial Intelligence Review, 2020, 53, 1873-1918.	15.7	34
4	Qualitative and Quantitative Performance Comparison of ECG Noise Reduction and Signal Enhancement Method based on Various Digital Filter Designs and Discrete Wavelet Transform. International Journal of Computing and Digital Systems, 2020, 9, 553-565.	0.7	2
5	Investigation of Attention Deficit/Hyperactivity Disorder Assessment Using Electro Interstitial Scan Based on Chronoamperometry Technique. IEEE Access, 2019, 7, 144679-144690.	4.2	4
6	Mini Home-Based Vital Sign Monitor with Android Mobile Application (myVitalGear)., 2018,,.		6
7	The Study and Comparison between Various Digital Filters for ECG De-noising. , 2018, , .		5
8	Android-based Mobile Application for Home-based Electrocardiogram Monitoring Device with Google Technology and Bluetooth Wireless Communication. , 2018, , .		6
9	Hardware transactional memory architecture with adaptive version management for multi-processor FPGA platforms. Journal of Systems Architecture, 2017, 73, 42-52.	4.3	3
10	Robust arrhythmia classifier using wavelet transform and support vector machine classification. , 2017, , .		0
11	Embedded system-on-chip design of atrial fibrillation classifier. , 2017, , .		5
12	EFFICIENT QRS COMPLEX DETECTION ALGORITHM IMPLEMENTATION ON SOC-BASED EMBEDDED SYSTEM. Jurnal Teknologi (Sciences and Engineering), 2016, 78, .	0.4	2
13	SOC-BASED BIOMEDICAL EMBEDDED SYSTEM DESIGN OF ARRHYTHMIA DETECTOR. Jurnal Teknologi (Sciences and Engineering), 2016, 78, .	0.4	O
14	New Vertical Handover Method to Optimize Utilization of Wireless Local Area Network in High-Speed Environment. PLoS ONE, 2016, 11, e0165888.	2.5	10
15	Artificial intelligence classification methods of atrial fibrillation with implementation technology. Computer Assisted Surgery, 2016, 21, 154-161.	1.3	14
16	PLATFORM INDEPENDENT WEB-BASED TELECARDIOLOGY FOR CONNECTED HEART CARE. Jurnal Teknologi (Sciences and Engineering), 2016, 78, .	0.4	0
17	AUTONOMOUS NETWORK SELECTION STRATEGY FOR TELECARDIOLOGY APPLICATION IN HETEROGENEOUS WIRELESS NETWORKS. Jurnal Teknologi (Sciences and Engineering), 2015, 77, .	0.4	1
18	Hardware/software partitioning of embedded System-on-Chip applications. , 2015, , .		9

#	Article	IF	Citations
19	Adaptive Configurable Transactional Memory for Multi-processor FPGA Platforms. , 2015, , .		1
20	USER-CENTRIC BASED VERTICAL HANDOVER DECISION ALGORITHM FOR TELECARDIOLOGY APPLICATION IN HETEROGENEOUS NETWORKS. Jurnal Teknologi (Sciences and Engineering), 2015, 77, .	0.4	6
21	On-board touch screen graphical interface design for SoC-based arrhythmia detector. , 2014, , .		O
22	Hardware transactional memory on multi-processor FPGA platform. , 2014, , .		1
23	Packet logging mechanism for adaptive online fault detection on Network-on-Chip. , $2014, $ , .		2
24	Development of platform-independent web-based telecardiology application for pilot case study. , 2014, , .		0
25	FPGA design and implementation of Electrocardiogram biomedical embedded system., 2014,,.		8
26	Remote dynamically reconfigurable platform using NetFPGA. , 2014, , .		0
27	Comparative study of electrocardiogram QRS complex detection algorithm on Field Programmable Gate Array platform. , 2014, , .		6
28	SoC-based design of arrhythmia detector. , 2014, , .		2
29	PVDF sensor design and FPGA implementation of ultrasound power measurement. , 2014, , .		0
30	Network partitioning and GA heuristic crossover for NoC application mapping. , 2013, , .		4
31	Feasible transition path generation for EFSM-based system testing. , 2013, , .		4
32	A Network-on-Chip simulation framework for homogeneous Multi-Processor System-on-Chip. , 2011, , .		0
33	A tightly coupled finite field arithmetic hardware in an FPGA-based embedded processor core for elliptic curve cryptography. International Journal of Information and Communication Technology, 2009, 2, 60.	0.1	2