

Andreas G G Andreou

List of Publications by Year in descending order

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189
papers

3,652
citations

186265

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189892

50
g-index

191
all docs

191
docs citations

191
times ranked

2639
citing authors

#	ARTICLE	IF	CITATIONS
1	Trade-Offs in Sensor Systems Design: A Tutorial. IEEE Sensors Journal, 2022, 22, 10040-10061.	4.7	1
2	Real Number Modeling of a SAR ADC behavior using SystemVerilog. , 2022, , .		1
3	Parallel Computation of Event-Based Visual Features Using Relational Graphs. , 2021, , .		0
4	Efficient, event-driven feature extraction and unsupervised object tracking for embedded applications. , 2021, , .		1
5	A Spike-based Cellular-Neural Network Architecture for Spatiotemporal filtering. , 2021, , .		2
6	Architecture and Algorithm Co-Design Framework for Embedded Processors in Event-Based Cameras. , 2021, , .		2
7	7 TOPS/W Cellular Neural Network Processor Core for Intelligent Internet-of-Things. IEEE Transactions on Circuits and Systems II: Express Briefs, 2020, 67, 1324-1328.	3.0	4
8	High-Speed, Real-Time, Spike-Based Object Tracking and Path Prediction on Google Edge TPU. , 2020, , .		16
9	An FPGA multiprocessor architecture for Bayesian online change point detection using stochastic computation. Microprocessors and Microsystems, 2020, 74, 102968.	2.8	2
10	Sparse Coding Using the Locally Competitive Algorithm on the TrueNorth Neurosynaptic System. Frontiers in Neuroscience, 2019, 13, 754.	2.8	9
11	Parallelism Analysis for a Multi-core Speech Recognition Architecture. , 2019, , .		0
12	A Mixed-Signal Successive Approximation Architecture for Energy-Efficient Fixed-Point Arithmetic in 16nm FinFET. , 2019, , .		1
13	A Historical Perspective on Hardware AI Inference, Charge-Based Computational Circuits and an 8 bit Charge-Based Multiply-Add Core in 16 nm FinFET CMOS. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2019, 9, 532-543.	3.6	11
14	Socio-Emotional Robot with Distributed Multi-Platform Neuromorphic Processing : (Invited) Tj ETQq0 0 0 rgBT /Overlock 10 Tf 50 222 T		3
15	Multilevel Storage Cell Characterization and Behavior Modeling of a Crossbar Computational Array in ESF3 Flash Technology : (Invited Paper). , 2019, , .		0
16	Characterization of a pseudo-DRAM Crossbar Computational Memory Array in 55nm CMOS : (Invited) Tj ETQq0 0 0 rgBT /Overlock 10 Tf		2
17	Graphical Model Transformation Analysis for Cognitive Computing and Machine Learning on the SpiNNaker Chip Multiprocessor. , 2019, , .		0
18	The Conical-Fishbone Clock Tree: A Clock-Distribution Network for a Heterogeneous Chip Multiprocessor AI Chiplet. , 2019, , .		0

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19	Bio-Inspired Human Action Recognition With a Micro-Doppler Sonar System. IEEE Access, 2018, 6, 28388-28403.	4.2	9
20	Energy aware simplicial processor for embedded morphological visual processing in intelligent internet of things. Electronics Letters, 2018, 54, 420-422.	1.0	17
21	Neuromorphic Cellular Neural Network Processor for Intelligent Internet-of-Things. , 2018, , .		4
22	Word2vec Word Similarities on IBM's TrueNorth Neurosynaptic System. , 2018, , .		2
23	Implementation of the Neural Engineering Framework on the TrueNorth Neurosynaptic System. , 2018, , .		8
24	StethoVest: A simultaneous multichannel wearable system for cardiac acoustic mapping. , 2018, , .		6
25	A Charge-Based Architecture for Energy-Efficient Vector-Vector Multiplication in 65nm CMOS. , 2018, , .		7
26	A method for the computational modeling of the physics of heart murmurs. Journal of Computational Physics, 2017, 336, 546-568.	3.8	20
27	Audio-Visual beamforming with the Eigenmike microphone array an omni-camera and cognitive auditory features. , 2017, , .		0
28	Action recognition using micro-Doppler signatures and a recurrent neural network. , 2017, , .		13
29	Neuromorphic self-driving robot with retinomorphic vision and spike-based processing/closed-loop control. , 2017, , .		6
30	Characterization of RTN noise in the analog front-end of digital pixel imagers. , 2017, , .		1
31	Path planning on the TrueNorth neurosynaptic system. , 2017, , .		9
32	Spike propagation path planning on IBM TrueNorth neurosynaptic system. Electronics Letters, 2017, 53, 1023-1025.	1.0	0
33	Miniature atomic scalar magnetometer for space based on the rubidium isotope ⁸⁷ Rb. Journal of Geophysical Research: Space Physics, 2016, 121, 7870-7880.	2.4	46
34	Bio-inspired system architecture for energy efficient, BIGDATA computing with application to wide area motion imagery. , 2016, , .		6
35	Neuromorphic sampling on the SpiNNaker and parallella chip multiprocessors. , 2016, , .		4
36	Real-time sensory information processing using the TrueNorth Neurosynaptic System. , 2016, , .		12

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37	A true Random Number Generator using RTN noise and a sigma delta converter. , 2016, , .		8
38	A CMOS Neural Interface for a Multichannel Vestibular Prosthesis. IEEE Transactions on Biomedical Circuits and Systems, 2016, 10, 269-279.	4.0	25
39	Design of a vanishing point algorithm for custom ASIC. , 2015, , .		1
40	FPGA emulation of a spike-based, stochastic system for real-time image dewarping. , 2015, , .		6
41	Mechanical design, instrumentation and measurements from a hemoacoustic cardiac phantom. , 2015, , .		4
42	Mapping the cardiac acoustome: An overview of technologies, tools and methods. , 2015, , .		3
43	Acoustic micro-Doppler signal processing with foveated electronic cochlea. Electronics Letters, 2015, 51, 132-134.	1.0	2
44	FPGA implementation of a Deep Belief Network architecture for character recognition using stochastic computation. , 2015, , .		33
45	The Johns Hopkins University multimodal dataset for human action recognition. Proceedings of SPIE, 2015, , .	0.8	2
46	Markov Chain Monte Carlo inference on graphical models using event-based processing on the SpiNNaker neuromorphic architecture. , 2015, , .		11
47	Do audio-visual motion cues promote segregation of auditory streams?. Frontiers in Neuroscience, 2014, 8, 64.	2.8	6
48	Fully functional fine-grain vertically integrated 3D focal plane neuromorphic processor. , 2014, , .		5
49	Visualizing and Quantifying Charge Distributions Correlated to Threshold Voltage Shifts in Lateral Organic Transistors. ACS Nano, 2014, 8, 2714-2724.	14.6	11
50	All digital programmable Gaussian pulse generator for ultra-wideband transmitter. , 2013, , .		0
51	Auditory modulation of visual proto-object formation in a hierarchical auditory-visual saliency map. , 2013, , .		0
52	Signal to symbol converters: Overview, opportunities and challenges. , 2013, , .		1
53	Organic diode implementations in configurable architectures and temperature sensors. , 2013, , .		0
54	Design of silicon brains in the nano-CMOS era: Spiking neurons, learning synapses and neural architecture optimization. Neural Networks, 2013, 45, 4-26.	5.9	95

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55	Neuromorphic Engineering: From Neural Systems to Brain-Like Engineered Systems. <i>Neural Networks</i> , 2013, 45, 1-3.	5.9	9
56	Person localization through ground vibrations using a sand-scorpion inspired spiking neural network. , 2013, , .		2
57	Representation of temporal coherence: CHAINS algorithm and FPGA implementation. , 2013, , .		0
58	8â€channel 20ÂkHz to 200ÂMHz Nyquist and compressive sampler in 0.5 Î¼m CMOS. <i>Electronics Letters</i> , 2013, 49, 23-25.	1.0	3
59	Design of configurable chipping sequence generator for highâ€speed parallel samplers. <i>Electronics Letters</i> , 2013, 49, 875-876.	1.0	0
60	Audio-visual saliency map: Overview, basic models and hardware implementation. , 2013, , .		10
61	MULTIMODAL INTEGRATION OF MICRO-DOPPLER SONAR AND AUDITORY SIGNALS FOR BEHAVIOR CLASSIFICATION WITH CONVOLUTIONAL NETWORKS. <i>International Journal of Neural Systems</i> , 2013, 23, 1350021.	5.2	9
62	The role of perceived source location in auditory stream segregation: Separation affects sound organization, common fate does not. <i>Learning & Perception</i> , 2013, 5, 55-72.	2.4	6
63	Design of a Parallel Sampling Encoder for Analog to Information (A2I) Converters: Theory, Architecture and CMOS Implementation. <i>Electronics (Switzerland)</i> , 2013, 2, 57-79.	3.1	4
64	Kelvin probe microscopic visualization of charge storage at polystyrene interfaces with pentacene and gold. <i>Applied Physics Letters</i> , 2012, 100, .	3.3	9
65	An FPGA-based approach for parameter estimation in spiking neural networks. , 2012, , .		2
66	Flexible readout and integration sensor (FRIS): a bio-inspired, system-on-chip, event-based readout architecture. <i>Proceedings of SPIE</i> , 2012, , .	0.8	7
67	Printed Organic Electronic Sensors. <i>Springer Series on Chemical Sensors and Biosensors</i> , 2012, , 191-216.	0.5	1
68	High-performance, event-driven, low-cost, and SWaP imaging sensor for hostile fire detection, homeland protection, and border security. <i>Proceedings of SPIE</i> , 2012, , .	0.8	1
69	Beyond Amdahl's Law: An Objective Function That Links Multiprocessor Performance Gains to Delay and Energy. <i>IEEE Transactions on Computers</i> , 2012, 61, 1110-1126.	3.4	51
70	A wireless architecture for distributed sensing/actuation and pre-processing with microsecond synchronization. , 2011, , .		3
71	A multimodal-corpus data collection system for cognitive acoustic scene analysis. , 2011, , .		7
72	A bio-inspired event-driven digital readout architecture with pixel-level A/D conversion and non-uniformity correction. , 2011, , .		7

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73	Design of a CMOS A2I data converter: Theory, architecture and implementation. , 2011, , .		9
74	Contactless fluorescence imaging with a CMOS image sensor. , 2011, , .		3
75	Design of a one million neuron single FPGA neuromorphic system for real-time multimodal scene analysis. , 2011, , .		51
76	Guest Editorialâ€”Special Issue on Selected Papers From BioCAS 2010. IEEE Transactions on Biomedical Circuits and Systems, 2011, 5, 401-402.	4.0	0
77	Threshold voltage shifting for memory and tuning in printed transistor circuits. Materials Science and Engineering Reports, 2011, 72, 49-80.	31.8	40
78	Gait-based person and gender recognition using micro-doppler signatures. , 2011, , .		19
79	Evaluating on-chip interconnects for low operating frequency silicon neuron arrays. , 2011, , .		3
80	A high-level analytical model for application specific CMP design exploration. , 2011, , .		10
81	A 3-pin 1V 115µW 176×144 autonomous active pixel image sensor in 0.18µm CMOS. , 2011, , .		2
82	A low-power 8-bit SAR ADC for a QCIF image sensor. , 2011, , .		9
83	A 32×32 single photon avalanche diode imager with delay-insensitive address-event readout. , 2011, , .		4
84	Silicon-on-insulator (SOI) integration for organic field effect transistor (OFET) based circuits. , 2011, , .		2
85	A combinational digital logic approach to STDP. , 2011, , .		26
86	Johns Hopkins on the chip: microsystems and cognitive machines for sustainable, affordable, personalised medicine and healthcare. Electronics Letters, 2011, 47, S34.	1.0	2
87	Human Action Categorization Using Ultrasound Micro-Doppler Signatures. Lecture Notes in Computer Science, 2011, , 18-28.	1.3	13
88	Airâ€”Operable, Highâ€”Mobility Organic Transistors with Semifluorinated Side Chains and Unsubstituted Naphthalenetetracarboxylic Diimide Cores: High Mobility and Environmental and Bias Stress Stability from the Perfluorooctylpropyl Side Chain. Advanced Functional Materials, 2010, 20, 2930-2944.	14.9	66
89	PWL cores for nonlinear array processing. , 2010, , .		1
90	Optimum bias of CMOS organic field effect transistor inverter through threshold adjustment of both p- and n-type devices. Electronics Letters, 2010, 46, 1335.	1.0	6

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91	A spike based 3D imager chip using a mixed mode encoding readout. , 2010, , .		3
92	A switched capacitor implementation of the generalized linear integrate-and-fire neuron. , 2009, , .		38
93	Noise analysis and comparison of analog and digital readout integrated circuits for infrared focal plane arrays. , 2009, , .		2
94	Analytical methods for the design and optimization of chip-multiprocessor architectures. , 2009, , .		10
95	On projections of Gaussian distributions using maximum likelihood criteria. , 2009, , .		4
96	Pentaceneâ€Zinc Oxide Vertical Diode with Compatible Grains and 15â€MHz Rectification. Advanced Materials, 2008, 20, 1023-1028.	21.0	96
97	Silicon-on-sapphire CMOS and opportunities in niche markets: Old wine in a new bottle. , 2008, , .		5
98	Dynamical digital silicon neurons. , 2008, , .		52
99	Neuromorphic interconnects using Ultra Wideband radio. , 2008, , .		4
100	Photo-battery fabricated in silicon on sapphire CMOS. Electronics Letters, 2008, 44, 766.	1.0	12
101	Erratum for â€Single photon avalanche photodetector with integrated quenching fabricated in TSMC 0.18â€[micro sign]m 1.8â€V CMOS processâ€™. Electronics Letters, 2008, 44, 1284.	1.0	1
102	Single photon avalanche photodetector with integrated quenching fabricated in TSMC 0.18â€[micro sign]m 1.8â€V CMOS process. Electronics Letters, 2008, 44, 643.	1.0	35
103	Experimental results of simplicial CNN digital pixel processor. Electronics Letters, 2008, 44, 27.	1.0	7
104	A low-power silicon-on-sapphire tunable ultra-wideband transmitter. , 2008, , .		9
105	Fabrication and Testing of Single Photon Avalanche Detectors in the TSMC 0.18¼m CMOS Technology. , 2007, , .		9
106	Localized closed-loop temperature control and regulation in hybrid silicon/silicone life science microsystems. , 2007, , .		0
107	Design and characterization of a gain-enhanced floating gate-body tied photodetector in Silicon on Sapphire CMOS. , 2007, , .		1
108	Acoustic Micro-Doppler Gait Signatures of Humans and Animals. , 2007, , .		19

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109	Address-data event representation for communication in multichip neuromorphic system architectures. Electronics Letters, 2007, 43, 767.	1.0	4
110	Enabling Technologies in Drug Delivery and Clinical Care. , 2007, , .		2
111	Distortion of Neural Signals by Spike Coding. Neural Computation, 2007, 19, 2797-2839.	2.2	4
112	Design of An Ultra Wideband Transmitter in 0.18µm 3D Silicon on Insulator CMOS. , 2007, , .		0
113	Address Data Event Representation (ADER) for Efficient Neuromorphic Communication. , 2007, , .		2
114	A high voltage PMOS transistor for quenching of geiger-mode avalanche photodiodes in deep submicron CMOS technologies. , 2007, , .		2
115	Architecture of a µRFID with integrated antenna in 3D SOI-CMOS. , 2007, , .		0
116	Design, Analysis and Implementation of Integrated Micro-Thermal Control Systems. , 2007, , .		0
117	A UV Photodetector with Internal Gain Fabricated in Silicon on Sapphire CMOS. , 2007, , .		13
118	FPGA Based Silicon Spiking Neural Array. , 2007, , .		52
119	Digital isolation amplifier in silicon-on-sapphire CMOS. Electronics Letters, 2007, 43, 451.	1.0	5
120	Design, Fabrication, and Testing of a Hybrid CMOS/PDMS Microsystem for Cell Culture and Incubation. IEEE Transactions on Biomedical Circuits and Systems, 2007, 1, 3-18.	4.0	72
121	A Self-Biased Operational Transconductance Amplifier in 0.18 micron 3D SOI-CMOS. , 2007, , .		2
122	Acoustic micro-Doppler radar for human gait imaging. Journal of the Acoustical Society of America, 2007, 121, EL110-EL113.	1.1	40
123	Capacitive Inter-Chip Data and Power Transfer for 3-D VLSI. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2006, 53, 1348-1352.	2.2	83
124	Integrated PDMS/CMOS Microsystem for Autonomous Incubation and Imaging in Cell Culture Studies. , 2006, , .		1
125	An 8-bit 800-µs Successive Approximation ADC in SOI CMOS. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2006, 53, 858-861.	2.2	28
126	A monolithic isolation amplifier in silicon-on-insulator CMOS: Testing and applications. Analog Integrated Circuits and Signal Processing, 2006, 49, 63-70.	1.4	2

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127	CMOS image sensors for sensor networks. Analog Integrated Circuits and Signal Processing, 2006, 49, 39-51.	1.4	38
128	Hybrid Silicon/Silicone (polydimethylsiloxane) Microsystem for Cell Culture. , 2006, 2006, 2490-3.		2
129	VLSI implementation of an energy-aware wake-up detector for an acoustic surveillance sensor network. ACM Transactions on Sensor Networks, 2006, 2, 594-611.	3.6	13
130	High-speed, address-encoding arbiter architecture. Electronics Letters, 2006, 42, 170.	1.0	9
131	Special Issue on Advances in Life Science Systems and Applications: Guest Editorial. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2006, 53, 2345-2348.	0.1	0
132	Experimental results for cascable micropower time delay estimator. Electronics Letters, 2006, 42, 1218.	1.0	6
133	A low-power correlation-derivative CMOS VLSI circuit for bearing estimation. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2006, 14, 207-212.	3.1	12
134	Hybrid Silicon/Silicone (polydimethylsiloxane) Microsystem for Cell Culture. Annual International Conference of the IEEE Engineering in Medicine and Biology Society, 2006, , .	0.5	0
135	<title>A miniature low-power intelligent sensor node for persistent acoustic surveillance</title>. , 2005, , .		11
136	Monolithic digital galvanic isolation buffer fabricated in silicon on sapphire CMOS. Electronics Letters, 2005, 41, 526.	1.0	13
137	Isolation charge pump fabricated in silicon on sapphire CMOS technology. Electronics Letters, 2005, 41, 590.	1.0	8
138	A low-power silicon on sapphire CMOS optoelectronic receiver using low- and high-threshold devices. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2005, 52, 253-261.	0.1	5
139	Spike communication of dynamic stimuli: rate decoding versus temporal decoding. Neurocomputing, 2004, 58-60, 101-107.	5.9	4
140	16Å—16 pixel silicon on sapphire CMOS digital pixel photosensor array. Electronics Letters, 2004, 40, 66.	1.0	12
141	A Comparative Study of Sound Localization Algorithms for Energy Aware Sensor Network Nodes. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2004, 51, 640-648.	0.1	47
142	A Scalable and Programmable Simplicial CNN Digital Pixel Processor Architecture. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2004, 51, 988-996.	0.1	22
143	A 2.5-mW SOS CMOS Optical Receiver for Chip-to-Chip Interconnect. Journal of Lightwave Technology, 2004, 22, 2149-2157.	4.6	13
144	An SOS MEMS interferometer. , 2004, 5346, 27.		4

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145	Energy efficiency in a channel model for the spiking axon. <i>Neurocomputing</i> , 2003, 52-54, 39-44.	5.9	15
146	Multichannel ultrathin silicon-on-sapphire optical interconnects. <i>IEEE Journal of Selected Topics in Quantum Electronics</i> , 2003, 9, 380-386.	2.9	19
147	Guest editorial - Special issue on neural networks hardware implementations. <i>IEEE Transactions on Neural Networks</i> , 2003, 14, 976-979.	4.2	17
148	A comparative study of access topologies for chip-level address-event communication channels. <i>IEEE Transactions on Neural Networks</i> , 2003, 14, 1266-1277.	4.2	26
149	Polarization imaging: principles and integrated polarimeters. <i>IEEE Sensors Journal</i> , 2002, 2, 566-576.	4.7	171
150	<title>Advanced on-FPA signal processing for staring IRFPAs</title>. , 2001, 4435, 247.		1
151	A communication channel model for information transmission in the blowfly photoreceptor. <i>BioSystems</i> , 2001, 62, 113-133.	2.0	14
152	Probabilistic synaptic weighting in a reconfigurable network of VLSI integrate-and-fire neurons. <i>Neural Networks</i> , 2001, 14, 781-793.	5.9	166
153	FGMOS dosimetry: design and implementation. <i>IEEE Transactions on Nuclear Science</i> , 2001, 48, 2050-2055.	2.0	41
154	Relating information capacity to a biophysical model for blowfly photoreceptors. <i>Neurocomputing</i> , 2000, 32-33, 9-16.	5.9	7
155	Integrated imaging linear polarimeter. <i>ISA Transactions</i> , 1999, 38, 203-209.	5.7	2
156	Bipolar/CMOS current-source flip-flop for application in neuro-fuzzy systems. <i>Electronics Letters</i> , 1999, 35, 1326.	1.0	2
157	Heteroscedastic discriminant analysis and reduced rank HMMs for improved speech recognition. <i>Speech Communication</i> , 1998, 26, 283-297.	2.8	259
158	Winner-Takes-All Associative Memory: A Hamming Distance Vector Quantizer. , 1998, , 437-456.		1
159	Asynchronous Communication of 2D Motion Information Using Winner-Takes-All Arbitration. , 1998, , 217-227.		2
160	A model for visible photon emission from reverse-biased silicon p-n junctions. <i>Applied Physics Letters</i> , 1997, 70, 470-471.	3.3	38
161	1D polarisation contrast retina. <i>Electronics Letters</i> , 1997, 33, 38.	1.0	14
162	Liquid crystal polarization camera. <i>IEEE Transactions on Automation Science and Engineering</i> , 1997, 13, 195-203.	2.3	78

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163	On fault probabilities and yield models for VLSI neural networks. IEEE Journal of Solid-State Circuits, 1997, 32, 1284-1287.	5.4	2
164	Winner-Takes-All Associative Memory: A Hamming Distance Vector Quantizer. Analog Integrated Circuits and Signal Processing, 1997, 13, 211-222.	1.4	14
165	Asynchronous Communication of 2D Motion Information Using Winner-Takes-All Arbitration. Analog Integrated Circuits and Signal Processing, 1997, 13, 103-109.	1.4	14
166	Translinear circuits in subthreshold MOS. Analog Integrated Circuits and Signal Processing, 1996, 9, 141-166.	1.4	171
167	Polarization camera sensors. Image and Vision Computing, 1995, 13, 497-510.	4.5	95
168	Linearised differential transconductors in subthreshold CMOS. Electronics Letters, 1995, 31, 545-547.	1.0	64
169	Characterization of subthreshold MOS mismatch in transistors for VLSI systems. Analog Integrated Circuits and Signal Processing, 1994, 6, 75-85.	1.4	56
170	Characterization of subthreshold MOS mismatch in transistors for VLSI systems. Journal of Signal Processing Systems, 1994, 8, 75-85.	1.0	39
171	A multiple input differential amplifier based on charge sharing on a floating-gate MOSFET. Analog Integrated Circuits and Signal Processing, 1994, 6, 197-208.	1.4	31
172	A state assignment approach to asynchronous CMOS circuit design. IEEE Transactions on Computers, 1994, 43, 460-469.	3.4	8
173	Voiced-speech representation by an analog silicon model of the auditory periphery. IEEE Transactions on Neural Networks, 1992, 3, 477-487.	4.2	71
174	Current-mode subthreshold MOS implementation of the Herault-Jutten autoadaptive network. IEEE Journal of Solid-State Circuits, 1992, 27, 714-727.	5.4	44
175	<title>Modeling inner and outer plexiform retinal processing using nonlinear coupled resistive networks</title>. , 1991, 1453, 270.		7
176	Current-mode subthreshold MOS circuits for analog VLSI neural systems. IEEE Transactions on Neural Networks, 1991, 2, 205-213.	4.2	277
177	Electronic arts imitate life. Nature, 1991, 354, 501-501.	27.8	4
178	Synthetic Neural Circuits Using Current-Domain Signal Representations. Neural Computation, 1989, 1, 489-501.	2.2	21
179	A heteroassociative memory using current-mode MOS analog VLSI circuits. IEEE Transactions on Circuits and Systems, 1989, 36, 747-755.	0.9	45
180	In-Situ Characterization of Carrier Mobility in Field Effect Transistors. , 1989, , 1247-1254.		2

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181	Acetylcholine Receptor-Based Biosensor. Analytical Letters, 1988, 21, 1665-1680.	1.8	61
182	In-situ mobility profiling of GaAs MESFET's using the hall current technique. IEEE Transactions on Electron Devices, 1987, 34, 1441-1447.	3.0	2
183	Analytical calculation of the quantum-mechanical transmission coefficient for a triangular, planar-doped potential barrier. Solid-State Electronics, 1985, 28, 821-822.	1.4	19
184	Hybrid Sensor Network And Fusion Algorithm For Sound Source Localization. , 0, , .		7
185	Event-Based Imaging with Active Illumination in Sensor Networks. , 0, , .		19
186	Field Test Results for Low Power Bearing Estimator Sensor Nodes. , 0, , .		3
187	Capacitive Coupling of Data and Power for 3D Silicon-on-Insulator VLSI. , 0, , .		19
188	A Monolithic Isolation Amplifier in Silicon-on-Insulator CMOS. , 0, , .		1
189	A semi-supervised version of heteroscedastic linear discriminant analysis. , 0, , .		1