Andreas G G Andreou

List of Publications by Year
in descending order

189 papers	**3,652** citations	186265 **28** h-index	189892 **50** g-index
191 all docs	**191** docs citations	**191** times ranked	**2639** citing authors

#	Article	IF	Citations
1	Current-mode subthreshold MOS circuits for analog VLSI neural systems. IEEE Transactions on Neural Networks, 1991, 2, 205-213.	4.2	277
2	Heteroscedastic discriminant analysis and reduced rank HMMs for improved speech recognition. Speech Communication, 1998, 26, 283-297.	2.8	259
3	Translinear circuits in subthreshold MOS. Analog Integrated Circuits and Signal Processing, 1996, 9, 141-166.	1.4	171
4	Polarization imaging: principles and integrated polarimeters. IEEE Sensors Journal, 2002, 2, 566-576.	4.7	171
5	Probabilistic synaptic weighting in a reconfigurable network of VLSI integrate-and-fire neurons. Neural Networks, 2001, 14, 781-793.	5.9	166
6	Pentacene–Zinc Oxide Vertical Diode with Compatible Grains and 15 MHz Rectification. Advanced Materials, 2008, 20, 1023-1028.	21.0	96
7	Polarization camera sensors. Image and Vision Computing, 1995, 13, 497-510.	4.5	95
8	Design of silicon brains in the nano-CMOS era: Spiking neurons, learning synapses and neural architecture optimization. Neural Networks, 2013, 45, 4-26.	5.9	95
9	Capacitive Inter-Chip Data and Power Transfer for 3-D VLSI. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2006, 53, 1348-1352.	2.2	83
10	Liquid crystal polarization camera. IEEE Transactions on Automation Science and Engineering, 1997, 13, 195-203.	2.3	78
11	Design, Fabrication, and Testing of a Hybrid CMOS/PDMS Microsystem for Cell Culture and Incubation. IEEE Transactions on Biomedical Circuits and Systems, 2007, 1, 3-18.	4.0	72
12	Voiced-speech representation by an analog silicon model of the auditory periphery. IEEE Transactions on Neural Networks, 1992, 3, 477-487.	4.2	71
13	Air–Operable, High–Mobility Organic Transistors with Semifluorinated Side Chains and Unsubstituted Naphthalenetetracarboxylic Diimide Cores: High Mobility and Environmental and Bias Stress Stability from the Perfluorooctylpropyl Side Chain. Advanced Functional Materials, 2010, 20, 2930-2944.	14.9	66
14	Linearised differential transconductors in subthreshold CMOS. Electronics Letters, 1995, 31, 545-547.	1.0	64
15	Acetylcholine Receptor-Based Biosensor. Analytical Letters, 1988, 21, 1665-1680.	1.8	61
16	Characterization of subthreshold MOS mismatch in transistors for VLSI systems. Analog Integrated Circuits and Signal Processing, 1994, 6, 75-85.	1.4	56
17	FPGA Based Silicon Spiking Neural Array. , 2007, , .		52
18	Dynamical digital silicon neurons. , 2008, , .		52

#	ARTICLE	IF	CITATIONS
19	Design of a one million neuron single FPGA neuromorphic system for real-time multimodal scene analysis. , 2011, , .		51
20	Beyond Amdahl's Law: An Objective Function That Links Multiprocessor Performance Gains to Delay and Energy. IEEE Transactions on Computers, 2012, 61, 1110-1126.	3.4	51
21	A Comparative Study of Sound Localization Algorithms for Energy Aware Sensor Network Nodes. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2004, 51, 640-648.	0.1	47
22	Miniature atomic scalar magnetometer for space based on the rubidium isotope ^{87}Rb. Journal of Geophysical Research: Space Physics, 2016, 121, 7870-7880.	2.4	46
23	A heteroassociative memory using current-mode MOS analog VLSI circuits. IEEE Transactions on Circuits and Systems, 1989, 36, 747-755.	0.9	45
24	Current-mode subthreshold MOS implementation of the Herault-Jutten autoadaptive network. IEEE Journal of Solid-State Circuits, 1992, 27, 714-727.	5.4	44
25	FGMOS dosimetry: design and implementation. IEEE Transactions on Nuclear Science, 2001, 48, 2050-2055.	2.0	41
26	Acoustic micro-Doppler radar for human gait imaging. Journal of the Acoustical Society of America, 2007, 121, EL110-EL113.	1.1	40
27	Threshold voltage shifting for memory and tuning in printed transistor circuits. Materials Science and Engineering Reports, 2011, 72, 49-80.	31.8	40
28	Characterization of subthreshold MOS mismatch in transistors for VLSI systems. Journal of Signal Processing Systems, 1994, 8, 75-85.	1.0	39
29	A model for visible photon emission from reverse-biased silicon p-n junctions. Applied Physics Letters, 1997, 70, 470-471.	3.3	38
30	CMOS image sensors for sensor networks. Analog Integrated Circuits and Signal Processing, 2006, 49, 39-51.	1.4	38
31	A switched capacitor implementation of the generalized linear integrate-and-fire neuron. , 2009, , .		38
32	Single photon avalanche photodetector with integrated quenching fabricated in TSMC 0.18â€¦[micro sign]m 1.8â€¦V CMOS process. Electronics Letters, 2008, 44, 643.	1.0	35
33	FPGA implementation of a Deep Belief Network architecture for character recognition using stochastic computation. , 2015, , .		33
34	A multiple input differential amplifier based on charge sharing on a floating-gate MOSFET. Analog Integrated Circuits and Signal Processing, 1994, 6, 197-208.	1.4	31
35	An 8-bit 800-$\mu\mbox{W}$ 1.23-MS/s Successive Approximation ADC in SOI CMOS. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2006, 53, 858-861.	2.2	28
36	A comparative study of access topologies for chip-level address-event communication channels. IEEE Transactions on Neural Networks, 2003, 14, 1266-1277.	4.2	26

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37	A combinational digital logic approach to STDP. , 2011, , .		26
38	A CMOS Neural Interface for a Multichannel Vestibular Prosthesis. IEEE Transactions on Biomedical Circuits and Systems, 2016, 10, 269-279.	4.0	25
39	A Scalable and Programmable Simplicial CNN Digital Pixel Processor Architecture. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2004, 51, 988-996.	0.1	22
40	Synthetic Neural Circuits Using Current-Domain Signal Representations. Neural Computation, 1989, 1, 489-501.	2.2	21
41	A method for the computational modeling of the physics of heart murmurs. Journal of Computational Physics, 2017, 336, 546-568.	3.8	20
42	Analytical calculation of the quantum-mechanical transmission coefficient for a triangular, planar-doped potential barrier. Solid-State Electronics, 1985, 28, 821-822.	1.4	19
43	Multichannel ultrathin silicon-on-sapphire optical interconnects. IEEE Journal of Selected Topics in Quantum Electronics, 2003, 9, 380-386.	2.9	19
44	Event-Based Imaging with Active Illumination in Sensor Networks. , 0, , .		19
45	Capacitive Coupling of Data and Power for 3D Silicon-on-Insulator VLSI. , 0, , .		19
46	Acoustic Micro-Doppler Gait Signatures of Humans and Animals. , 2007, , .		19
47	Gait-based person and gender recognition using micro-doppler signatures. , 2011, , .		19
48	Guest editorial - Special issue on neural networks hardware implementations. IEEE Transactions on Neural Networks, 2003, 14, 976-979.	4.2	17
49	Energy aware simplicial processor for embedded morphological visual processing in intelligent internet of things. Electronics Letters, 2018, 54, 420-422.	1.0	17
50	High-Speed, Real-Time, Spike-Based Object Tracking and Path Prediction on Google Edge TPU. , 2020, , .		16
51	Energy efficiency in a channel model for the spiking axon. Neurocomputing, 2003, 52-54, 39-44.	5.9	15
52	1D polarisation contrast retina. Electronics Letters, 1997, 33, 38.	1.0	14
53	Winner-Takes-All Associative Memory: A Hamming Distance Vector Quantizer. Analog Integrated Circuits and Signal Processing, 1997, 13, 211-222.	1.4	14
54	Asynchronous Communication of 2D Motion Information Using Winner-Takes-All Arbitration. Analog Integrated Circuits and Signal Processing, 1997, 13, 103-109.	1.4	14

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55	A communication channel model for information transmission in the blowfly photoreceptor. BioSystems, 2001, 62, 113-133.	2.0	14
56	A 2.5-mW SOS CMOS Optical Receiver for Chip-to-Chip Interconnect. Journal of Lightwave Technology, 2004, 22, 2149-2157.	4.6	13
57	Monolithic digital galvanic isolation buffer fabricated in silicon on sapphire CMOS. Electronics Letters, 2005, 41, 526.	1.0	13
58	VLSI implementation of an energy-aware wake-up detector for an acoustic surveillance sensor network. ACM Transactions on Sensor Networks, 2006, 2, 594-611.	3.6	13
59	A UV Photodetector with Internal Gain Fabricated in Silicon on Sapphire CMOS. , 2007, , .		13
60	Action recognition using micro-Doppler signatures and a recurrent neural network. , 2017, , .		13
61	Human Action Categorization Using Ultrasound Micro-Doppler Signatures. Lecture Notes in Computer Science, 2011, , 18-28.	1.3	13
62	16Ã—16 pixel silicon on sapphire CMOS digital pixel photosensor array. Electronics Letters, 2004, 40, 66.	1.0	12
63	A low-power correlation-derivative CMOS VLSI circuit for bearing estimation. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2006, 14, 207-212.	3.1	12
64	Photo-battery fabricated in silicon on sapphire CMOS. Electronics Letters, 2008, 44, 766.	1.0	12
65	Real-time sensory information processing using the TrueNorth Neurosynaptic System. , 2016, , .		12
66	<title>A miniature low-power intelligent sensor node for persistent acoustic surveillance</title>. , 2005, , .		11
67	Visualizing and Quantifying Charge Distributions Correlated to Threshold Voltage Shifts in Lateral Organic Transistors. ACS Nano, 2014, 8, 2714-2724.	14.6	11
68	Markov Chain Monte Carlo inference on graphical models using event-based processing on the SpiNNaker neuromorphic architecture. , 2015, , .		11
69	A Historical Perspective on Hardware AI Inference, Charge-Based Computational Circuits and an 8 bit Charge-Based Multiply-Add Core in 16 nm FinFET CMOS. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2019, 9, 532-543.	3.6	11
70	Analytical methods for the design and optimization of chip-multiprocessor architectures. , 2009, , .		10
71	A high-level analytical model for application specific CMP design exploration. , 2011, , .		10
72	Audio-visual saliency map: Overview, basic models and hardware implementation. , 2013, , .		10

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73	High-speed, address-encoding arbiter architecture. Electronics Letters, 2006, 42, 170.	1.0	9
74	Fabrication and Testing of Single Photon Avalanche Detectors in the TSMC 0.18&#x003BC;m CMOS Technology. , 2007, , .		9
75	A low-power silicon-on-sapphire tunable ultra-wideband transmitter. , 2008, , .		9
76	Design of a CMOS A2I data converter: Theory, architecture and implementation. , 2011, , .		9
77	A low-power 8-bit SAR ADC for a QCIF image sensor. , 2011, , .		9
78	Kelvin probe microscopic visualization of charge storage at polystyrene interfaces with pentacene and gold. Applied Physics Letters, 2012, 100, .	3.3	9
79	Neuromorphic Engineering: From Neural Systems to Brain-Like Engineered Systems. Neural Networks, 2013, 45, 1-3.	5.9	9
80	MULTIMODAL INTEGRATION OF MICRO-DOPPLER SONAR AND AUDITORY SIGNALS FOR BEHAVIOR CLASSIFICATION WITH CONVOLUTIONAL NETWORKS. International Journal of Neural Systems, 2013, 23, 1350021.	5.2	9
81	Path planning on the TrueNorth neurosynaptic system. , 2017, , .		9
82	Bio-Inspired Human Action Recognition With a Micro-Doppler Sonar System. IEEE Access, 2018, 6, 28388-28403.	4.2	9
83	Sparse Coding Using the Locally Competitive Algorithm on the TrueNorth Neurosynaptic System. Frontiers in Neuroscience, 2019, 13, 754.	2.8	9
84	A state assignment approach to asynchronous CMOS circuit design. IEEE Transactions on Computers, 1994, 43, 460-469.	3.4	8
85	Isolation charge pump fabricated in silicon on sapphire CMOS technology. Electronics Letters, 2005, 41, 590.	1.0	8
86	A true Random Number Generator using RTN noise and a sigma delta converter. , 2016, , .		8
87	Implementation of the Neural Engineering Framework on the TrueNorth Neurosynaptic System. , 2018, , .		8
88	<title>Modeling inner and outer plexiform retinal processing using nonlinear coupled resistive networks</title>. , 1991, 1453, 270.		7
89	Relating information capacity to a biophysical model for blowfly photoreceptors. Neurocomputing, 2000, 32-33, 9-16.	5.9	7
90	Hybrid Sensor Network And Fusion Algorithm For Sound Source Localization. , 0, , .		7

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91	Experimental results of simplicial CNN digital pixel processor. Electronics Letters, 2008, 44, 27.	1.0	7
92	A multimodal-corpus data collection system for cognitive acoustic scene analysis. , 2011, , .		7
93	A bio-inspired event-driven digital readout architecture with pixel-level A/D conversion and non-uniformity correction. , 2011, , .		7
94	Flexible readout and integration sensor (FRIS): a bio-inspired, system-on-chip, event-based readout architecture. Proceedings of SPIE, 2012, , .	0.8	7
95	A Charge-Based Architecture for Energy-Efficient Vector-Vector Multiplication in 65nm CMOS. , 2018, , .		7
96	Experimental results for cascadable micropower time delay estimator. Electronics Letters, 2006, 42, 1218.	1.0	6
97	Optimum bias of CMOS organic field effect transistor inverter through threshold adjustment of both p- and n-type devices. Electronics Letters, 2010, 46, 1335.	1.0	6
98	The role of perceived source location in auditory stream segregation: Separation affects sound organization, common fate does not. Learning & Perception, 2013, 5, 55-72.	2.4	6
99	Do audio-visual motion cues promote segregation of auditory streams?. Frontiers in Neuroscience, 2014, 8, 64.	2.8	6
100	FPGA emulation of a spike-based, stochastic system for real-time image dewarping. , 2015, , .		6
101	Bio-inspired system architecture for energy efficient, BIGDATA computing with application to wide area motion imagery. , 2016, , .		6
102	Neuromorphic self-driving robot with retinomorphic vision and spike-based processing/closed-loop control. , 2017, , .		6
103	StethoVest: A simultaneous multichannel wearable system for cardiac acoustic mapping. , 2018, , .		6
104	A low-power silicon on sapphire CMOS optoelectronic receiver using low- and high-threshold devices. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2005, 52, 253-261.	0.1	5
105	Digital isolation amplifier in silicon-on-sapphire CMOS. Electronics Letters, 2007, 43, 451.	1.0	5
106	Silicon-on-sapphire CMOS and opportunities in niche markets: Old wine in a new bottle. , 2008, , .		5
107	Fully functional fine-grain vertically integrated 3D focal plane neuromorphic processor. , 2014, , .		5
108	Electronic arts imitate life. Nature, 1991, 354, 501-501.	27.8	4

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109	Spike communication of dynamic stimuli: rate decoding versus temporal decoding. Neurocomputing, 2004, 58-60, 101-107.	5.9	4
110	An SOS MEMS interferometer. , 2004, 5346, 27.		4
111	Address-data event representation for communication in multichip neuromorphic system architectures. Electronics Letters, 2007, 43, 767.	1.0	4
112	Distortion of Neural Signals by Spike Coding. Neural Computation, 2007, 19, 2797-2839.	2.2	4
113	Neuromorphic interconnects using Ultra Wideband radio. , 2008, , .		4
114	On projections of Gaussian distributions using maximum likelihood criteria. , 2009, , .		4
115	A 32&#x00D7;32 single photon avalanche diode imager with delay-insensitive address-event readout. , 2011, , .		4
116	Design of a Parallel Sampling Encoder for Analog to Information (A2I) Converters: Theory, Architecture and CMOS Implementation. Electronics (Switzerland), 2013, 2, 57-79.	3.1	4
117	Mechanical design, instrumentation and measurements from a hemoacoustic cardiac phantom. , 2015, , .		4
118	Neuromorphic sampling on the SpiNNaker and parallella chip multiprocessors. , 2016, , .		4
119	Neuromorphic Cellular Neural Network Processor for Intelligent Internet-of-Things. , 2018, , .		4
120	7 TOPS/W Cellular Neural Network Processor Core for Intelligent Internet-of-Things. IEEE Transactions on Circuits and Systems II: Express Briefs, 2020, 67, 1324-1328.	3.0	4
121	Field Test Results for Low Power Bearing Estimator Sensor Nodes. , 0, , .		3
122	A spike based 3D imager chip using a mixed mode encoding readout. , 2010, , .		3
123	A wireless architecture for distributed sensing/actuation and pre-processing with microsecond synchronization. , 2011, , .		3
124	Contactless fluorescence imaging with a CMOS image sensor. , 2011, , .		3
125	Evaluating on-chip interconnects for low operating frequency silicon neuron arrays. , 2011, , .		3
126	8â€channel 20ÂkHz to 200ÂMHz Nyquist and compressive sampler in 0.5 Î¼m CMOS. Electronics Letters, 2013, 49, 23-25.	1.0	3

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127	Mapping the cardiac acousteome: An overview of technologies, tools and methods. , 2015, , .		3
128	Socio-Emotional Robot with Distributed Multi-Platform Neuromorphic Processing : (Invited) Tj ETQq0 0 0 rgBT /Overlock 10 Tf 50 702 To		3
129	In-situ mobility profiling of GaAs MESFET's using the hall current technique. IEEE Transactions on Electron Devices, 1987, 34, 1441-1447.	3.0	2
130	On fault probabilities and yield models for VLSI neural networks. IEEE Journal of Solid-State Circuits, 1997, 32, 1284-1287.	5.4	2
131	Integrated imaging linear polarimeter. ISA Transactions, 1999, 38, 203-209.	5.7	2
132	Bipolar/CMOS current-source flip-flop for application in neuro-fuzzy systems. Electronics Letters, 1999, 35, 1326.	1.0	2
133	A monolithic isolation amplifier in silicon-on-insulator CMOS: Testing and applications. Analog Integrated Circuits and Signal Processing, 2006, 49, 63-70.	1.4	2
134	Hybrid Silicon/Silicone (polydimethylsiloxane) Microsystem for Cell Culture. , 2006, 2006, 2490-3.		2
135	Enabling Technologies in Drug Delivery and Clinical Care. , 2007, , .		2
136	Address Data Event Representation (ADER) for Efficient Neuromorphic Communication. , 2007, , .		2
137	A high voltage PMOS transistor for quenching of geiger-mode avalanche photodiodes in deep submicron CMOS technologies. , 2007, , .		2
138	A Self-Biased Operational Transconductance Amplifier in 0.18 micron 3D SOI-CMOS. , 2007, , .		2
139	Noise analysis and comparison of analog and digital readout integrated circuits for infrared focal plane arrays. , 2009, , .		2
140	A 3-pin 1V 115µW 176×144 autonomous active pixel image sensor in 0.18µm CMOS. , 2011, , .		2
141	Silicon-on-insulator (SOI) integration for organic field effect transistor (OFET) based circuits. , 2011, , .		2
142	Johns Hopkins on the chip: microsystems and cognitive machines for sustainable, affordable, personalised medicine and healthcare. Electronics Letters, 2011, 47, S34.	1.0	2
143	An FPGA-based approach for parameter estimation in spiking neural networks. , 2012, , .		2
144	Person localization through ground vibrations using a sand-scorpion inspired spiking neural network. , 2013, , .		2

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145	Acoustic micro€Doppler signal processing with foveated electronic cochlea. Electronics Letters, 2015, 51, 132-134.	1.0	2
146	The Johns Hopkins University multimodal dataset for human action recognition. Proceedings of SPIE, 2015, , .	0.8	2
147	Word2vec Word Similarities on IBM's TrueNorth Neurosynaptic System. , 2018, , .		2
148	Characterization of a pseudo-DRAM Crossbar Computational Memory Array in 55nm CMOS : (Invited) Tj ETQq0 0 0 rgBT /Overlock 10 Tf		2
149	An FPGA multiprocessor architecture for Bayesian online change point detection using stochastic computation. Microprocessors and Microsystems, 2020, 74, 102968.	2.8	2
150	A Spike-based Cellular-Neural Network Architecture for Spatiotemporal filtering. , 2021, , .		2
151	Architecture and Algorithm Co-Design Framework for Embedded Processors in Event-Based Cameras. , 2021, , .		2
152	In-Situ Characterization of Carrier Mobility in Field Effect Transistors. , 1989, , 1247-1254.		2
153	Asynchronous Communication of 2D Motion Information Using Winner-Takes-All Arbitration. , 1998, , 217-227.		2
154	Winner-Takes-All Associative Memory: A Hamming Distance Vector Quantizer. , 1998, , 437-456.		1
155	<title>Advanced on-FPA signal processing for staring IRFPAs</title>. , 2001, 4435, 247.		1
156	A Monolithic Isolation Amplifier in Silicon-on-Insulator CMOS. , 0, , .		1
157	Integrated PDMS/CMOS Microsystem for Autonomous Incubation and Imaging in Cell Culture Studies. , 2006, , .		1
158	Design and characterization of a gain-enhanced floating gate-body tied photodetector in Silicon on Sapphire CMOS. , 2007, , .		1
159	Erratum for â€˜Single photon avalanche photodetector with integrated quenching fabricated in TSMC 0.18â€¦[micro sign]m 1.8â€¦V CMOS processâ€™. Electronics Letters, 2008, 44, 1284.	1.0	1
160	PWL cores for nonlinear array processing. , 2010, , .		1
161	Printed Organic Electronic Sensors. Springer Series on Chemical Sensors and Biosensors, 2012, , 191-216.	0.5	1
162	High-performance, event-driven, low-cost, and SWaP imaging sensor for hostile fire detection, homeland protection, and border security. Proceedings of SPIE, 2012, , .	0.8	1

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163	Signal to symbol converters: Overview, opportunities and challenges. , 2013, , .		1
164	Design of a vanishing point algorithm for custom ASIC. , 2015, , .		1
165	Characterization of RTN noise in the analog front-end of digital pixel imagers. , 2017, , .		1
166	A Mixed-Signal Successive Approximation Architecture for Energy-Efficient Fixed-Point Arithmetic in 16nm FinFET. , 2019, , .		1
167	Efficient, event-driven feature extraction and unsupervised object tracking for embedded applications. , 2021, , .		1
168	A semi-supervised version of heteroscedastic linear discriminant analysis. , 0, , .		1
169	Trade-Offs in Sensor Systems Design: A Tutorial. IEEE Sensors Journal, 2022, 22, 10040-10061.	4.7	1
170	Real Number Modeling of a SAR ADC behavior using SystemVerilog. , 2022, , .		1
171	Special Issue on Advances in Life Science Systems and Applications: Guest Editorial. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2006, 53, 2345-2348.	0.1	0
172	Localized closed-loop temperature control and regulation in hybrid silicon/silicone life science microsystems. , 2007, , .		0
173	Design of An Ultra Wideband Transmitter in 0.18μm 3D Silicon on Insulator CMOS. , 2007, , .		0
174	Architecture of a &#x003BC;RFID with integrated antenna in 3D SOI-CMOS. , 2007, , .		0
175	Design, Analysis and Implementation of Integrated Micro-Thermal Control Systems. , 2007, , .		0
176	Guest Editorialâ€"Special Issue on Selected Papers From BioCAS 2010. IEEE Transactions on Biomedical Circuits and Systems, 2011, 5, 401-402.	4.0	0
177	All digital programmable Gaussian pulse generator for ultra-wideband transmitter. , 2013, , .		0
178	Auditory modulation of visual proto-object formation in a hierarchical auditory-visual saliency map. , 2013, , .		0
179	Organic diode implementations in configurable architectures and temperature sensors. , 2013, , .		0
180	Representation of temporal coherence: CHAINS algorithm and FPGA implementation. , 2013, , .		0

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181	Design of configurable chipping sequence generator for high‑speed parallel samplers. Electronics Letters, 2013, 49, 875-876.	1.0	0
182	Audio-Visual beamforming with the Eigenmike microphone array an omni-camera and cognitive auditory features. , 2017, , .		0
183	Spike propagation path planning on IBM TrueNorth neurosynaptic system. Electronics Letters, 2017, 53, 1023-1025.	1.0	0
184	Parallelism Analysis for a Multi-core Speech Recognition Architecture. , 2019, , .		0
185	Multilevel Storage Cell Characterization and Behavior Modeling of a Crossbar Computational Array in ESF3 Flash Technology : (Invited Paper). , 2019, , .		0
186	Graphical Model Transformation Analysis for Cognitive Computing and Machine Learning on the SpiNNaker Chip Multiprocessor. , 2019, , .		0
187	The Conical-Fishbone Clock Tree: A Clock-Distribution Network for a Heterogeneous Chip Multiprocessor AI Chiplet. , 2019, , .		0
188	Parallel Computation of Event-Based Visual Features Using Relational Graphs. , 2021, , .		0
189	Hybrid Silicon/Silicone (polydimethylsiloxane) Microsystem for Cell Culture. Annual International Conference of the IEEE Engineering in Medicine and Biology Society, 2006, , .	0.5	0