

Jack Dongarra

List of Publications by Year in descending order

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535
papers

22,357
citations

29994

54
h-index

24179

110
g-index

576
all docs

576
docs citations

576
times ranked

8930
citing authors

#	ARTICLE	IF	CITATIONS
1	Accelerating Restarted GMRES With Mixed Precision Arithmetic. IEEE Transactions on Parallel and Distributed Systems, 2022, 33, 1027-1037.	4.0	13
2	Accelerating Geostatistical Modeling and Prediction With Mixed-Precision Computations: A High-Productivity Approach With PaRSEC. IEEE Transactions on Parallel and Distributed Systems, 2022, 33, 964-976.	4.0	14
3	Evaluating Data Redistribution in PaRSEC. IEEE Transactions on Parallel and Distributed Systems, 2022, 33, 1856-1872.	4.0	2
4	Using long vector extensions for MPI reductions. Parallel Computing, 2022, 109, 102871.	1.3	5
5	Comparing Distributed Termination Detection Algorithms for Modern HPC Platforms. International Journal of Networking and Computing, 2022, 12, 26-46.	0.3	0
6	Batch QR Factorization on GPUs: Design, Optimization, and Tuning. Lecture Notes in Computer Science, 2022, , 60-74.	1.0	5
7	A Framework to Exploit Data Sparsity in Tile Low-Rank Cholesky Factorization. , 2022, , .		5
8	Exploiting Block Structures of KKT Matrices for Efficient Solution of Convex Optimization Problems. IEEE Access, 2021, 9, 116604-116611.	2.6	0
9	A survey of numerical linear algebra methods utilizing mixed-precision arithmetic. International Journal of High Performance Computing Applications, 2021, 35, 344-369.	2.4	61
10	Translational process: Mathematical software perspective. Journal of Computational Science, 2021, 52, 101216.	1.5	1
11	Leveraging PaRSEC Runtime Support to Tackle Challenging 3D Data-Sparse Matrix Problems. , 2021, , .		6
12	Efficient exascale discretizations: High-order finite element methods. International Journal of High Performance Computing Applications, 2021, 35, 527-552.	2.4	24
13	A Set of Batched Basic Linear Algebra Subprograms and LAPACK Routines. ACM Transactions on Mathematical Software, 2021, 47, 1-23.	1.6	16
14	Computational Science in the Interconnected World: Selected papers from 2019 International Conference on Computational Science. Journal of Computational Science, 2020, 47, 101222.	1.5	4
15	Matrix multiplication on batches of small matrices in half and half-complex precisions. Journal of Parallel and Distributed Computing, 2020, 145, 188-201.	2.7	10
16	MAGMA templates for scalable linear algebra on emerging architectures. International Journal of High Performance Computing Applications, 2020, 34, 645-658.	2.4	6
17	Reducing the amount of out-of-core data access for GPU-accelerated randomized SVD. Concurrency Computation Practice and Experience, 2020, 32, e5754.	1.4	8
18	Numerical algorithms for high-performance computational science. Philosophical Transactions Series A, Mathematical, Physical, and Engineering Sciences, 2020, 378, 20190066.	1.6	15

#	ARTICLE	IF	CITATIONS
19	Investigating the Benefit of FP16-Enabled Mixed-Precision Solvers for Symmetric Positive Definite Matrices Using GPUs. Lecture Notes in Computer Science, 2020, , 237-250.	1.0	6
20	Mixed-precision iterative refinement using tensor cores on GPUs to accelerate solution of linear systems. Proceedings of the Royal Society A: Mathematical, Physical and Engineering Sciences, 2020, 476, 20200110.	1.0	26
21	Design, Optimization, and Benchmarking of Dense Linear Algebra Algorithms on AMD GPUs. , 2020, , .		10
22	High-Order Finite Element Method using Standard and Device-Level Batch GEMM on GPUs. , 2020, , .		4
23	Load-balancing Sparse Matrix Vector Product Kernels on GPUs. ACM Transactions on Parallel Computing, 2020, 7, 1-26.	1.2	22
24	Extreme-Scale Task-Based Cholesky Factorization Toward Climate and Weather Prediction Applications. , 2020, , .		24
25	Using Advanced Vector Extensions AVX-512 for MPI Reductions. , 2020, , .		3
26	HAN: a Hierarchical Autotuned Collective Communication Framework. , 2020, , .		12
27	Improving the Performance of the GMRES Method Using Mixed-Precision Techniques. Communications in Computer and Information Science, 2020, , 51-66.	0.4	5
28	Integrating Deep Learning in Domain Sciences at Exascale. Communications in Computer and Information Science, 2020, , 35-50.	0.4	3
29	Flexible Data Redistribution in a Task-Based Runtime System. , 2020, , .		3
30	Scalable Data Generation for Evaluating Mixed-Precision Solvers. , 2020, , .		0
31	Replacing Pivoting in Distributed Gaussian Elimination with Randomized Techniques. , 2020, , .		2
32	Least squares solvers for distributed-memory machines with GPU accelerators. , 2019, , .		2
33	ParLLUT - A Parallel Threshold ILU for GPUs. , 2019, , .		7
34	Distributed-memory lattice H-matrix factorization. International Journal of High Performance Computing Applications, 2019, 33, 1046-1063.	2.4	13
35	Massively Parallel Automated Software Tuning. , 2019, , .		4
36	Guest editorsâ€™ note: Special issue on clusters, clouds, and data for scientific computing. International Journal of High Performance Computing Applications, 2019, 33, 1067-1068.	2.4	0

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37	Performance of asynchronous optimized Schwarz with one-sided communication. <i>Parallel Computing</i> , 2019, 86, 66-81.	1.3	15
38	PAPI software-defined events for in-depth performance analysis. <i>International Journal of High Performance Computing Applications</i> , 2019, 33, 1113-1127.	2.4	11
39	Comparing the performance of rigid, moldable and grid-shaped applications on failure-prone HPC platforms. <i>Parallel Computing</i> , 2019, 85, 1-12.	1.3	8
40	Race to Exascale. <i>Computing in Science and Engineering</i> , 2019, 21, 4-5.	1.2	26
41	Checkpointing Strategies for Shared High-Performance Computing Platforms. <i>International Journal of Networking and Computing</i> , 2019, 9, 28-52.	0.3	4
42	Impacts of Multi-GPU MPI Collective Communications on Large FFT Computation. , 2019, , .		14
43	Performance Analysis of Tile Low-Rank Cholesky Factorization Using PaRSEC Instrumentation Tools. , 2019, , .		12
44	Towards Half-Precision Computation for Complex Matrices: A Case Study for Mixed Precision Solvers on GPUs. , 2019, , .		6
45	Evaluation of Programming Models to Address Load Imbalance on Distributed Multi-Core CPUs: A Case Study with Block Low-Rank Factorization. , 2019, , .		5
46	Generic Matrix Multiplication for Multi-GPU Accelerated Distributed-Memory Platforms over PaRSEC. , 2019, , .		14
47	Fast Batched Matrix Multiplication for Small Sizes Using Half-Precision Arithmetic on GPUs. , 2019, , .		20
48	Increasing Accuracy of Iterative Refinement in Limited Floating-Point Arithmetic on Half-Precision Accelerators. , 2019, , .		3
49	Progressive Optimization of Batched LU Factorization on GPUs. , 2019, , .		3
50	Solving Linear Diophantine Systems on Parallel Architectures. <i>IEEE Transactions on Parallel and Distributed Systems</i> , 2019, 30, 1158-1169.	4.0	13
51	Variable-size batched Gauss-Jordan elimination for block-Jacobi preconditioning on graphics processors. <i>Parallel Computing</i> , 2019, 81, 131-146.	1.3	10
52	Investigating power capping toward energy-efficient scientific applications. <i>Concurrency Computation Practice and Experience</i> , 2019, 31, e4485.	1.4	27
53	Adaptive precision in block-Jacobi preconditioning for iterative sparse linear system solvers. <i>Concurrency Computation Practice and Experience</i> , 2019, 31, e4460.	1.4	41
54	Fine-grained bit-flip protection for relaxation methods. <i>Journal of Computational Science</i> , 2019, 36, 100583.	1.5	3

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55	Hands-On Research and Training in High Performance Data Sciences, Data Analytics, and Machine Learning for Emerging Environments. Lecture Notes in Computer Science, 2019, , 643-655.	1.0	1
56	SLATE. , 2019, , .		42
57	Linear Systems Solvers for Distributed-Memory Machines with GPU Accelerators. Lecture Notes in Computer Science, 2019, , 495-506.	1.0	5
58	MagmaDNN: Towards High-Performance Data Analytics and Machine Learning for Data-Driven Scientific Computing. Lecture Notes in Computer Science, 2019, , 490-503.	1.0	4
59	Batched one-sided factorizations of tiny matrices using GPUs: Challenges and countermeasures. Journal of Computational Science, 2018, 26, 226-236.	1.5	9
60	Symmetric Indefinite Linear Solver Using OpenMP Task on Multicore Architectures. IEEE Transactions on Parallel and Distributed Systems, 2018, 29, 1879-1892.	4.0	6
61	Accelerating the SVD bi-diagonalization of a batch of small matrices using GPUs. Journal of Computational Science, 2018, 26, 237-245.	1.5	12
62	Evaluation of dataflow programming models for electronic structure theory. Concurrency Computation Practice and Experience, 2018, 30, e4490.	1.4	1
63	Guest editorsâ€™ note. International Journal of High Performance Computing Applications, 2018, 32, 3-3.	2.4	0
64	Optimization and performance evaluation of the IDR iterative Krylov solver on GPUs. International Journal of High Performance Computing Applications, 2018, 32, 220-230.	2.4	4
65	Incomplete Sparse Approximate Inverses for Parallel Preconditioning. Parallel Computing, 2018, 71, 1-22.	1.3	35
66	Accelerating NWChem Coupled Cluster through dataflow-based execution. International Journal of High Performance Computing Applications, 2018, 32, 540-551.	2.4	3
67	A failure detector for HPC platforms. International Journal of High Performance Computing Applications, 2018, 32, 139-158.	2.4	12
68	A Guide for Achieving High Performance with Very Small Matrices on GPU: A Case Study of Batched LU and Cholesky Factorizations. IEEE Transactions on Parallel and Distributed Systems, 2018, 29, 973-984.	4.0	16
69	Accelerating the SVD two stage bidiagonal reduction and divide and conquer using GPUs. Parallel Computing, 2018, 74, 3-18.	1.3	9
70	Variable-Size Batched Condition Number Calculation on GPUs. , 2018, , .		1
71	Task based Cholesky decomposition on Xeon Phi architectures using OpenMP. International Journal of Computational Science and Engineering, 2018, 17, 310.	0.4	0
72	Harnessing GPU Tensor Cores for Fast FP16 Arithmetic to Speed up Mixed-Precision Iterative Refinement Solvers. , 2018, , .		104

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73	Post-exascale supercomputing: research opportunities abound. <i>Frontiers of Information Technology and Electronic Engineering</i> , 2018, 19, 1203-1208.	1.5	3
74	Optimizing GPU Kernels for Irregular Batch Workloads: A Case Study for Cholesky Factorization. , 2018, , .		1
75	ADAPT. , 2018, , .		16
76	The Singular Value Decomposition: Anatomy of Optimizing an Algorithm for Extreme Scale. <i>SIAM Review</i> , 2018, 60, 808-865.	4.2	57
77	Computational Benefit of GPU Optimization for the Atmospheric Chemistry Modeling. <i>Journal of Advances in Modeling Earth Systems</i> , 2018, 10, 1952-1969.	1.3	7
78	Autotuning Numerical Dense Linear Algebra for Batched Computation With GPU Hardware Accelerators. <i>Proceedings of the IEEE</i> , 2018, 106, 2040-2055.	16.4	9
79	ParLLUT—A New Parallel Threshold ILU Factorization. <i>SIAM Journal of Scientific Computing</i> , 2018, 40, C503-C519.	1.3	20
80	Using Jacobi iterations and blocking for solving sparse triangular systems in incomplete factorization preconditioning. <i>Journal of Parallel and Distributed Computing</i> , 2018, 119, 219-230.	2.7	27
81	Autotuning in High-Performance Computing Applications. <i>Proceedings of the IEEE</i> , 2018, 106, 2068-2083.	16.4	72
82	Big data and extreme-scale computing. <i>International Journal of High Performance Computing Applications</i> , 2018, 32, 435-479.	2.4	90
83	Performance of Hierarchical-matrix BiCGStab Solver on GPU Clusters. , 2018, , .		5
84	The Design of Fast and Energy-Efficient Linear Solvers: On the Potential of Half-Precision Arithmetic and Iterative Refinement Techniques. <i>Lecture Notes in Computer Science</i> , 2018, , 586-600.	1.0	29
85	Analysis and Design Techniques towards High-Performance and Energy-Efficient Dense Linear Solvers on GPUs. <i>IEEE Transactions on Parallel and Distributed Systems</i> , 2018, 29, 2700-2712.	4.0	7
86	Stability and Performance of Various Singular Value QR Implementations on Multicore CPU with a GPU. <i>ACM Transactions on Mathematical Software</i> , 2017, 43, 1-18.	1.6	9
87	Porting the PLASMA Numerical Library to the OpenMP Standard. <i>International Journal of Parallel Programming</i> , 2017, 45, 612-633.	1.1	25
88	Fast Cholesky factorization on GPUs for batch and native modes in MAGMA. <i>Journal of Computational Science</i> , 2017, 20, 85-93.	1.5	11
89	Solving dense symmetric indefinite systems using GPUs. <i>Concurrency Computation Practice and Experience</i> , 2017, 29, e4055.	1.4	3
90	Batched Gauss-Jordan Elimination for Block-Jacobi Preconditioner Generation on GPUs. , 2017, , .		17

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91	Structure-Aware Linear Solver for Realtime Convex Optimization for Embedded Systems. IEEE Embedded Systems Letters, 2017, 9, 61-64.	1.3	3
92	With Extreme Computing, the Rules Have Changed. Computing in Science and Engineering, 2017, 19, 52-62.	1.2	21
93	Factorization and Inversion of a Million Matrices using GPUs: Challenges and Countermeasures. Procedia Computer Science, 2017, 108, 606-615.	1.2	15
94	Preconditioned Krylov solvers on GPUs. Parallel Computing, 2017, 68, 32-44.	1.3	31
95	The Design and Performance of Batched BLAS on Modern High-Performance Computing Systems. Procedia Computer Science, 2017, 108, 495-504.	1.2	50
96	High-performance Cholesky factorization for GPU-only execution. , 2017, , .		11
97	Optimized Batched Linear Algebra for Modern Architectures. Lecture Notes in Computer Science, 2017, , 511-522.	1.0	6
98	Variable-Size Batched LU for Small Matrices and Its Integration into Block-Jacobi Preconditioning. , 2017, , .		7
99	The Art of Computational Science, Bridging Gaps “ Forming Alloys. Preface for ICCS 2017. Procedia Computer Science, 2017, 108, 1-6.	1.2	2
100	Variable-Size Batched Gauss-Huard for Block-Jacobi Preconditioning. Procedia Computer Science, 2017, 108, 1783-1792.	1.2	4
101	Optimizing the SVD Bidiagonalization Process for a Batch of Small Matrices. Procedia Computer Science, 2017, 108, 1008-1018.	1.2	4
102	Flexible batched sparse matrix-vector product on GPUs. , 2017, , .		6
103	A look back on 30 years of the Gordon Bell Prize. International Journal of High Performance Computing Applications, 2017, 31, 469-484.	2.4	13
104	Improving Performance of GMRES by Reducing Communication and Pipelining Global Collectives. , 2017, , .		13
105	On the performance and energy efficiency of sparse linear algebra on GPUs. International Journal of High Performance Computing Applications, 2017, 31, 375-390.	2.4	17
106	Non-GPU-resident symmetric indefinite factorization. Concurrency Computation Practice and Experience, 2017, 29, e4012.	1.4	6
107	Power-aware computing: Measurement, control, and performance analysis for Intel Xeon Phi. , 2017, , .		14
108	Novel HPC techniques to batch execution of many variable size BLAS computations on GPUs. , 2017, , .		15

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109	Dynamic task discovery in PaRSEC. , 2017, , .		43
110	Towards numerical benchmark for half-precision floating point arithmetic. , 2017, , .		7
111	Bidiagonalization and R-Bidiagonalization: Parallel Tiled Algorithms, Critical Paths and Distributed-Memory Implementation. , 2017, , .		1
112	Investigating half precision arithmetic to accelerate dense linear system solvers. , 2017, , .		37
113	Autotuning batch Cholesky factorization in CUDA with interleaved layout of matrices. , 2017, , .		3
114	Sampling algorithms to update truncated SVD. , 2017, , .		2
115	Out of memory SVD solver for big data. , 2017, , .		6
116	Scaling point set registration in 3D across thread counts on multicore and hardware accelerator platforms through autotuning for large scale analysis of scientific point clouds. , 2017, , .		1
117	Accelerating the Conjugate Gradient Algorithm with GPUs in CFD Simulations. Lecture Notes in Computer Science, 2017, , 35-43.	1.0	1
118	A Framework for Out of Memory SVD Algorithms. Lecture Notes in Computer Science, 2017, , 158-178.	1.0	11
119	Performance optimization of Sparse Matrix×Vector Multiplication for multi-component PDE-based applications using GPUs. Concurrency Computation Practice and Experience, 2016, 28, 3447-3465.	1.4	8
120	Performance-Portable Autotuning of OpenCL Kernels for Convolutional Layers of Deep Neural Networks. , 2016, , .		8
121	Efficiency of General Krylov Methods on GPUs – An Experimental Study. , 2016, , .		7
122	Towards Achieving Performance Portability Using Directives for Accelerators. , 2016, , .		19
123	Failure Detection and Propagation in HPC systems. , 2016, , .		12
124	Techniques for Solving Large-Scale Graph Problems on Heterogeneous Platforms. Communications in Computer and Information Science, 2016, , 318-332.	0.4	0
125	LU, QR, and Cholesky factorizations: Programming model, performance analysis and optimization techniques for the Intel Knights Landing Xeon Phi. , 2016, , .		7
126	Linear algebra software for large-scale accelerated multicore computing. Acta Numerica, 2016, 25, 1-160.	6.3	11

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127	Batched Generation of Incomplete Sparse Approximate Inverses on GPUs. , 2016, , .		11
128	Why is it Hard to Describe Properties of Algorithms?. Procedia Computer Science, 2016, 101, 4-7.	1.2	9
129	Performance analysis and acceleration of explicit integration for large kinetic networks using batched GPU computations. , 2016, , .		2
130	On the Development of Variable Size Batched Computation for Heterogeneous Parallel Architectures. , 2016, , .		5
131	Performance Tuning and Optimization Techniques of Fixed and Variable Size Batched Cholesky Factorization on GPUs. Procedia Computer Science, 2016, 80, 119-130.	1.2	9
132	Data through the Computational Lens, Preface for ICCS 2016. Procedia Computer Science, 2016, 80, 1-7.	1.2	5
133	Sunway TaihuLight supercomputer makes its appearance. National Science Review, 2016, 3, 265-266.	4.6	27
134	Hessenberg Reduction with Transient Error Resilience on GPU-Based Hybrid Architectures. , 2016, , .		2
135	Task-Based Cholesky Decomposition on Knights Corner Using OpenMP. Lecture Notes in Computer Science, 2016, , 544-562.	1.0	6
136	Heterogeneous Streaming. , 2016, , .		12
137	Search Space Generation and Pruning System for Autotuners. , 2016, , .		9
138	A new metric for ranking high-performance computing systems. National Science Review, 2016, 3, 30-35.	4.6	24
139	Updating incomplete factorization preconditioners for model order reduction. Numerical Algorithms, 2016, 73, 611-630.	1.1	14
140	Assessing the cost of redistribution followed by a computational kernel: Complexity and performance results. Parallel Computing, 2016, 52, 22-41.	1.3	6
141	Implementation and Tuning of Batched Cholesky Factorization and Solve for NVIDIA GPUs. IEEE Transactions on Parallel and Distributed Systems, 2016, 27, 2036-2048.	4.0	29
142	High-performance conjugate-gradient benchmark: A new metric for ranking high-performance computing systems. International Journal of High Performance Computing Applications, 2016, 30, 3-10.	2.4	93
143	Accelerating NWChem Coupled Cluster Through Dataflow-Based Execution. Lecture Notes in Computer Science, 2016, , 366-376.	1.0	5
144	Dense Symmetric Indefinite Factorization on GPU Accelerated Architectures. Lecture Notes in Computer Science, 2016, , 86-95.	1.0	4

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145	Power Management and Event Verification in PAPI. , 2016, , 41-51.		10
146	Domain Overlap for Iterative Sparse Triangular Solves on GPUs. Lecture Notes in Computational Science and Engineering, 2016, , 527-545.	0.1	5
147	Performance, Design, and Autotuning of Batched GEMM for GPUs. Lecture Notes in Computer Science, 2016, , 21-38.	1.0	70
148	With Extreme Scale Computing the Rules Have Changed. Lecture Notes in Computer Science, 2016, , 3-6.	1.0	3
149	Practical scalable consensus for pseudo-synchronous distributed systems. , 2015, , .		12
150	Efficient implementation of quantum materials simulations on distributed CPU-GPU systems. , 2015, , .		10
151	GPU-accelerated co-design of induced dimension reduction. , 2015, , .		3
152	HPC Programming on Intel Many-Integrated-Core Hardware with MAGMA Port to Xeon Phi. Scientific Programming, 2015, 2015, 1-11.	0.5	12
153	Strengthening compute and data intensive capacities of Armenia. , 2015, , .		12
154	Performance Analysis and Optimisation of Two-sided Factorization Algorithms for Heterogeneous Platform. Procedia Computer Science, 2015, 51, 180-190.	1.2	1
155	Towards batched linear solvers on accelerated hardware platforms. ACM SIGPLAN Notices, 2015, 50, 261-262.	0.2	5
156	Guest Editorsâ€™ Note: Special Issue on Clusters, Clouds and Data for Scientific Computing. Parallel Processing Letters, 2015, 25, 1502002.	0.4	0
157	A survey of recent developments in parallel implementations of Gaussian elimination. Concurrency Computation Practice and Experience, 2015, 27, 1292-1309.	1.4	19
158	A scalable approach to solving dense linear algebra problems on hybrid CPUâ€™GPU systems. Concurrency Computation Practice and Experience, 2015, 27, 3702-3723.	1.4	7
159	Experiences in autotuning matrix multiplication for energy minimization on GPUs. Concurrency Computation Practice and Experience, 2015, 27, 5096-5113.	1.4	16
160	Computing Low-Rank Approximation of a Dense Matrix on Multicore CPUs with a GPU and Its Application to Solving a Hierarchically Semiseparable Linear System of Equations. Scientific Programming, 2015, 2015, 1-17.	0.5	0
161	Batched matrix computations on hardware accelerators based on GPUs. International Journal of High Performance Computing Applications, 2015, 29, 193-208.	2.4	48
162	Mixed-Precision Cholesky QR Factorization and Its Case Studies on Multicore CPU with Multiple GPUs. SIAM Journal of Scientific Computing, 2015, 37, C307-C330.	1.3	36

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163	Weighted dynamic scheduling with many parallelism grains for offloading of numerical workloads to multiple varied accelerators. , 2015, , .		2
164	The TOP500 List and Progress in High-Performance Computing. Computer, 2015, 48, 42-49.	1.2	31
165	Adaptive precision solvers for sparse linear systems. , 2015, , .		8
166	Tuning stationary iterative solvers for fault resilience. , 2015, , .		6
167	PaRSEC in Practice: Optimizing a Legacy Chemistry Application through Distributed Task-Based Execution. , 2015, , .		15
168	Mixed-precision block gram Schmidt orthogonalization. , 2015, , .		5
169	Flexible Linear Algebra Development and Scheduling with Cholesky Factorization. , 2015, , .		4
170	Accelerating collaborative filtering using concepts from high performance computing. , 2015, , .		25
171	MAGMA embedded: Towards a dense linear algebra library for energy efficient extreme computing. , 2015, , .		11
172	Optimization for performance and energy for batched matrix computations on GPUs. , 2015, , .		7
173	Energy efficiency and performance frontiers for sparse computations on GPU supercomputers. , 2015, , .		9
174	Asynchronous Iterative Algorithm for Computing Incomplete Factorizations on GPUs. Lecture Notes in Computer Science, 2015, , 1-16.	1.0	20
175	Acceleration of GPU-based Krylov solvers via data transfer reduction. International Journal of High Performance Computing Applications, 2015, 29, 366-383.	2.4	17
176	Towards batched linear solvers on accelerated hardware platforms. , 2015, , .		12
177	Exascale computing and big data. Communications of the ACM, 2015, 58, 56-68.	3.3	322
178	Hierarchical DAG Scheduling for Hybrid Distributed Systems. , 2015, , .		34
179	Design for a Soft Error Resilient Dynamic Task-Based Runtime. , 2015, , .		19
180	Algorithm-Based Fault Tolerance for Dense Matrix Factorizations, Multiple Failures and Accuracy. ACM Transactions on Parallel Computing, 2015, 1, 1-28.	1.2	17

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181	Mixing LU and QR factorization algorithms to design high-performance dense linear algebra solvers. <i>Journal of Parallel and Distributed Computing</i> , 2015, 85, 32-46.	2.7	5
182	Fault Tolerance Techniques for High-Performance Computing. <i>Computer Communications and Networks</i> , 2015, , 3-85.	0.8	36
183	Iterative Sparse Triangular Solves for Preconditioning. <i>Lecture Notes in Computer Science</i> , 2015, , 650-661.	1.0	46
184	Accelerating Computation of Eigenvectors in the Dense Nonsymmetric Eigenvalue Problem. <i>Lecture Notes in Computer Science</i> , 2015, , 182-191.	1.0	3
185	Mixed-Precision Orthogonalization Scheme and Adaptive Step Size for Improving the Stability and Performance of CA-GMRES on GPUs. <i>Lecture Notes in Computer Science</i> , 2015, , 17-30.	1.0	6
186	A Framework for Batched and GPU-Resident Factorization Algorithms Applied to Block Householder Transformations. <i>Lecture Notes in Computer Science</i> , 2015, , 31-47.	1.0	20
187	Randomized algorithms to update partial singular value decomposition on a hybrid CPU/GPU cluster. , 2015, , .		7
188	Performance of random sampling for computing low-rank approximations of a dense matrix on GPUs. , 2015, , .		4
189	Composing resilience techniques: ABFT, periodic and incremental checkpointing. <i>International Journal of Networking and Computing</i> , 2015, 5, 2-25.	0.3	18
190	Self-adaptive Multiprecision Preconditioners on Multicore and Manycore Architectures. <i>Lecture Notes in Computer Science</i> , 2015, , 115-123.	1.0	0
191	Heterogenous Acceleration for Linear Algebra in Multi-coprocessor Environments. <i>Lecture Notes in Computer Science</i> , 2015, , 31-42.	1.0	1
192	Performance and reliability trade-offs for the double checkpointing algorithm. <i>International Journal of Networking and Computing</i> , 2014, 4, 23-41.	0.3	4
193	Design and Implementation of a Large Scale Tree-Based QR Decomposition Using a 3D Virtual Systolic Array and a Lightweight Runtime. <i>Parallel Processing Letters</i> , 2014, 24, 1442004.	0.4	0
194	Assessing the Impact of ABFT and Checkpoint Composite Strategies. , 2014, , .		10
195	POSTER: Utilizing dataflow-based execution for coupled cluster methods. , 2014, , .		4
196	Optimizing Krylov Subspace Solvers on Graphics Processing Units. , 2014, , .		12
197	PTG: An Abstraction for Unhindered Parallelism. , 2014, , .		31
198	Dynamically Balanced Synchronization-Avoiding LU Factorization with Multicore and GPUs. , 2014, , .		6

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199	New Algorithm for Computing Eigenvectors of the Symmetric Eigenvalue Problem. , 2014, , .		2
200	Access-averse framework for computing low-rank matrix approximations. , 2014, , .		3
201	Scaling up matrix computations on shared-memory manycore systems with 1000 CPU cores. , 2014, , .		7
202	Hybrid Multi-elimination ILU Preconditioners on GPUs. , 2014, , .		2
203	Performance and Portability with OpenCL for Throughput-Oriented HPC Workloads across Accelerators, Coprocessors, and Multicore Processors. , 2014, , .		4
204	LU Factorization of Small Matrices: Accelerating Batched DGETRF on the GPU. , 2014, , .		32
205	Deflation Strategies to Improve the Convergence of Communication-Avoiding GMRES. , 2014, , .		2
206	Designing LU-QR Hybrid Solvers for Performance and Stability. , 2014, , .		3
207	Improving the Performance of CA-GMRES on Multicores with Multiple GPUs. , 2014, , .		35
208	A Step towards Energy Efficient Computing: Redesigning a Hydrodynamic Application on CPU-GPU. , 2014, , .		35
209	Unified model for assessing checkpointing protocols at extremeâ€scale. Concurrency Computation Practice and Experience, 2014, 26, 2772-2791.	1.4	40
210	Parallel Simulation of Superscalar Scheduling. , 2014, , .		3
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