

Abbas Rahimi

List of Publications by Year in descending order

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Version: 2024-02-01

82
papers

2,686
citations

430874

18
h-index

395702

33
g-index

83
all docs

83
docs citations

83
times ranked

1442
citing authors

#	ARTICLE	IF	CITATIONS
1	Generalized Key-Value Memory to Flexibly Adjust Redundancy in Memory-Augmented Networks. IEEE Transactions on Neural Networks and Learning Systems, 2023, 34, 10993-10998.	11.3	4
2	A Survey on Hyperdimensional Computing aka Vector Symbolic Architectures, Part I: Models and Data Transformations. ACM Computing Surveys, 2023, 55, 1-40.	23.0	21
3	A wearable biosensing system with in-sensor adaptive machine learning for hand gesture recognition. Nature Electronics, 2021, 4, 54-63.	26.0	317
4	An Ensemble of Hyperdimensional Classifiers: Hardware-Friendly Short-Latency Seizure Detection With Automatic iEEG Electrode Selection. IEEE Journal of Biomedical and Health Informatics, 2021, 25, 935-946.	6.3	27
5	Explainable Deep Learning for Medical Time Series Data. Lecture Notes of the Institute for Computer Sciences, Social-Informatics and Telecommunications Engineering, 2021, , 244-256.	0.3	4
6	Robust high-dimensional memory-augmented neural networks. Nature Communications, 2021, 12, 2468.	12.8	50
7	Energy Efficient In-Memory Hyperdimensional Encoding for Spatio-Temporal Signal Processing. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 1725-1729.	3.0	6
8	Real-time Language Recognition using Hyperdimensional Computing on Phase-change Memory Array. , 2021, , .		2
9	A Primer on Hyperdimensional Computing for iEEG Seizure Detection. Frontiers in Neurology, 2021, 12, 701791.	2.4	5
10	Assessing Robustness of Hyperdimensional Computing Against Errors in Associative Memory : (Invited) Tj ETQq0 0 0 rgBT /Overlock 10 T		11
11	Guest Editorial: IEEE TC Special Issue On Smart Edge Computing and IoT. IEEE Transactions on Computers, 2021, 70, 1146-1147.	3.4	0
12	Near-channel classifier: symbiotic communication and classification in high-dimensional space. Brain Informatics, 2021, 8, 16.	3.0	9
13	A 5 <i>î¼</i>W Standard Cell Memory-Based Configurable Hyperdimensional Computing Accelerator for Always-on Smart Sensing. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 4116-4128.	5.4	12
14	Hardware/Software Codesign for Energy Efficiency and Robustness: From Error-Tolerant Computing to Approximate Computing. Embedded Systems, 2021, , 527-543.	0.6	3
15	Hyperdimensional Computing for Blind and One-Shot Classification of EEG Error-Related Potentials. Mobile Networks and Applications, 2020, 25, 1958-1969.	3.3	30
16	Autoscaling Bloom filter: controlling trade-off between true and false positives. Neural Computing and Applications, 2020, 32, 3675-3684.	5.6	20
17	Hyperdimensional Computing With Local Binary Patterns: One-Shot Learning of Seizure Onset and Identification of Ictogenic Brain Regions Using Short-Time iEEG Recordings. IEEE Transactions on Biomedical Engineering, 2020, 67, 601-613.	4.2	45
18	Compressing Subject-specific Brain-Computer Interface Models into One Model by Superposition in Hyperdimensional Space. , 2020, , .		7

#	ARTICLE	IF	CITATIONS
19	Binarization Methods for Motor-Imagery Brain-Computer Interface Classification. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2020, 10, 567-577.	3.6	6
20	In-memory hyperdimensional computing. Nature Electronics, 2020, 3, 327-337.	26.0	145
21	Evolvable Hyperdimensional Computing: Unsupervised Regeneration of Associative Memory to Recover Faulty Components. , 2020, , .		6
22	Hyperdimensional computing nanosystem: in-memory computing using monolithic 3D integration of RRAM and CNFET. , 2020, , 195-219.		2
23	Binary Models for Motor-Imagery Brain-Computer Interfaces: Sparse Random Projection and Binarized SVM. , 2020, , .		2
24	Integrating event-based dynamic vision sensors with sparse hyperdimensional computing. , 2020, , .		13
25	Hyperdimensional Computing-based Multimodality Emotion Recognition with Physiological Signals. , 2019, , .		38
26	Hardware Optimizations of Dense Binary Hyperdimensional Computing: Rematerialization of Hypervectors, Binarized Bundling, and Combinational Associative Memory. ACM Journal on Emerging Technologies in Computing Systems, 2019, 15, 1-25.	2.3	38
27	Laelaps: An Energy-Efficient Seizure Detection Algorithm from Long-term Human iEEG Recordings without False Alarms. , 2019, , .		39
28	Online Learning and Classification of EMG-Based Gestures on a Parallel Ultra-Low Power Platform Using Hyperdimensional Computing. IEEE Transactions on Biomedical Circuits and Systems, 2019, 13, 516-528.	4.0	53
29	Applications of Computation-In-Memory Architectures based on Memristive Devices. , 2019, , .		24
30	Analysis of Contraction Effort Level in EMG-Based Gesture Recognition Using Hyperdimensional Computing. , 2019, , .		7
31	Efficient Biosignal Processing Using Hyperdimensional Computing: Network Templates for Combined Learning and Classification of ExG Signals. Proceedings of the IEEE, 2019, 107, 123-143.	21.3	82
32	Resistive CAM Acceleration for Tunable Approximate Computing. IEEE Transactions on Emerging Topics in Computing, 2019, 7, 271-280.	4.6	23
33	Towards Versatile Fast Training for Wearable Interfaces in Prosthetics. Biosystems and Biorobotics, 2019, , 157-161.	0.3	0
34	HDNA: Energy-efficient DNA sequencing using hyperdimensional computing. , 2018, , .		42
35	CLIM: A Cross-Level Workload-Aware Timing Error Prediction Model for Functional Units. IEEE Transactions on Computers, 2018, 67, 771-783.	3.4	26
36	Classification and Recall With Binary Hyperdimensional Computing: Tradeoffs in Choice of Density and Mapping Characteristics. IEEE Transactions on Neural Networks and Learning Systems, 2018, 29, 5880-5898.	11.3	64

#	ARTICLE	IF	CITATIONS
37	Brain-inspired computing exploiting carbon nanotube FETs and resistive RAM: Hyperdimensional computing case study. , 2018, , .		84
38	Multi-Stage Tunable Approximate Search in Resistive Associative Memory. IEEE Transactions on Multi-Scale Computing Systems, 2018, 4, 17-29.	2.4	17
39	PULP-HD: Accelerating Brain-Inspired High-Dimensional Computing on a Parallel Ultra-Low Power Platform. , 2018, , .		6
40	One-shot Learning for iEEG Seizure Detection Using End-to-end Binary Operations: Local Binary Patterns with Hyperdimensional Computing. , 2018, , .		55
41	Hyperdimensional Computing Exploiting Carbon Nanotube FETs, Resistive RAM, and Their Monolithic 3D Integration. IEEE Journal of Solid-State Circuits, 2018, 53, 3183-3196.	5.4	49
42	PULP-HD. , 2018, , .		32
43	An EMG Gesture Recognition System with Flexible High-Density Sensors and Brain-Inspired High-Dimensional Classifier. , 2018, , .		65
44	An 826 MOPS, 210uW/MHz Unum ALU in 65 nm. , 2018, , .		13
45	Exploring Hyperdimensional Associative Memory. , 2017, , .		132
46	An Approximation Workflow for Exploiting Data-Level Parallelism in FPGA Acceleration. , 2017, , 151-164.		3
47	Memristive-Based Associative Memory for Approximate Computational Reuse. , 2017, , 165-179.		0
48	Memristive-Based Associative Memory for Error Recovery. , 2017, , 117-130.		0
49	High-Dimensional Computing as a Nanoscalable Paradigm. IEEE Transactions on Circuits and Systems I: Regular Papers, 2017, 64, 2508-2521.	5.4	92
50	Low-Power Sparse Hyperdimensional Encoder for Language Recognition. IEEE Design and Test, 2017, 34, 94-101.	1.2	32
51	Human-centric computing â€” The case for a Hyper-Dimensional approach. , 2017, , .		1
52	Efficient neural network acceleration on GPGPU using content addressable memory. , 2017, , .		30
53	SLoT: A supervised learning model to predict dynamic timing errors of functional units. , 2017, , .		21
54	VoiceHD: Hyperdimensional Computing for Efficient Speech Recognition. , 2017, , .		104

#	ARTICLE	IF	CITATIONS
55	Hyperdimensional Computing for Noninvasive Brain-Computer Interfaces: Blind and One-Shot Classification of EEG Error-Related Potentials. , 2017, , .		36
56	Work-Unit Tolerance. , 2017, , 91-115.		0
57	Kernel-Level Tolerance. , 2017, , 61-74.		0
58	Sequence-Level Tolerance. , 2017, , 21-46.		0
59	Spatial and Temporal Memoization. , 2017, , 181-190.		0
60	Hierarchically Focused Guardbanding. , 2017, , 75-88.		0
61	Instruction-Level Tolerance. , 2017, , 11-19.		0
62	Procedure-Level Tolerance. , 2017, , 47-60.		0
63	WILD: A workload-based learning model to predict dynamic delay of functional units. , 2016, , .		8
64	A low-power hybrid magnetic cache architecture exploiting narrow-width values. , 2016, , .		15
65	Hyperdimensional computing with 3D VRRAM in-memory kernels: Device-architecture co-design for energy-efficient, error-resilient language recognition. , 2016, , .		95
66	Associative Memristive Memory for Approximate Computing in GPUs. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2016, 6, 222-234.	3.6	22
67	ACAM. , 2016, , .		51
68	A Robust and Energy-Efficient Classifier Using Brain-Inspired Hyperdimensional Computing. , 2016, , .		160
69	Hyperdimensional biosignal processing: A case study for EMG-based hand gesture recognition. , 2016, , .		103
70	CIRCA-GPUs: Increasing Instruction Reuse Through Inexact Computing in GP-GPUs. IEEE Design and Test, 2016, 33, 85-92.	1.2	6
71	Variability Mitigation in Nanometer CMOS Integrated Systems: A Survey of Techniques From Circuits to Software. Proceedings of the IEEE, 2016, 104, 1410-1448.	21.3	32
72	Resistive Configurable Associative Memory for Approximate Computing. , 2016, , .		59

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73	Resistive Bloom Filters: From Approximate Membership to Approximate Computing with Bounded Errors. , 2016, , .		3
74	Axilog: Language Support for Approximate Hardware Design. , 2015, , .		29
75	Approximate Associative Memristive Memory for Energy-Efficient GPUs. , 2015, , .		46
76	Axilog: Abstractions for Approximate Hardware Design and Reuse. IEEE Micro, 2015, 35, 16-30.	1.8	11
77	NSF expedition on variability-aware software: Recent results and contributions. IT - Information Technology, 2015, 57, 181-198.	0.9	10
78	Task scheduling strategies to mitigate hardware variability in embedded shared memory clusters. , 2015, , .		10
79	Supervised learning based model for predicting variability-induced timing errors. , 2015, , .		8
80	Aging-Aware Compilation for GP-GPUs. Transactions on Architecture and Code Optimization, 2015, 12, 1-20.	2.0	8
81	Energy-efficient mapping of biomedical applications on domain-specific accelerator under process variation. , 2014, , .		18
82	Application-Adaptive Guardbanding to Mitigate Static and Dynamic Variability. IEEE Transactions on Computers, 2014, 63, 2160-2173.	3.4	37