

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	VMT: Virtualized Multi-Threading for Accelerating Graph Workloads on Commodity Processors. IEEE Transactions on Computers, 2022, 71, 1386-1398.	3.4	0
2	DeepP: Deep Learning Multi-Program Prefetch Configuration for the IBM POWER 8. IEEE Transactions on Computers, 2022, 71, 2646-2658.	3.4	1
3	Effect of Hyper-Threading in Latency-Critical Multithreaded Cloud Applications and Utilization Analysis of the Major System Resources. Future Generation Computer Systems, 2022, 131, 194-208.	7.5	4
4	The Forward Slice Core: A High-Performance, Yet Low-Complexity Microarchitecture. Transactions on Architecture and Code Optimization, 2022, 19, 1-25.	2.0	0
5	A Neural Network to Estimate Isolated Performance from Multi-Program Execution. , 2022, , .		0
6	ITSLF: Inter-Thread Store-to-Load Forwarding in Simultaneous Multithreading. , 2021, , .		1
7	Thread Isolation to Improve Symbiotic Scheduling on SMT Multicore Processors. IEEE Transactions on Parallel and Distributed Systems, 2020, 31, 359-373.	5.6	4
8	Precise Runahead Execution. , 2020, , .		12
9	Bandwidth-Aware Dynamic Prefetch Configuration for IBM POWER8. IEEE Transactions on Parallel and Distributed Systems, 2020, 31, 1970-1982.	5.6	6
10	The Forward Slice Core Microarchitecture. , 2020, , .		8
11	Precise Runahead Execution. IEEE Computer Architecture Letters, 2019, 18, 71-74.	1.5	3
12	Designing lab sessions focusing on real processors for computer architecture courses: A practical perspective. Journal of Parallel and Distributed Computing, 2018, 118, 128-139.	4.1	1
13	A Workload Generator for Evaluating SMT Real-Time Systems. , 2018, , .		0
14	Perf&Fair: A Progress-Aware Scheduler to Enhance Performance and Fairness in SMT Multicores. IEEE Transactions on Computers, 2017, 66, 905-911.	3.4	20
15	Improving IBM POWER8 Performance Through Symbiotic Job Scheduling. IEEE Transactions on Parallel and Distributed Systems, 2017, 28, 2838-2851.	5.6	4
16	Symbiotic job scheduling on the IBM POWER8. , 2016, , .		11
17	Bandwidth-Aware On-Line Scheduling in SMT Multicores. IEEE Transactions on Computers, 2016, 65, 422-434.	3.4	14
18	Addressing Fairness in SMT Multicores with a Progress-Aware Scheduler. , 2015, , .		12

#	ARTICLE	IF	CITATIONS
19	Addressing bandwidth contention in SMT multicores through scheduling. , 2014, , .		2
20	Cache-Hierarchy Contention-Aware Scheduling in CMPs. IEEE Transactions on Parallel and Distributed Systems, 2014, 25, 581-590.	5.6	21
21	Using Huge Pages and Performance Counters to Determine the LLC Architecture. Procedia Computer Science, 2013, 18, 2557-2560.	2.0	1
22	An empirical model for predicting cross-core performance interference on multicore processors. , 2013, , .		2
23	Understanding Cache Hierarchy Contention in CMPs to Improve Job Scheduling. , 2012, , .		16