

Alberto L Sangiovanni-Vincentelli

List of Publications by Year in descending order

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398
papers

17,511
citations

34105

52
h-index

30922

102
g-index

409
all docs

409
docs citations

409
times ranked

6535
citing authors

#	ARTICLE	IF	CITATIONS
1	Logic Minimization Algorithms for VLSI Synthesis. Kluwer International Series in Engineering and Computer Science, 1984, , .	0.2	1,404
2	Modeling Cyber-Physical Systems. Proceedings of the IEEE, 2012, 100, 13-28.	21.3	566
3	A framework for comparing models of computation. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1998, 17, 1217-1229.	2.7	480
4	Hardware-Software Co-Design of Embedded Systems. , 1997, , .		479
5	Metropolis: an integrated electronic system design environment. Computer, 2003, 36, 45-52.	1.1	371
6	Design of embedded systems: formal models, validation, and synthesis. Proceedings of the IEEE, 1997, 85, 366-390.	21.3	351
7	Theory of latency-insensitive design. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2001, 20, 1059-1076.	2.7	333
8	DELIGHT.SPICE: an optimization-based system for the design of integrated circuits. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1988, 7, 501-519.	2.7	311
9	Multilevel logic synthesis. Proceedings of the IEEE, 1990, 78, 264-300.	21.3	304
10	Platform-based design and software design methodology for embedded systems. IEEE Design and Test of Computers, 2001, 18, 23-33.	1.0	283
11	Convergence and finite-time behavior of simulated annealing. Advances in Applied Probability, 1986, 18, 747-771.	0.7	268
12	NOVA: state assignment of finite state machines for optimal two-level logic implementation. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1990, 9, 905-924.	2.7	266
13	Quo Vadis, SLD? Reasoning About the Trends and Challenges of System Level Design. Proceedings of the IEEE, 2007, 95, 467-506.	21.3	254
14	Model predictive control with signal temporal logic specifications. , 2014, , .		251
15	Taming Dr. Frankenstein: Contract-Based Design for Cyber-Physical Systems*. European Journal of Control, 2012, 18, 217-238.	2.6	246
16	Combinational test generation using satisfiability. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1996, 15, 1167-1176.	2.7	240
17	Architecture of field-programmable gate arrays. Proceedings of the IEEE, 1993, 81, 1013-1029.	21.3	234
18	MUSTANG: state assignment of finite state machines targeting multilevel logic implementations. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1988, 7, 1290-1300.	2.7	230

#	ARTICLE	IF	CITATIONS
19	Secure State Estimation for Cyber-Physical Systems Under Sensor Attacks: A Satisfiability Modulo Theory Approach. IEEE Transactions on Automatic Control, 2017, 62, 4917-4932.	5.7	219
20	Multi-level logic minimization using implicit don't cares. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1988, 7, 723-740.	2.7	216
21	Driving-Style-Based Codesign Optimization of an Automated Electric Vehicle: A Cyber-Physical System Approach. IEEE Transactions on Industrial Electronics, 2019, 66, 2965-2975.	7.9	195
22	Hardware-software codesign of embedded systems. IEEE Micro, 1994, 14, 26-36.	1.8	194
23	Domain Randomization and Pyramid Consistency: Simulation-to-Real Generalization Without Accessing Target Domain Data. , 2019, , .		177
24	Test generation for sequential circuits. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1988, 7, 1081-1093.	2.7	172
25	Retiming and resynthesis: optimizing sequential networks with combinational techniques. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1991, 10, 74-84.	2.7	158
26	Automation of IC layout with analog constraints. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1996, 15, 923-942.	2.7	156
27	Embedded System Design for Automotive Applications. Computer, 2007, 40, 42-51.	1.1	149
28	A Contract-Based Methodology for Aircraft Electric Power System Design. IEEE Access, 2014, 2, 1-25.	4.2	147
29	A theoretical framework for simulated annealing. Algorithmica, 1991, 6, 302-345.	1.3	142
30	Moving From Federated to Integrated Architectures in Automotive: The Role of Standards, Methods and Tools. Proceedings of the IEEE, 2010, 98, 603-620.	21.3	141
31	Minimizing production test time to detect faults in analog circuits. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1994, 13, 796-813.	2.7	138
32	It usually works: The temporal logic of stochastic systems. Lecture Notes in Computer Science, 1995, , 155-165.	1.3	137
33	Cyber-Security for the Controller Area Network (CAN) Communication Protocol. , 2012, , .		130
34	Optimization of task allocation and priority assignment in hard real-time distributed systems. Transactions on Embedded Computing Systems, 2012, 11, 1-30.	2.9	130
35	A Review of Single-Source Deep Unsupervised Visual Domain Adaptation. IEEE Transactions on Neural Networks and Learning Systems, 2022, 33, 473-493.	11.3	123
36	A LiDAR Point Cloud Generator. , 2018, , .		119

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37	Contracts for System Design. Foundations and Trends in Electronic Design Automation, 2018, 12, 124-400.	1.0	116
38	A Platform-Based Taxonomy for ESL Design. IEEE Design and Test of Computers, 2006, 23, 359-374.	1.0	115
39	A Platform-Based Design Methodology With Contracts and Related Tools for the Design of Cyber-Physical Systems. Proceedings of the IEEE, 2015, 103, 2104-2132.	21.3	114
40	Total and Peak Energy Consumption Minimization of Building HVAC Systems Using Model Predictive Control. IEEE Design and Test of Computers, 2012, 29, 26-35.	1.0	105
41	Applying harmonic balance to almost-periodic circuits. IEEE Transactions on Microwave Theory and Techniques, 1988, 36, 366-378.	4.6	101
42	Scheduling for embedded real-time systems. IEEE Design and Test of Computers, 1998, 15, 71-82.	1.0	101
43	Automatic synthesis of interfaces between incompatible protocols. , 1998, , .		100
44	Coping with latency in SOC design. IEEE Micro, 2002, 22, 24-35.	1.8	100
45	Time-domain non-Monte Carlo noise simulation for nonlinear dynamic circuits with arbitrary excitations. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1996, 15, 493-505.	2.7	99
46	Schedule Optimization of Time-Triggered Systems Communicating Over the FlexRay Static Segment. IEEE Transactions on Industrial Informatics, 2011, 7, 1-17.	11.3	98
47	Convergence and finite-time behavior of simulated annealing. , 1985, , .		97
48	Languages and Tools for Hybrid Systems Design. Foundations and Trends in Electronic Design Automation, 2006, 1, 1-193.	1.0	92
49	A distributed minimum variance estimator for sensor networks. IEEE Journal on Selected Areas in Communications, 2008, 26, 609-621.	14.0	89
50	The Swarm at the Edge of the Cloud. IEEE Design and Test, 2014, 31, 8-20.	1.2	83
51	A synthesis and optimization procedure for fully and easily testable sequential machines. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1989, 8, 1100-1107.	2.7	79
52	Automatic generation of analytical models for interconnect capacitances. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1995, 14, 470-480.	2.7	79
53	Complexity of two-level logic minimization. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2006, 25, 1230-1246.	2.7	79
54	The Tire as an Intelligent Sensor. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2009, 28, 941-955.	2.7	78

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55	Benefits and challenges for platform-based design. , 2004, , .		77
56	A Top-Down, Constraint-Driven Design Methodology for Analog Integrated Circuits. , 1997, , .		72
57	ESPRESSO-SIGNATURE: a new exact minimizer for logic functions. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 1993, 1, 432-440.	3.1	72
58	Irredundant sequential machines via optimal logic synthesis. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1990, 9, 8-18.	2.7	71
59	Delay fault coverage and performance tradeoffs. , 1993, , .		70
60	Formal models for embedded system design. IEEE Design and Test of Computers, 2000, 17, 14-27.	1.0	69
61	Period optimization for hard real-time distributed automotive systems. Proceedings - Design Automation Conference, 2007, , .	0.0	67
62	Stochastic Analysis of CAN-Based Real-Time Automotive Systems. IEEE Transactions on Industrial Informatics, 2009, 5, 388-401.	11.3	64
63	Performance analysis and optimization of latency insensitive systems. , 2000, , .		63
64	Efficient software performance estimation methods for hardware/software codesign. , 1996, , .		60
65	Limitations and challenges of computer-aided design technology for CMOS VLSI. Proceedings of the IEEE, 2001, 89, 341-365.	21.3	60
66	Model predictive control approach to online computation of demand-side flexibility of commercial buildings HVAC systems for Supply Following. , 2014, , .		59
67	Synthesis of embedded software using free-choice Petri nets. , 1999, , .		57
68	Model-Based Hierarchical Optimal Control Design for HVAC Systems. , 2011, , .		57
69	Synthesis of Multitask Implementations of Simulink Models With Minimum Delays. IEEE Transactions on Industrial Informatics, 2010, 6, 637-651.	11.3	55
70	Optimizing the Software Architecture for Extensibility in Hard Real-Time Distributed Systems. IEEE Transactions on Industrial Informatics, 2010, 6, 621-636.	11.3	55
71	<scp>metro</scp> II. Transactions on Embedded Computing Systems, 2013, 12, 1-31.	2.9	54
72	Task generation and compile-time scheduling for mixed data-control embedded software. , 2000, , .		53

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73	Convertibility verification and converter synthesis. IEEE/ACM International Conference on Computer-Aided Design, Digest of Technical Papers, 2002, , .	0.0	52
74	System Level Design for Clustered Wireless Sensor Networks. IEEE Transactions on Industrial Informatics, 2007, 3, 202-214.	11.3	52
75	Design Automation for Smart Building Systems. Proceedings of the IEEE, 2018, 106, 1680-1699.	21.3	52
76	Delay fault coverage, test set size, and performance trade-offs. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1995, 14, 32-44.	2.7	50
77	An embedded system for an eye-detection sensor. Computer Vision and Image Understanding, 2005, 98, 104-123.	4.7	50
78	SMT-Based Observer Design for Cyber-Physical Systems under Sensor Attacks. ACM Transactions on Cyber-Physical Systems, 2018, 2, 1-27.	2.5	50
79	Model matching for finite-state machines. IEEE Transactions on Automatic Control, 2001, 46, 1726-1743.	5.7	49
80	The design of dynamical observers for hybrid systems: Theory and application to an automotive control problem. Automatica, 2013, 49, 915-925.	5.0	49
81	System level design paradigms. ACM Transactions on Design Automation of Electronic Systems, 2006, 11, 537-563.	2.6	48
82	Security-aware mapping for CAN-based real-time distributed automotive systems. , 2013, , .		48
83	Polynomial-Time Verification of PCTL Properties of MDPs with Convex Uncertainties. Lecture Notes in Computer Science, 2013, , 527-542.	1.3	48
84	Constraint-based channel routing for analog and mixed analog/digital circuits. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1993, 12, 497-510.	2.7	47
85	Area routing for analog layout. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1993, 12, 1186-1197.	2.7	47
86	A fully implicit algorithm for exact state minimization. , 1994, , .		47
87	The tides of EDA. IEEE Design and Test of Computers, 2003, 20, 59-75.	1.0	47
88	A 6-Bit 50-MS/s Threshold Configuring SAR ADC in 90-nm Digital CMOS. IEEE Transactions on Circuits and Systems I: Regular Papers, 2012, 59, 80-92.	5.4	47
89	A Constraint-driven Placement Methodology For Analog Integrated Circuits. , 0, , .		47
90	SAT sweeping with local observability don't-cares. , 2006, , .		45

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91	Using Statistical Methods to Compute the Probability Distribution of Message Response Time in Controller Area Network. IEEE Transactions on Industrial Informatics, 2010, 6, 678-691.	11.3	45
92	A case study in computer-aided co-design of embedded controllers. Design Automation for Embedded Systems, 1996, 1, 51-67.	1.0	44
93	Hybrid control in automotive applications: the cut-off control. Automatica, 1999, 35, 519-535.	5.0	43
94	A hierarchical coordination language for interacting real-time tasks. , 2006, , .		42
95	Synthesis method for field programmable gate arrays. Proceedings of the IEEE, 1993, 81, 1057-1083.	21.3	39
96	A macromodeling algorithm for analog circuits. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1991, 10, 150-160.	2.7	38
97	Individual cylinder characteristic estimation for a spark injection engine. Automatica, 2003, 39, 1157-1169.	5.0	38
98	Security-Aware Modeling and Efficient Mapping for CAN-Based Real-Time Distributed Automotive Systems. IEEE Embedded Systems Letters, 2015, 7, 11-14.	1.9	38
99	Substrate optimization based on semi-analytical techniques. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1999, 18, 172-190.	2.7	37
100	Security-Aware Design Methodology and Optimization for Automotive Systems. ACM Transactions on Design Automation of Electronic Systems, 2015, 21, 1-26.	2.6	37
101	Automatic generation of parasitic constraints for performance-constrained physical design of analog circuits. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1993, 12, 208-224.	2.7	36
102	Explicit and implicit algorithms for binate covering problems. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1997, 16, 677-691.	2.7	36
103	An FSM Reengineering Approach to Sequential Circuit Synthesis by State Splitting. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2008, 27, 1159-1164.	2.7	36
104	A mixed frequency-time approach for distortion analysis of switching filter circuits. IEEE Journal of Solid-State Circuits, 1989, 24, 443-451.	5.4	35
105	Duty-cycle optimization for IEEE 802.15.4 wireless sensor networks. ACM Transactions on Sensor Networks, 2013, 10, 1-32.	3.6	35
106	SMC: Satisfiability Modulo Convex Programming. Proceedings of the IEEE, 2018, 106, 1655-1679.	21.3	35
107	Modeling and Designing Heterogeneous Systems. Lecture Notes in Computer Science, 2002, , 228-273.	1.3	35
108	Linear temporal logic motion planning for teams of underactuated robots using satisfiability modulo convex programming. , 2017, , .		33

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109	Latency Insensitive Protocols. Lecture Notes in Computer Science, 1999, , 123-133.	1.3	33
110	Optimum functional decomposition using encoding. , 1994, , .		32
111	A compilation-based software estimation scheme for hardware/software co-simulation. , 1999, , .		32
112	Source-level timing annotation and simulation for a heterogeneous multiprocessor. , 2008, , .		32
113	Scheduling the FlexRay bus using optimization techniques. , 2009, , .		32
114	Optimizing Extensibility in Hard Real-Time Distributed Systems. , 2009, , .		32
115	Metamodeling: An Emerging Representation Paradigm for System-Level Design. IEEE Design and Test of Computers, 2009, 26, 54-69.	1.0	32
116	Model Predictive Control of regulation services from commercial buildings to the smart grid. , 2014, , .		32
117	Designing a Cyber-Physical System for Fall Prevention by Corticospinal Muscular Coupling Detection. IEEE Design and Test, 2016, 33, 66-76.	1.2	32
118	Diagnosis and Repair for Synthesis from Signal Temporal Logic Specifications. , 2016, , .		31
119	Maximizing the stability radius of a set of systems under real-time scheduling constraints. IEEE Transactions on Automatic Control, 2005, 50, 1790-1795.	5.7	30
120	A framework for satisfying input and output encoding constraints. , 1991, , .		29
121	SMC. , 2017, , .		29
122	Fault-tolerant deployment of embedded software for cost-sensitive real-time feedback-control applications. , 0, , .		28
123	Satisfaction of input and output encoding constraints. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1994, 13, 589-602.	2.7	27
124	Fault-Tolerant Distributed Deployment of Embedded Control Software. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2008, 27, 906-919.	2.7	27
125	Composing heterogeneous reactive systems. Transactions on Embedded Computing Systems, 2008, 7, 1-36.	2.9	27
126	Synthesis of embedded networks for building automation and control. , 2008, , .		27

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127	Co-design of control algorithm and embedded platform for building HVAC systems. , 2013, , .		27
128	Optimal load management system for Aircraft Electric Power distribution. , 2013, , .		27
129	Extensible and scalable time triggered scheduling. , 2005, , .		26
130	Uniprocessor scheduling under precedence constraints for embedded systems design. Transactions on Embedded Computing Systems, 2007, 7, 1-30.	2.9	26
131	Sound and complete state estimation for linear dynamical systems under sensor attacks using Satisfiability Modulo Theory solving. , 2015, , .		26
132	Techniques for multilayer channel routing. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1988, 7, 698-712.	2.7	25
133	Electronic-system design in the automobile industry. IEEE Micro, 2003, 23, 8-18.	1.8	25
134	Metamodels in Europe: Languages, Tools, and Applications. IEEE Design and Test of Computers, 2009, 26, 38-53.	1.0	25
135	Online Simultaneous State Estimation and Parameter Adaptation for Building Predictive Control. , 2013, , .		25
136	Codesign Methodologies and Tools for Cyber-Physical Systems. Proceedings of the IEEE, 2018, 106, 1484-1500.	21.3	25
137	Three-dimensional capacitance evaluation on a Connection Machine. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1988, 7, 1125-1133.	2.7	24
138	Boolean decomposition in multilevel logic optimization. IEEE Journal of Solid-State Circuits, 1989, 24, 399-408.	5.4	24
139	Security-aware mapping for TDMA-based real-time distributed systems. , 2014, , .		24
140	Synthesis of hazard-free asynchronous circuits with bounded wire delays. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1995, 14, 61-86.	2.7	23
141	Logic synthesis for manufacturability. IEEE Design and Test of Computers, 2004, 21, 192-199.	1.0	23
142	Loosely time-triggered architectures based on communication-by-sampling. , 2007, , .		23
143	Methodology for the Design of Analog Integrated Interfaces Using Contracts. IEEE Sensors Journal, 2012, 12, 3329-3345.	4.7	23
144	Metronomy. , 2014, , .		23

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145	Scalable lazy SMT-based motion planning. , 2016, , .		23
146	Industrial Cyber-Physical Systems â€œ iCyPhy. , 2014, , 21-37.		23
147	Performance optimization using exact sensitization. , 1994, , .		22
148	Maximal Safe Set Computation for Idle Speed Control of an Automotive Engine. Lecture Notes in Computer Science, 2000, , 32-44.	1.3	22
149	Tag machines. , 2005, , .		22
150	Period Optimization for Hard Real-time Distributed Automotive Systems. Proceedings - Design Automation Conference, 2007, , .	0.0	22
151	Heterogeneous Reactive Systems Modeling and Correct-by-Construction Deployment. Lecture Notes in Computer Science, 2003, , 35-50.	1.3	21
152	Heterogeneous reactive systems modeling. , 2004, , .		21
153	Correct-by-Construction Transformations across Design Environments for Model-Based Embedded Software Development. , 0, , .		21
154	Embedding Mixed-Signal Design in Systems-on-Chip. Proceedings of the IEEE, 2006, 94, 1070-1088.	21.3	21
155	Platform based design for wireless sensor networks. Mobile Networks and Applications, 2006, 11, 469-485.	3.3	21
156	Characterization, Analysis, and Recommendations for Exploiting the Opportunities of Cyber-Physical Systems. , 2017, , 3-14.		21
157	Formal Models for Communication-Based Design. Lecture Notes in Computer Science, 2000, , 29-47.	1.3	21
158	Reduced offsets for minimization of binary-valued functions. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1991, 10, 413-426.	2.7	20
159	Performance optimization of pipelined logic circuits using peripheral retiming and resynthesis. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1993, 12, 568-578.	2.7	20
160	Symbolic two-level minimization. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1997, 16, 692-708.	2.7	20
161	Component-based design for the future. , 2011, , .		20
162	Routing-Aware Design of Indoor Wireless Sensor Networks Using an Interactive Tool. IEEE Systems Journal, 2015, 9, 714-727.	4.6	20

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163	Turning <i>coders</i> into <i>makers</i> . , 2017, , .		20
164	CHASE: Contract-based requirement engineering for cyber-physical system design. , 2018, , .		20
165	ThunderBird: a complete standard cell layout package. IEEE Journal of Solid-State Circuits, 1988, 23, 410-420.	5.4	19
166	Explainable AI for Chiller Fault-Detection Systems: Gaining Human Trust. Computer, 2021, 54, 60-68.	1.1	19
167	Interchange Format for Hybrid Systems: Abstract Semantics. Lecture Notes in Computer Science, 2006, , 491-506.	1.3	19
168	Stochastic Assume-Guarantee Contracts for Cyber-Physical System Design. Transactions on Embedded Computing Systems, 2019, 18, 1-26.	2.9	19
169	CAD tools for ASIC design. Proceedings of the IEEE, 1987, 75, 765-776.	21.3	18
170	Exact minimization of binary decision diagrams using implicit techniques. IEEE Transactions on Computers, 1998, 47, 1282-1296.	3.4	18
171	HILAC: A framework for hardware in the loop simulation and multi-platform automatic code generation of WSN applications. , 2010, , .		18
172	Secure state reconstruction in differentially flat systems under sensor attacks using satisfiability modulo theory solving. , 2015, , .		18
173	A mixed discrete-continuous optimization scheme for Cyber-Physical System architecture exploration. , 2015, , .		18
174	Equivalences for fair Kripke structures. Lecture Notes in Computer Science, 1994, , 364-375.	1.3	18
175	A massively parallel algorithm for three-dimensional device simulation. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1991, 10, 1201-1209.	2.7	17
176	Automated design management using traces. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1993, 12, 1077-1095.	2.7	17
177	Circuit delay models and their exact computation using Timed Boolean Functions. , 1993, , .		17
178	Valid clock frequencies and their computation in wavepipelined circuits. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1996, 15, 791-807.	2.7	17
179	Interchange Formats for Hybrid Systems: Review and Proposal. Lecture Notes in Computer Science, 2005, , 526-541.	1.3	17
180	FSM re-engineering and its application in low power state encoding. , 2005, , .		17

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181	COSI: A Framework for the Design of Interconnection Networks. IEEE Design and Test of Computers, 2008, 25, 402-415.	1.0	17
182	Breath: A Self-Adapting Protocol for Wireless Sensor Networks in Control and Automation. , 2008, , .		17
183	Source-Level Timing Annotation and Simulation for a Heterogeneous Multiprocessor. , 2008, , .		17
184	Minimum Energy coding in CDMA Wireless Sensor Networks. IEEE Transactions on Wireless Communications, 2009, 8, 985-994.	9.2	17
185	A Methodology for Constraint-Driven Synthesis of On-Chip Communications. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2009, 28, 364-377.	2.7	17
186	Development of Building Automation and Control Systems. IEEE Design and Test of Computers, 2012, 29, 45-55.	1.0	17
187	Stochastic contracts for cyber-physical system design under probabilistic requirements. , 2017, , .		17
188	Adaptive Body Area Networks Using Kinematics and Biosignals. IEEE Journal of Biomedical and Health Informatics, 2021, 25, 623-633.	6.3	17
189	Theory of Optimal Control Using Bisimulations. Lecture Notes in Computer Science, 2000, , 89-102.	1.3	17
190	Symbolic minimization of multilevel logic and the input encoding problem. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1992, 11, 825-843.	2.7	16
191	Embedded System Design using UML and Platforms. , 2003, , 119-128.		16
192	Rialto. , 2005, , .		16
193	Improving the Size of Communication Buffers in Synchronous Models With Time Constraints. IEEE Transactions on Industrial Informatics, 2009, 5, 229-240.	11.3	16
194	Optimized Selection of Reliable and Cost-Effective Cyber-Physical System Architectures. , 2015, , .		16
195	Smart Connected Buildings Design Automation: Foundations and Trends. Foundations and Trends in Electronic Design Automation, 2016, 10, 1-143.	1.0	16
196	Detecting and Diagnosing Incipient Building Faults Using Uncertainty Information from Deep Neural Networks. , 2019, , .		16
197	Optimized Selection of Reliable and Cost-Effective Safety-Critical System Architectures. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2020, 39, 2109-2123.	2.7	16
198	Behavioral Representations For VCO And Detectors In Phase-lock Systems. , 0, , .		15

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199	E2RINA: an Energy Efficient and Reliable In-Network Aggregation for Clustered Wireless Sensor Networks. , 2007, , .		15
200	Duty-Cycle Optimization in Unslotted 802.15.4 Wireless Sensor Networks. , 2008, , .		15
201	Compositional Modeling in Metropolis. Lecture Notes in Computer Science, 2002, , 93-107.	1.3	15
202	Logic verification algorithms and their parallel implementation. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1989, 8, 181-189.	2.7	14
203	Circuit structure relations to redundancy and delay. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1994, 13, 875-883.	2.7	14
204	Synthesis for testability techniques for asynchronous circuits. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1995, 14, 1569-1577.	2.7	14
205	A module generator for high-speed CMOS current output digital/analog converters. IEEE Journal of Solid-State Circuits, 1996, 31, 448-451.	5.4	14
206	Platform-based embedded software design and system integration for autonomous vehicles. Proceedings of the IEEE, 2003, 91, 198-211.	21.3	14
207	Synthesis for manufacturability: a sanity check. , 0, , .		14
208	Efficient Solution of Optimal Control Problems Using Hybrid Systems. SIAM Journal on Control and Optimization, 2005, 43, 1923-1952.	2.1	14
209	Robustness in analog systems: Design techniques, methodologies and tools. , 2011, , .		14
210	INDEX: an inductance extractor for superconducting circuits. IEEE Transactions on Applied Superconductivity, 1993, 3, 2629-2632.	1.7	13
211	Efficient methods for embedded system design space exploration. , 2000, , .		13
212	Contract-based system-level composition of analog circuits. , 2009, , .		13
213	Efficient Wire Routing and Wire Sizing for Weight Minimization of Automotive Systems. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2015, 34, 1730-1741.	2.7	13
214	Industrial Control via Application Containers: Migrating from Bare-Metal to IAAS. , 2019, , .		13
215	Characterization of integration algorithms for the timing analysis of mos vlsi circuits. International Journal of Circuit Theory and Applications, 1982, 10, 299-309.	2.0	12
216	Concurrent execution semantics and sequential simulation algorithms for the metropolis meta-model. , 2002, , .		12

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217	A tool for describing and evaluating hierarchical real-time bus scheduling policies. , 2003, , .		12
218	A formal approach to fault tree synthesis for the analysis of distributed fault tolerant systems. , 2005, , .		12
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