

# Tohru Nakamura

## List of Publications by Year in descending order

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32  
papers

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840776

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docs citations

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times ranked

723  
citing authors

#	ARTICLE	IF	CITATIONS
1	Impact of gate electrode formation process on Al <sub>2</sub> O <sub>3</sub> /GaN interface properties and channel mobility. Applied Physics Express, 2021, 14, 081001.	2.4	4
2	Gamma-ray induced photo emission from ZnO single crystal wafer: Comparison with GaN. Solid State Communications, 2021, 336, 114413.	1.9	0
3	Low interface state densities at Al <sub>2</sub> O <sub>3</sub> /GaN interfaces formed on vicinal polar and non-polar surfaces. Applied Physics Letters, 2020, 117, .	3.3	18
4	Electrical properties of GaN metal-insulator-semiconductor field-effect transistors with Al <sub>2</sub> O <sub>3</sub> /GaN interfaces formed on vicinal Ga-polar and nonpolar surfaces. Applied Physics Letters, 2020, 117, .	3.3	12
5	Ion implanted GaN MISFETs fabricated in Mg implanted layers activated by conventional rapid thermal annealing. Nuclear Instruments & Methods in Physics Research B, 2019, 449, 49-53.	1.4	12
6	Fully Ion Implanted Normally-Off GaN DMOSFETs with ALD-Al <sub>2</sub> O <sub>3</sub> Gate Dielectrics. Materials, 2019, 12, 689.	2.9	21
7	Homogeneity evaluation of Mg implanted GaN layer by on-wafer forward diode current mapping. Surface and Coatings Technology, 2018, 355, 7-10.	4.8	7
8	5.0 kV breakdown-voltage vertical GaN p-n junction diodes. Japanese Journal of Applied Physics, 2018, 57, 04FG09.	1.5	88
9	Ion-irradiation damage on GaN p-n junction diodes by inductively coupled plasma etching and its recovery by thermal treatment. Nuclear Instruments & Methods in Physics Research B, 2017, 409, 65-68.	1.4	7
10	High-k Dielectric Passivation for GaN Diode with a Field Plate Termination. Electronics (Switzerland), 2016, 5, 15.	3.1	16
11	1.7-kV and 0.55- $\Omega \cdot \text{cm}^2$ GaN p-n Diodes on Bulk GaN Substrates With Avalanche Capability. IEEE Electron Device Letters, 2016, 37, 161-164.	3.9	153
12	GaN MISFETs Using Tilt Angle Ion Implantation of Magnesium. IEJ Transactions on Electronics, Information and Systems, 2016, 136, 444-448.	0.2	0
13	Formation of definite GaN p-n junction by Mg-ion implantation to n <sup>+</sup> -GaN epitaxial layers grown on a high-quality free-standing GaN substrate. Nuclear Instruments & Methods in Physics Research B, 2015, 365, 168-170.	1.4	85
14	Vertical GaN p-n Junction Diodes With High Breakdown Voltages Over 4 kV. IEEE Electron Device Letters, 2015, 36, 1180-1182.	3.9	195
15	Nitrogen ion implantation isolation technology for normally-off GaN MISFETs on GaN substrate. Physica Status Solidi C: Current Topics in Solid State Physics, 2014, 11, 914-917.	0.8	10
16	High performance normally-off self-aligned metal gate GaN MISFETs on free-standing GaN substrates. Physica Status Solidi C: Current Topics in Solid State Physics, 2014, 11, 918-923.	0.8	3
17	Normally-off GaN MOSFETs with high-k dielectric CeO <sub>2</sub> films deposited by RF sputtering. Physica Status Solidi C: Current Topics in Solid State Physics, 2014, 11, 302-306.	0.8	5
18	High-Breakdown-Voltage and Low-Specific-on-Resistance GaN p-n Junction Diodes on Free-Standing GaN Substrates Fabricated Through Low-Damage Field-Plate Process. Japanese Journal of Applied Physics, 2013, 52, 028007.	1.5	99

#	ARTICLE	IF	CITATIONS
19	Effects of surface micromesas on reverse leakage current in InGaN/GaN Schottky barriers. Journal of Applied Physics, 2012, 112, 044505.	2.5	4
20	High threshold voltage normally-off GaN MISFETs using self-alignment technique. Physica Status Solidi C: Current Topics in Solid State Physics, 2012, 9, 858-860.	0.8	1
21	Over 1.0kV GaN p-n junction diodes on free-standing GaN substrates. Physica Status Solidi (A) Applications and Materials Science, 2011, 208, 1535-1537.	1.8	79
22	55 nm gate ion-implanted GaN-HEMTs on sapphire and Si substrates. Physica Status Solidi C: Current Topics in Solid State Physics, 2011, 8, 2410-2412.	0.8	4
23	Self-aligned silicide gate GaN MISFETs with normally-off operation. Physica Status Solidi C: Current Topics in Solid State Physics, 2011, 8, 2413-2415.	0.8	1
24	Reduction of on-resistance in ion-implanted GaN/AlGaIn/GaN HEMTs with low gate leakage current. Electronics and Communications in Japan, 2010, 93, 19-24.	0.5	3
25	Improvement of Current Gain in Triple Ion Implanted 4H-SiC Bipolar Junction Transistor with Etched Extrinsic Base Regions. IEEJ Transactions on Electronics, Information and Systems, 2010, 130, 2188-2191.	0.2	0
26	Low-Frequency Noise Characteristics in Ion-Implanted GaN-Based HEMTs. IEEE Electron Device Letters, 2008, 29, 827-829.	3.9	6
27	Reduction of On-Resistance in Ion-Implanted GaN/AlGaIn/GaN HEMTs with Low Gate Leakage Current. IEEJ Transactions on Electronics, Information and Systems, 2008, 128, 885-889.	0.2	0
28	Remarkable Reduction of On-Resistance by Ion Implantation in GaN/AlGaIn/GaN HEMTs With Low Gate Leakage Current. IEEE Electron Device Letters, 2007, 28, 939-941.	3.9	25
29	Ti/Al- GaN Reaction Mechanism Forming Low Contact Resistivity. Materials Research Society Symposia Proceedings, 2002, 743, L11.54.1.	0.1	0
30	SMALL-SCALE InGaP/GaAs HETEROJUNCTION BIPOLAR TRANSISTORS FOR HIGH-SPEED AND LOW-POWER INTEGRATED-CIRCUIT APPLICATIONS. Selected Topics in Electornics and Systems, 2001, , 115-136.	0.2	0
31	Characteristics of Shallow Boron-Doped Layers in Si by Rapid Vapor-Phase Direct Doping. Journal of the Electrochemical Society, 1993, 140, 1117-1121.	2.9	16
32	Large GaN p-n Junction Diodes of 3 mm in Diameter on Free-Standing GaN Substrates with High Breakdown Voltage. Materials Science Forum, 0, 717-720, 1299-1302.	0.3	1