Keshab K Parhi

List of Publications by Year in descending order

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234 papers

5,155 citations

34 h-index 133063 59 g-index

234 all docs

234 docs citations

times ranked

234

3621 citing authors

#	Article	IF	CITATIONS
1	Reservoir Computing Using DNA Oscillators. ACS Synthetic Biology, 2022, 11, 780-787.	1.9	12
2	Applicability of Hyperdimensional Computing to Seizure Detection. IEEE Open Journal of Circuits and Systems, 2022, 3, 59-71.	1.4	5
3	Polynomial Multiplication Architecture with Integrated Modular Reduction for R-LWE Cryptosystems. Journal of Signal Processing Systems, 2022, 94, 799-809.	1.4	1
4	DNA Memristors and Their Application to Reservoir Computing. ACS Synthetic Biology, 2022, 11, 2202-2213.	1.9	5
5	Seizure Onset Zone Identification From iEEG: A Review. IEEE Access, 2022, 10, 62535-62547.	2.6	8
6	Predicting Biological Gender and Intelligence From fMRI via Dynamic Functional Connectivity. IEEE Transactions on Biomedical Engineering, 2021, 68, 815-825.	2.5	18
7	A Reconfigurable 74-140Mbps LDPC Decoding System for CCSDS Standard. IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences, 2021, E104.A, 1509-1515.	0.2	2
8	Correction to "Brain-Inspired Computing: Models and Architectures― IEEE Open Journal of Circuits and Systems, 2021, 2, 291-291.	1.4	0
9	Region-Level Functional and Effective Network Analysis of Human Brain During Cognitive Task Engagement. IEEE Transactions on Neural Systems and Rehabilitation Engineering, 2021, 29, 1651-1660.	2.7	7
10	Teaching Digital Signal Processing by Partial Flipping, Active Learning, and Visualization: Keeping Students Engaged With Blended Teaching. IEEE Signal Processing Magazine, 2021, 38, 20-29.	4.6	2
11	Reduced-Complexity Modular Polynomial Multiplication for R-LWE Cryptosystems. , 2021, , .		4
12	Seizure Detection Using Power Spectral Density via Hyperdimensional Computing. , 2021, , .		3
13	Classification of Adolescent Major Depressive Disorder Via Static and Dynamic Connectivity. IEEE Journal of Biomedical and Health Informatics, 2021, 25, 2604-2614.	3.9	20
14	Effect of SSRIs on Resting-State Functional Brain Networks in Adolescents with Major Depressive Disorder. Journal of Clinical Medicine, 2021, 10, 4322.	1.0	6
15	Graph-Theoretic Properties of Sub-Graph Entropy. IEEE Signal Processing Letters, 2021, 28, 135-139.	2.1	2
16	A low-power twiddle factor addressing architecture for split-radix FFT processor. Microelectronics Journal, 2021, 117, 105276.	1.1	2
17	Biological Gender Classification from fMRI via Hyperdimensional Computing. , 2021, , .		5
18	Spectral Features Based Decoding of Task Engagement: The Role of Theta and High Gamma Bands in Cognitive Control., 2021, 2021, 6062-6065.		0

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19	Decoding Human Cognitive Control Using Functional Connectivity of Local Field Potentials., 2021, 2021, 451-454.		1
20	Effect of Modulating fMRI Time-Series on Fluid Ability and Fluid Intelligence for Healthy Humans. , 2021, 2021, 6070-6073.		0
21	LayerPipe: Accelerating Deep Neural Network Training by Intra-Layer and Inter-Layer Gradient Pipelining and Multiprocessor Scheduling. , 2021, , .		3
22	M3U: Minimum Mean Minimum Uncertainty Feature Selection for Multiclass Classification. Journal of Signal Processing Systems, 2020, 92, 9-22.	1.4	3
23	Molecular MUX-Based Physical Unclonable Functions. , 2020, , .		1
24	Brain-Inspired Computing: Models and Architectures. IEEE Open Journal of Circuits and Systems, 2020, 1, 185-204.	1.4	21
25	Probabilistic Hardware Trojan Attacks on Multiple Layers of Reconfigurable Network Infrastructure. Journal of Hardware and Systems Security, 2020, 4, 343-360.	0.8	4
26	Molecular and DNA Artificial Neural Networks via Fractional Coding. IEEE Transactions on Biomedical Circuits and Systems, 2020, 14, 490-503.	2.7	19
27	Classification Using Hyperdimensional Computing: A Review. IEEE Circuits and Systems Magazine, 2020, 20, 30-47.	2.6	99
28	A High-Performance Stochastic LDPC Decoder Architecture Designed via Correlation Analysis. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 5429-5442.	3.5	5
29	Homogeneous and Heterogeneous Feed-Forward XOR Physical Unclonable Functions. IEEE Transactions on Information Forensics and Security, 2020, 15, 2485-2498.	4.5	50
30	Sub-graph entropy based network approaches for classifying adolescent obsessive-compulsive disorder from resting-state functional MRI. NeuroImage: Clinical, 2020, 26, 102208.	1.4	15
31	Fast 2D Convolution Algorithms for Convolutional Neural Networks. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 1678-1691.	3.5	18
32	MUSE: Minimum Uncertainty and Sample Elimination Based Binary Feature Selection. IEEE Transactions on Knowledge and Data Engineering, 2019, 31, 1750-1764.	4.0	11
33	Discriminative Ratio of Spectral Power and Relative Power Features Derived via Frequency-Domain Model Ratio With Application to Seizure Prediction. IEEE Transactions on Biomedical Circuits and Systems, 2019, 13, 645-657.	2.7	11
34	Predicting Male vs. Female from Task-fMRI Brain Connectivity. , 2019, 2019, 4089-4092.		4
35	Not All Feed-Forward MUX PUFs Generate Unique Signatures. , 2019, , .		2
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37	Computing Radial Basis Function Support Vector Machine using DNA via Fractional Coding., 2019,,.		4
38	Effect of Finite Word-Length on SQNR, Area and Power for Real-Valued Serial FFT. , 2019, , .		5
39	RETOUCH: The Retinal OCT Fluid Detection and Segmentation Benchmark and Challenge. IEEE Transactions on Medical Imaging, 2019, 38, 1858-1874.	5.4	139
40	Architecture Optimization and Performance Comparison of Nonce-Misuse-Resistant Authenticated Encryption Algorithms. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 1053-1066.	2.1	10
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42	Training DNA Perceptrons via Fractional Coding. , 2019, , .		2
43	Predicting Tasks from Task-fMRI Using Blind Source Separation. , 2019, , .		1
44	Classification of Major Depressive Disorder from Resting-State fMRI., 2019, 2019, 3511-3514.		16
45	Computing Arithmetic Functions Using Stochastic Logic by Series Expansion. IEEE Transactions on Emerging Topics in Computing, 2019, 7, 44-59.	3.2	54
46	ProTro: A Probabilistic Counter Based Hardware Trojan Attack on FPGA Based MACSec Enabled Ethernet Switch. Lecture Notes in Computer Science, 2019, , 159-175.	1.0	3
47	A Physical Unclonable Function based on Capacitor Mismatch in a Charge-Redistribution SAR-ADC. , 2018, , .		8
48	Function-specific and Enhanced Brain Structural Connectivity Mapping via Joint Modeling of Diffusion and Functional MRI. Scientific Reports, 2018, 8, 4741.	1.6	35
49	Linear-Phase Lattice FIR Digital Filter Architectures Using Stochastic Logic. Journal of Signal Processing Systems, 2018, 90, 791-803.	1.4	8
50	Canonic Composite Length Real-Valued FFT. Journal of Signal Processing Systems, 2018, 90, 1401-1414.	1.4	1
51	Key-Based Dynamic Functional Obfuscation of Integrated Circuits Using Sequentially Triggered Mode-Based Design. IEEE Transactions on Information Forensics and Security, 2018, 13, 79-93.	4.5	36
52	A Serial Commutator Fast Fourier Transform Architecture for Real-Valued Signals. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 1693-1697.	2.2	22
53	Altered Structural Connection Between Hippocampus and Insula in Adolescent Major Depressive Disorder using DTI., 2018,,.		1
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56	Anatomical Biomarkers for Adolescent Major Depressive Disorder from Diffusion Weighted Imaging using SVM Classifier., 2018, 2018, 2740-2743.		10
57	OCT Fluid Segmentation using Graph Shortest Path and Convolutional Neural Network. , 2018, 2018, 3426-3429.		21
58	Classifying Treated vs. Untreated MDD Adolescents from Anatomical Connectivity using Nonlinear SVM., 2018, 2018, 1-4.		3
59	Biomarkers for Adolescent MDD from Anatomical Connectivity and Network Topology Using Diffusion MRI., 2018, 2018, 1152-1155.		6
60	Stochastic Logic Implementations of Polynomials With All Positive Coefficients by Expansion Methods. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 1698-1702.	2.2	8
61	Low-Energy Architectures of Linear Classifiers for IoT Applications using Incremental Precision and Multi-Level Classification. , 2018, , .		1
62	Computing Mathematical Functions using DNA via Fractional Coding. Scientific Reports, 2018, 8, 8312.	1.6	30
63	Incremental-Precision Based Feature Computation and Multi-Level Classification for Low-Energy Internet-of-Things. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2018, 8, 822-835.	2.7	12
64	Reliable PUF-Based Local Authentication With Self-Correction. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2017, 36, 201-213.	1.9	35
65	LLR-Based Successive-Cancellation List Decoder for Polar Codes With Multibit Decision. IEEE Transactions on Circuits and Systems II: Express Briefs, 2017, 64, 21-25.	2.2	28
66	Secure and Reliable XOR Arbiter PUF Design. , 2017, , .		57
67	Computing Polynomials Using Unipolar Stochastic Logic. ACM Journal on Emerging Technologies in Computing Systems, 2017, 13, 1-30.	1.8	18
68	Computing Polynomials with Positive Coefficients using Stochastic Logic by Double-NAND Expansion. , 2017, , .		14
69	FPGA implementation and comparison of AES-GCM and Deoxys authenticated encryption schemes. , 2017, , \cdot		19
70	Fully-Automated Segmentation of Fluid/Cyst Regions in Optical Coherence Tomography Images with Diabetic Macular Edema using Neutrosophic Sets and Graph Algorithms. IEEE Transactions on Biomedical Engineering, 2017, 65, 1-1.	2.5	64
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75	Hierarchical Functional Obfuscation of Integrated Circuits using a Mode-Based Approach. , 2017, , .		7
76	Functional encryption of integrated circuits by key-based hybrid obfuscation. , 2017, , .		3
77	Performance comparison of AES-GCM-SIV and AES-GCM algorithms for authenticated encryption on FPGA platforms. , 2017, , .		9
78	Molecular computation of complex Markov chains with self-loop state transitions. , 2017, , .		0
79	Fully-automated segmentation of fluid regions in exudative age-related macular degeneration subjects: Kernel graph cut in neutrosophic domain. PLoS ONE, 2017, 12, e0186949.	1.1	36
80	Synthesis of correlated bit streams for stochastic computing. , 2016, , .		6
81	Computing hyperbolic tangent and sigmoid functions using stochastic logic. , 2016, , .		13
82	Seizure prediction using long-term fragmented intracranial canine and human EEG recordings. , 2016, , .		6
83	Belief propagation decoding of polar codes using stochastic computing. , 2016, , .		19
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85	Computing RBF Kernel for SVM Classification Using Stochastic Logic. , 2016, , .		15
86	Architectures for Recursive Digital Filters Using Stochastic Computing. IEEE Transactions on Signal Processing, 2016, 64, 3705-3718.	3.2	50
87	Abnormal Neural Oscillations in Schizophrenia Assessed by Spectral Power Ratio of MEG During Word Processing. IEEE Transactions on Neural Systems and Rehabilitation Engineering, 2016, 24, 1148-1158.	2.7	13
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91	Low-Complexity Seizure Prediction From iEEG/sEEG Using Spectral Power and Ratios of Spectral Power. IEEE Transactions on Biomedical Circuits and Systems, 2016, 10, 693-706.	2.7	171
92	Improved BER Performance With Rotated Head Array and 2-D Detector in Two-Dimensional Magnetic Recording. IEEE Transactions on Magnetics, 2016, 52, 1-6.	1.2	3
93	Network analysis of functional brain connectivity in borderline personality disorder using resting-state fMRI. Neurolmage: Clinical, 2016, 11, 302-315.	1.4	79
94	Estimating Delay Differences of Arbiter PUFs Using Silicon Data. , 2016, , .		6
95	Canonic real-valued radix-2n FFT computations. , 2015, , .		2
96	Seizure prediction using cross-correlation and classification. , 2015, , .		4
97	Seizure prediction using polynomial SVM classification. , 2015, 2015, 5748-51.		25
98	Seizure detection using regression tree based feature selection and polynomial SVM classification., 2015, 2015, 6578-81.		22
99	Molecular Sensing and Computing Systems. IEEE Transactions on Molecular, Biological, and Multi-Scale Communications, 2015 , 1 , 249 - 264 .	1.4	24
100	Architectures for stochastic normalized and modified lattice IIR filters., 2015,,.		6
101	Low-Latency Successive-Cancellation List Decoders for Polar Codes With Multibit Decision. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2015, 23, 2268-2280.	2.1	86
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103	An obfuscated radix-2 real FFT architecture. , 2015, , .		6
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107	Lattice FIR digital filter architectures using stochastic computing. , 2015, , .		24
108	Early Seizure Detection Using Neuronal Potential Similarity: A Generalized Low-Complexity and Robust Measure. International Journal of Neural Systems, 2015, 25, 1550019.	3.2	22

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109	Markov chain computations using molecular reactions. , 2015, , .		17
110	Joint brain connectivity estimation from diffusion and functional MRI data., 2015,,.		2
111	Obfuscating DSP Circuits via High-Level Transformations. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2015, 23, 819-830.	2.1	71
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113	Protecting DSP circuits through obfuscation. , 2014, , .		8
114	Seizure detection using wavelet decomposition of the prediction error signal from a single channel of intra-cranial EEG., 2014, 2014, 4443-6.		11
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116	Successive cancellation list polar decoder using log-likelihood ratios., 2014,,.		27
117	Algorithm and architecture for hybrid decoding of polar codes. , 2014, , .		11
118	Canonic real-valued FFT structures. , 2014, , .		5
119	Robust and low complexity algorithms for seizure detection. , 2014, 2014, 4447-50.		6
120	Classification of borderline personality disorder based on spectral power of resting-state fMRI., 2014, 2014, 5036-9.		11
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122	Impulse Noise Correction in OFDM Systems. Journal of Signal Processing Systems, 2014, 74, 245-262.	1.4	4
123	Low-Complexity Welch Power Spectral Density Computation. IEEE Transactions on Circuits and Systems I: Regular Papers, 2014, 61, 172-182.	3.5	73
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125	Low-Latency Successive-Cancellation Polar Decoder Architectures Using 2-Bit Decoding. IEEE Transactions on Circuits and Systems I: Regular Papers, 2014, 61, 1241-1254.	3.5	98
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129	FFT Architectures for Real-Valued Signals Based on Radix-\$2^{3}\$ and Radix-\$2^{4}\$ Algorithms. IEEE Transactions on Circuits and Systems I: Regular Papers, 2013, 60, 2422-2430.	3.5	41
130	Hierarchical Folding and Synthesis of Iterative Data Flow Graphs. IEEE Transactions on Circuits and Systems II: Express Briefs, 2013, 60, 597-601.	2.2	13
131	Multidimensional Analysis of the Abnormal Neural Oscillations Associated With Lexical Processing in Schizophrenia. Clinical EEG and Neuroscience, 2013, 44, 135-143.	0.9	23
132	Schizophrenia classification with single-trial MEG during language processing., 2013,,.		0
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134	Comments on "Low-energy CSMT carry generators and binary adders". IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2013, 21, 791-791.	2.1	2
135	Discrete-Time Signal Processing with DNA. ACS Synthetic Biology, 2013, 2, 245-254.	1.9	77
136	Pipelined Architectures for Real-Valued FFT and Hermitian-Symmetric IFFT With Real Datapaths. IEEE Transactions on Circuits and Systems II: Express Briefs, 2013, 60, 507-511.	2.2	39
137	An In-Place FFT Architecture for Real-Valued Signals. IEEE Transactions on Circuits and Systems II: Express Briefs, 2013, 60, 652-656.	2.2	52
138	Design and Optimization of Multiplierless FIR Filters Using Sub-Threshold Circuits. Journal of Signal Processing Systems, 2013, 70, 259-274.	1.4	1
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140	Architectures for digital filters using stochastic computing. , 2013, , .		56
141	Architecture optimizations for BP polar decoders. , 2013, , .		85
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143	Automated localization of cysts in diabetic macular edema using optical coherence tomography images., 2013, 2013, 1426-9.		27
144	Screening fundus images for diabetic retinopathy. , 2012, , .		15

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145	Low complexity algorithm for seizure prediction using Adaboost. , 2012, 2012, 1061-4.		26
146	Optimized Joint Timing Synchronization and Channel Estimation for OFDM Systems. IEEE Wireless Communications Letters, 2012, $1,149-152$.	3.2	18
147	Verifying equivalence of digital signal processing circuits. , 2012, , .		10
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149	A Network-Efficient Nonbinary QC-LDPC Decoder Architecture. IEEE Transactions on Circuits and Systems I: Regular Papers, 2012, 59, 1359-1371.	3.5	9
150	Digital Signal Processing With Molecular Reactions. IEEE Design and Test of Computers, 2012, 29, 21-31.	1.4	23
151	Pipelined Parallel FFT Architectures via Folding Transformation. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2012, 20, 1068-1081.	2.1	140
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154	Architecture Optimizations for the RSA Public Key Cryptosystem: A Tutorial. IEEE Circuits and Systems Magazine, 2011, 11, 24-34.	2.6	16
155	High-Speed Parallel Architectures for Linear Feedback Shift Registers. IEEE Transactions on Signal Processing, 2011, 59, 4459-4469.	3.2	54
156	Power Reduction in Frequency-Selective FIR Filters Under Voltage Overscaling. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2011, 1, 343-356.	2.7	10
157	Seizure prediction with spectral power of EEG using cost-sensitive support vector machines. Epilepsia, 2011, 52, 1761-1770.	2.6	341
158	Secure Variable Data Rate Transmission. IEEE Transactions on Circuits and Systems II: Express Briefs, 2011, 58, 100-104.	2.2	3
159	Synchronous sequential computation with molecular reactions. , 2011, , .		35
160	Asynchronous computation with molecular reactions. , 2011, , .		4
161	Fast Reconfigurable Elliptic Curve Cryptography Acceleration for GF(2 m) on 32 bit Processors. Journal of Signal Processing Systems, 2010, 60, 31-45.	1.4	15
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164	Parallel - pipelined radix-2 ² FFT architecture for real valued signals. , 2010, , .		8
165	Improved approach for calculating model parameters in speaker recognition using Gaussian Mixture Models. , 2010, , .		0
166	Novel Variable length Teager Energy Based features for person recognition from their hum. , 2010, , .		14
167	Efficient parallel VLSI architecture for linear feedback shift registers. , 2010, , .		14
168	Computation Error Analysis in Digital Signal Processing Systems With Overscaled Supply Voltage. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2010, 18, 517-526.	2.1	59
169	Min-Sum Decoder Architectures With Reduced Word Length for LDPC Codes. IEEE Transactions on Circuits and Systems I: Regular Papers, 2010, 57, 105-115.	3.5	55
170	Digital signal processing with biomolecular reactions. , 2010, , .		3
171	A synthesis flow for digital signal processing with biomolecular reactions. , 2010, , .		12
172	Underdetermined blind source separation based on Continuous Density Hidden Markov Models. , 2010, , .		2
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175	Probabilistic Spherical Detection and VLSI Implementation for Multiple-Antenna Systems. IEEE Transactions on Circuits and Systems I: Regular Papers, 2009, 56, 685-698.	3.5	9
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179	Low-Latency Low-Complexity Architectures for Viterbi Decoders. IEEE Transactions on Circuits and Systems I: Regular Papers, 2009, 56, 2315-2324.	3 . 5	8
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183	Novel FEXT Cancellation and Equalization for High Speed Ethernet Transmission. IEEE Transactions on Circuits and Systems I: Regular Papers, 2009, 56, 1272-1285.	3.5	13
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185	Multi-delay block frequency domain adaptive filters with sparse partial subblock update. , 2009, , .		2
186	Sparse severe error removal in OFDM demodulators for erasure channels., 2009,,.		2
187	High speed VLSI architecture for general linear feedback shift register (LFSR) structures. , 2009, , .		10
188	High-Speed VLSI Implementation of 2-D Discrete Wavelet Transform. IEEE Transactions on Signal Processing, 2008, 56, 393-403.	3.2	76
189	Area efficient controller design of barrel shifters for reconfigurable LDPC decoders. , 2008, , .		9
190	Hardware Efficient Low-Latency Architecture for High Throughput Rate Viterbi Decoders. IEEE Transactions on Circuits and Systems II: Express Briefs, 2008, 55, 1254-1258.	2.2	13
191	Low-Complexity Echo and NEXT Cancellers for High-Speed Ethernet Transceivers. IEEE Transactions on Circuits and Systems I: Regular Papers, 2008, 55, 2827-2840.	3. 5	5
192	Further cost reduction of adaptive echo and next cancellers for high-speed Ethernet transceivers. , 2008, , .		1
193	Minimal complexity low-latency architectures for Viterbi decoders. , 2008, , .		3
194	High-speed implementation of Smith-Waterman algorithm for DNA sequence scanning in VLSI., 2008,,.		1
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196	Optimally quantized offset min-sum algorithm for flexible LDPC decoder. , 2008, , .		9
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200	Low- Cost Parallel FIR Filter Structures With 2-Stage Parallelism. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2007, 54, 280-290.	0.1	39
201	Design of a Sample-Rate Converter From CD to DAT Using Fractional Delay Allpass Filter. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2007, 54, 19-23.	2.3	13
202	High-Speed Architecture Design of Tomlinson–Harashima Precoders. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2007, 54, 1929-1937.	0.1	18
203	Efficient Highly-Parallel Decoder Architecture for Quasi-Cyclic Low-Density Parity-Check Codes., 2007,,.		12
204	Pipelined Parallel Decision-Feedback Decoders for High-Speed Ethernet Over Copper. IEEE Transactions on Signal Processing, 2007, 55, 707-715.	3.2	0
205	Low-Cost Fast VLSI Algorithm for Discrete Fourier Transform. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2007, 54, 791-806.	0.1	37
206	High-Throughput VLSI Architecture for FFT Computation. IEEE Transactions on Circuits and Systems II: Express Briefs, 2007, 54, 863-867.	2.2	59
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208	Adaptive Tap Management in Multi-Gigabit Echo& Next Cancellers. Signal Processing Systems Design and Implementation (siPS), IEEE Workshop on, 2006, , .	0.0	3
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