

Jiajia Li

List of Publications by Year in descending order

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Version: 2024-02-01

29
papers

533
citations

1307594

7
h-index

1372567

10
g-index

29
all docs

29
docs citations

29
times ranked

343
citing authors

#	ARTICLE	IF	CITATIONS
1	Evaluating Modern GPU Interconnect: PCIe, NVLink, NV-SLI, NVSwitch and GPUDirect. IEEE Transactions on Parallel and Distributed Systems, 2020, 31, 94-110.	5.6	104
2	HiCOO: Hierarchical Storage of Sparse Tensors. , 2018, , .		51
3	An input-adaptive and in-place approach to dense tensor-times-matrix multiply. , 2015, , .		35
4	Model-Driven Sparse CP Decomposition for Higher-Order Tensors. , 2017, , .		31
5	A pattern based algorithmic autotuner for graph processing on GPUs. , 2019, , .		30
6	Load-Balanced Sparse MTTKRP on GPUs. , 2019, , .		29
7	SMAT. ACM SIGPLAN Notices, 2013, 48, 117-126.	0.2	27
8	Design and Implementation of Adaptive SpMV Library for Multicore and Many-Core Architecture. ACM Transactions on Mathematical Software, 2018, 44, 1-25.	2.9	26
9	Efficient and effective sparse tensor reordering. , 2019, , .		25
10	An efficient mixed-mode representation of sparse tensors. , 2019, , .		23
11	Optimizing Sparse Tensor Times Matrix on Multi-core and Many-Core Architectures. , 2016, , .		19
12	Optimizing sparse tensor times matrix on GPUs. Journal of Parallel and Distributed Computing, 2019, 129, 99-109.	4.1	19
13	Bridging the gap between deep learning and sparse matrix format selection. ACM SIGPLAN Notices, 2018, 53, 94-108.	0.2	17
14	Sparta. , 2021, , .		12
15	An Initial Characterization of the Emu Chick. , 2018, , .		10
16	PASTA: a parallel sparse tensor algorithm benchmark suite. CCF Transactions on High Performance Computing, 2019, 1, 111-130.	1.7	10
17	An optimized large-scale hybrid DGEMM design for CPUs and ATI GPUs. , 2012, , .		9
18	Introducing high performance computing concepts into engineering undergraduate curriculum. , 2015, , .		9

#	ARTICLE	IF	CITATIONS
19	Understanding the GPU Microarchitecture to Achieve Bare-Metal Performance Tuning. ACM SIGPLAN Notices, 2017, 52, 31-43.	0.2	9
20	A Survey: Handling Irregularities in Neural Network Acceleration with FPGAs. , 2021, , .		7
21	A microbenchmark characterization of the Emu chick. Parallel Computing, 2019, 87, 60-69.	2.1	5
22	Athena. , 2021, , .		5
23	On the Feasibility of Using Reduced-Precision Tensor Core Operations for Graph Analytics. , 2020, , .		5
24	Efficient Parallel Sparse Symmetric Tucker Decomposition for High-Order Tensors. , 2021, , 193-204.		3
25	Programming Strategies for Irregular Algorithms on the Emu Chick. ACM Transactions on Parallel Computing, 2020, 7, 1-25.	1.4	3
26	A parallel sparse tensor benchmark suite on CPUs and GPUs. , 2020, , .		3
27	A Sparse Tensor Benchmark Suite for CPUs and GPUs. , 2020, , .		3
28	Automatically Tuned Dynamic Programming with an Algorithm-by-Blocks. , 2010, , .		2
29	An Autotuning Protocol to Rapidly Build Autotuners. ACM Transactions on Parallel Computing, 2019, 5, 1-25.	1.4	2