

Encarnacion Castillo

List of Publications by Year in descending order

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78
papers

768
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759233

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79
all docs

79
docs citations

79
times ranked

739
citing authors

#	ARTICLE	IF	CITATIONS
1	Properties of silver chloride and carbon screen printed patterns on different textiles. Textile Research Journal, 2022, 92, 2711-2718.	2.2	1
2	Table-Free Seed Generation for Hardware Newtonâ€™ Raphson Square Root and Inverse Square Root Implementations in IoT Devices. IEEE Internet of Things Journal, 2022, 9, 6985-6995.	8.7	5
3	Highly Reliable Quadruple-Node Upset-Tolerant D-Latch. IEEE Access, 2022, 10, 31836-31850.	4.2	9
4	Power-Efficient Implementation of Ternary Neural Networks in Edge Devices. IEEE Internet of Things Journal, 2022, 9, 20111-20121.	8.7	1
5	Dracon: An Open-Hardware Based Platform for Single-Chip Low-Cost Reconfigurable IoT Devices. Electronics (Switzerland), 2022, 11, 2080.	3.1	4
6	RESEKRA: Remote Enrollment Using SEaled Keys for Remote Attestation. Sensors, 2022, 22, 5060.	3.8	2
7	Gesture Recognition With Ultrasounds and Edge Computing. IEEE Access, 2021, 9, 38999-39008.	4.2	11
8	Low-Cost Soft Error Robust Hardened D-Latch for CMOS Technology Circuit. Electronics (Switzerland), 2021, 10, 1256.	3.1	3
9	Cellulose nanofibers as substrate for flexible and biodegradable moisture sensors. Composites Science and Technology, 2021, 208, 108738.	7.8	44
10	Object Positioning Algorithm Based on Multidimensional Scaling and Optimization for Synthetic Gesture Data Generation. Sensors, 2021, 21, 5923.	3.8	2
11	Air-Writing Character Recognition with Ultrasonic Transceivers. Sensors, 2021, 21, 6700.	3.8	6
12	Window Polarization in PCA-based Analysis of Non-Invasive Fetal ECG recordings. , 2021, , .		0
13	Cost-Effective Printed Electrodes Based on Emerging Materials Applied to Biosignal Acquisition. IEEE Access, 2020, 8, 127789-127800.	4.2	12
14	Privacy-enabled system based on Elliptic Curve Cryptography to reduce risks of contagion in pandemics. , 2020, , .		1
15	An Optimized Measurement Algorithm for Gas Sensors Based on Carbon Nanotubes: Optimizing Sensor Performance and Hardware Resources. IEEE Internet of Things Journal, 2019, 6, 9140-9146.	8.7	1
16	Inexpensive and flexible nanographene-based electrodes for ubiquitous electrocardiogram monitoring. Npj Flexible Electronics, 2019, 3, .	10.7	35
17	Wearable System for Biosignal Acquisition and Monitoring Based on Reconfigurable Technologies. Sensors, 2019, 19, 1590.	3.8	12
18	Control System in Open-Source FPGA for a Self-Balancing Robot. Electronics (Switzerland), 2019, 8, 198.	3.1	15

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19	Efficient Elliptic Curve Cryptoprocessor for enabling TLS protocol in low-cost reconfigurable SoCs. , 2019, , .		0
20	Efficient Implementation on Low-Cost SoC-FPGAs of TLSv1.2 Protocol with ECC_AES Support for Secure IoT Coordinators. Electronics (Switzerland), 2019, 8, 1238.	3.1	4
21	Elliptic Curve Cryptography hardware accelerator for high-performance secure servers. Journal of Supercomputing, 2019, 75, 1107-1122.	3.6	15
22	Design guidelines of laser reduced graphene oxide conformal thermistor for IoT applications. Sensors and Actuators A: Physical, 2018, 274, 148-154.	4.1	35
23	Non-Intrusive Tank-Filling Sensor Based on Sound Resonance. Electronics (Switzerland), 2018, 7, 378.	3.1	5
24	Why Use RF Energy Harvesting in Smart Grids. , 2018, , .		3
25	A clustering-based method for single-channel fetal heart rate monitoring. PLoS ONE, 2018, 13, e0199308.	2.5	30
26	Unified Compact ECC-AES Co-Processor with Group-Key Support for IoT Devices in Wireless Sensor Networks. Sensors, 2018, 18, 251.	3.8	20
27	Reconfigurable electronics: Addressing the uncontrolled increase of waste electrical and electronic equipment. Resources, Conservation and Recycling, 2018, 138, 47-48.	10.8	10
28	Reconfigurable instrument for measuring variations of capacitor's dielectric: an application to olive oil quality monitoring. , 2018, , .		0
29	Wearable biosignal acquisition system for decision aid. , 2018, , .		1
30	Classification Algorithms for Fetal QRS Extraction in Abdominal ECG Signals. Lecture Notes in Computer Science, 2017, , 524-535.	1.3	3
31	Reconfigurable wearable to monitor physiological variables and movement. , 2017, , .		0
32	The driving regulators of the connectivity protein network of brain malignancies. , 2017, , .		4
33	Dynamical graph theory networks techniques for the analysis of sparse connectivity networks in dementia. , 2017, , .		6
34	A new area-efficient BCD-digit multiplier. , 2017, 62, 1-10.		10
35	Image processing methods to evaluate tomato and zucchini damage in post-harvest stages. International Journal of Agricultural and Biological Engineering, 2017, 10, 126-133.	0.6	3
36	COST-EFFECTIVE TEACHING IN THE NANOTECHNOLOGY: MULTIPLE-LANGUAGES APPLIED TO VIRTUAL LESSONS AT THE NANOSCALE. , 2017, , .		0

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37	USING LEARNING OBJECTS TO CREATE SEMANTICALLY ENRICHED CONTENT TO SHARE KNOWLEDGE AND CREATE COMMUNITIES IN E-LEARNING SYSTEMS. , 2017, , .		0
38	PROJECT-BASED LEARNING IN FPGA. , 2017, , .		0
39	SIGN-LANGUAGE INCORPORATION TO NANOTECHNOLOGY VIRTUAL LABORATORIES. , 2017, , .		0
40	Hardware Activation by Means of PUFs and Elliptic Curve Cryptography in Field-Programmable Devices. Electronics (Switzerland), 2016, 5, 5.	3.1	6
41	Comments on "Fast architecture for decimal digit multiplication". Microprocessors and Microsystems, 2016, 47, 441-444.	2.8	1
42	Evaluation of a reconfigurable portable instrument for copper determination based on luminescent carbon dots. Analytical and Bioanalytical Chemistry, 2016, 408, 3013-3020.	3.7	25
43	Parametrized ECT processing over FPGA for a reconfigurable application. , 2015, , .		1
44	Design Time Optimization for Hardware Watermarking Protection of HDL Designs. Scientific World Journal, The, 2015, 2015, 1-14.	2.1	2
45	Towards Project-Based Learning applied to the Electronic Engineering studies. , 2015, , .		7
46	Adaptative ECT system based on reconfigurable electronics. Measurement: Journal of the International Measurement Confederation, 2015, 74, 238-245.	5.0	9
47	Improvements for the applicability of power-watermarking to embedded IP cores protection: e-coreIPP. , 2015, 44, 110-122.		8
48	Number Systems. Intelligent Systems Reference Library, 2014, , 1-70.	1.2	0
49	Galois Fields $GF(p^n)$. Intelligent Systems Reference Library, 2014, , 271-302.	1.2	0
50	Algebraic Circuits. Intelligent Systems Reference Library, 2014, , .	1.2	2
51	Efficient wavelet-based ECG processing for single-lead FHR extraction. , 2013, 23, 1897-1909.		59
52	An application of reconfigurable technologies for non-invasive fetal heart rate extraction. Medical Engineering and Physics, 2013, 35, 1005-1014.	1.7	25
53	Noise Suppression in ECG Signals through Efficient One-Step Wavelet Processing Techniques. Journal of Applied Mathematics, 2013, 2013, 1-13.	0.9	42
54	A reconstruction method for electrical capacitance tomography based on image fusion techniques. , 2012, 22, 885-893.		18

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55	Energy optimization of Application-Specific Instruction-Set Processors by using hardware accelerators in semicustom ICs technology. <i>Microprocessors and Microsystems</i> , 2012, 36, 127-137.	2.8	7
56	Wavelets for full reconfigurable ECG acquisition system. , 2011, , .		0
57	Intellectual property protection (IPP) using obfuscation in C, VHDL, and Verilog coding. <i>Proceedings of SPIE</i> , 2011, , .	0.8	8
58	An Imaging Method for Electrical Capacitance Tomography Based on Projections Multiplication. <i>Journal of Physics: Conference Series</i> , 2011, 307, 012032.	0.4	2
59	Flexible ECG acquisition system based on analog and digital reconfigurable devices. <i>Sensors and Actuators A: Physical</i> , 2011, 165, 261-270.	4.1	34
60	Watermarking strategies for IP protection of micro-processor cores. , 2010, , .		3
61	Nios II hardware acceleration of the epsilon quadratic sieve algorithm. <i>Proceedings of SPIE</i> , 2010, , .	0.8	2
62	Protection of microprocessor-based cores for FPL devices. , 2010, , .		1
63	Ring oscillators as thermal sensors in FPGAs: Experiments in low voltage. , 2010, , .		35
64	Automated Signature Insertion in Combinational Logic Patterns for HDL IP Core Protection. , 2008, , .		15
65	FPGA based Architecture for Robust Optical Flow Computation. , 2008, , .		0
66	Enhancing ADC resolution through Field Programmable Analog Array dynamic reconfiguration. , 2008, , .		6
67	HDL-level automated watermarking of IP cores. , 2008, , .		0
68	Intellectual property protection of IP cores through high-level watermarking. , 2007, , .		4
69	Exploiting Analog and Digital Reconfiguration for Smart Sensor Interfacing. , 2007, , .		1
70	Intellectual Property Protection of HDL IP Cores Through Automated Signature Hosting. , 2007, , .		0
71	Digital Signature Embedding Technique for IP Core Protection. , 2007, , .		3
72	IPP@HDL: Efficient Intellectual Property Protection Scheme for IP Cores. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2007, 15, 578-591.	3.1	110

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73	IPP Watermarking Technique for IP Core Protection on FPL Devices. , 2006, , .		4
74	Custom instruction set NIOS-based OFDM processor for FPGAs. , 2006, , .		2
75	Faster than the FFT: The chirp-z RAG-n Discrete Fast Fourier Transform. Frequenz, 2006, 60, .	0.9	5
76	Efficient Clock Distribution Scheme for VLSI RNS-Enabled Controllers. Lecture Notes in Computer Science, 2005, , 657-665.	1.3	0
77	Intellectual Property Protection for RNS Circuits on FPGAs. Lecture Notes in Computer Science, 2004, , 1139-1141.	1.3	6
78	Watermarking strategies for RNS-based system intellectual property protection. , 0, , .		2