Fadi N Sibai

List of Publications by Year
in descending order

89 papers	394 citations	1307594 7 h-index	1058476 14 g-index
89 all docs	89 docs citations	89 times ranked	202 citing authors

#	Article	IF	Citations
1	Power optimization and procedural sizing of photovoltaic arrays. Energy Systems, 2024, 15, 543-564.	3.0	1
2	Hardware Acceleration of the STRIKE String Kernel Algorithm for Estimating Protein to Protein Interactions. IEEE/ACM Transactions on Computational Biology and Bioinformatics, 2022, 19, 2272-2283.	3.0	2
3	Home Automation Using Augmented Reality (HAAR). Wireless Personal Communications, 2022, 124, 1525-1555.	2.7	3
4	Artificial intelligence and legal liability: towards an international approach of proportional liability based on risk sharing. Information and Communications Technology Law, 2021, 30, 169-192.	1.5	6
5	Recent Advancements in Fuzzy C-means Based Techniques for Brain MRI Segmentation. Current Medical Imaging, 2021, 17, 917-930.	0.8	9
6	AI Crimes: A Classification. , 2020, , .		3
7	Power, Voltage, and Current Characteristics of Photovoltaic Modules in Saudi Arabian Cities. , 2020, , .		2
8	Deployment and Analysis of a Hybrid Shared/Distributed-Memory Parallel Visualization Tool for 3-D Oil Reservoir Grid on OpenStack Cloud Computing. IEEE Access, 2020, 8, 212280-212297.	4.2	1
9	PMSMC: Priority-based Multi-requestor Scheduler for Embedded System Memory Controller. Journal of Parallel and Distributed Computing, 2020, 139, 135-147.	4.1	1
10	Optimizing Particle Systems through CUDA-Assisted Multithreading. WSEAS Transactions on Systems and Control, 2020, 15, 691-698.	0.8	0
11	Probabilistic Modeling and Study of Cybersecurity Attacks in Industrial Control Systems of Plants. , 2020, , .		2
12	Parallel Study of 3-D Oil Reservoir Data Visualization Tool Using Hybrid Distributed/Shared-Memory Models. , 2018, , .		4
13	Improving facial recognition accuracy by applying liveness monitoring technique. , 2015, , .		4
14	A smart data logger for enhancing data communication in Wi-Fi based mobile systems. , 2015, , .		4
15	Parallel PPI Prediction Performance Study on HPC Platforms. Journal of Circuits, Systems and Computers, 2015, 24, 1550074.	1.5	3
16	V-SET CACHE: AN EFFICIENT ADAPTIVE SHARED CACHE FOR MULTI-CORE PROCESSORS. Journal of Circuits, Systems and Computers, 2014, 23, 1450095.	1.5	2
17	Performance evaluation and comparison of parallel conjugate gradient on modern multi-core accelerator and massively parallel systems. International Journal of Parallel, Emergent and Distributed Systems, 2014, 29, 38-67.	1.0	2
18	Performance modeling and analysis of parallel Gaussian elimination on multi-core computers. Journal of King Saud University - Computer and Information Sciences, 2014, 26, 41-54.	3.9	2

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19	Ear recognition with feed-forward artificial neural networks. Neural Computing and Applications, 2013, 23, 1265-1273.	5.6	22
20	On level-1 cache locking for high-performance low-power real-time multicore systems. Computers and Electrical Engineering, 2013, 39, 1333-1345.	4.8	2
21	Performance and power comparisons of MPI Vs Pthread implementations on multicore systems. , 2013, , .		3
22	Evaluation of indoor mobile robot localization techniques. , 2012, , .		6
23	Throughput and latency analysis of the Spidergon-Donut interconnection network. , 2012, , .		1
24	V-Set Cache Design for LLC of Multi-core Processors. , 2012, , .		2
25	A Two-Dimensional Low-Diameter Scalable On-Chip Network for Interconnecting Thousands of Cores. IEEE Transactions on Parallel and Distributed Systems, 2012, 23, 193-201.	5.6	20
26	Design and construction of an in-pipe robot for inspection and maintenance. , 2012, , .		4
27	Cloud computing: A cloudy future?. , 2012, , .		0
28	Parallel Orthomin Equation Solver on the Cell Broadband Engine. , 2011, , .		0
29	Recognizing characters in Saudi License Plates using character boundaries. , 2011, , .		6
30	Improved neural network-based recognition of irises with sector and block partitioning. , 2011, , .		0
31	Design and evaluation of low latency interconnection networks for real-time many-core embedded systems. Computers and Electrical Engineering, 2011, 37, 958-972.	4.8	8
32	Iris recognition using artificial neural networks. Expert Systems With Applications, 2011, 38, 5940-5946.	7.6	57
33	Image processing applications performance study on Cell BE and Blue Gene/L. Concurrency Computation Practice and Experience, 2011, 23, 351-371.	2.2	4
34	Parallelization and performance comparison of the conjugate gradient equation solver on multicore Cell and Xeon computers. Concurrency Computation Practice and Experience, 2011, 23, 2463-2467.	2.2	0
35	Conotoxin protein classification using pairwise comparison and amino acid composition. , 2011, , .		4
36	Impact of transmission-related parameters on the energy performance of cluster-based routing protocols for wireless sensor networks. , 2011, , .		3

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37	A dynamic way cache locking scheme to improve the predictability of power-aware embedded systems. , 2011, , .		3
38	The distance-to-mean broadcast method for vehicular wireless communication systems. , 2011, , .		11
39	Detecting matrix multiplication faults in many-core systems. , 2011, , .		0
40	STRIKE: A Protein–Protein Interaction Classification Approach. Advances in Experimental Medicine and Biology, 2011, 696, 263-270.	1.6	1
41	Improving cache locking performance of modern embedded systems via the addition of a miss table at the L2 cache level. Journal of Systems Architecture, 2010, 56, 151-162.	4.3	18
42	Low Diameter Unicast On-Chip Interconnection Networks for Many-Core Embedded Systems. , 2010, , .		1
43	Simulation and Performance Analysis of Multi-core Thread Scheduling and Migration Algorithms. , 2010, , .		4
44	Parallel protein sequence matching on multicore computers. , 2010, , .		1
45	Which On-Chip Interconnection Network for 16-core MPSoCs?. , 2010, , .		1
46	Investigating cache parameters and locking in predictable and low power embedded systems. , 2010, , .		1
47	On the design of low-power cache memories for homogeneous multi-core processors. , 2010, , .		7
48	Thermal, power, and performance shaping of multicore floorplans. , 2010, , .		1
49	Evaluation of the impact of Miss Table and victim caches in parallel embedded systems. , 2010, , .		3
50	Fleet management automation using the global positioning system. , 2009, , .		8
51	Resource Sharing in Networks-on-Chip of Large Many-core Embedded Systems. , 2009, , .		5
52	Performance and energy consumption optimization by tuning level-2 cache attributes for real-time signal processing systems. , 2009, , .		2
53	Parallel Video Surveillance on the Multi-core Cell Broadband Engine. , 2009, , .		1
54	Teaching page replacement algorithms with a Java-based VM simulator. , 2009, , .		1

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55	Impact of level-2 cache sharing on the performance and power requirements of homogeneous multicore embedded systems. Microprocessors and Microsystems, 2009, 33, 388-397.	2.8	6
56	Conceptual modeling of petascale computer systems. , 2009, , .		0
57	Automatic processing of Arabic text. , 2009, , .		3
58	Impact of L1 entire locking and L2 way locking on the performance, power consumption, and predictability of multicore real-time systems. , 2009, , .		8
59	Highly parallel image processing on the STI Cell. , 2009, , .		1
60	Parallelization and Performance Analysis of an IMPES-based Oil-Water Reservoir Simulator. , 2009, , .		2
61	Parallel Simulation of Oil Reservoirs on a Multi-core Stream Computer. Lecture Notes in Computer Science, 2009, , 82-98.	1.3	3
62	Parallelization and Performance Evaluation of an Edge Detection Algorithm on a Streaming Multi-Core Engine. Journal of Information Technology Research, 2009, 2, 81-91.	0.5	1
63	On the performance benefits of sharing and privatizing second and third-level cache memories in homogeneous multi-core architectures. Microprocessors and Microsystems, 2008, 32, 405-412.	2.8	6
64	Adapting the Hyper-Ring Interconnect for Many-Core Processors. , 2008, , .		4
65	Image magnification and reduction using high order filtering on the cell broadband engine. , 2008, , .		9
66	Gauging the OpenSourceMark Benchmark in Measuring CPU Performance. , 2008, , .		0
67	Parallel edge detection on the multi-core cell broadband engine. , 2008, , .		3
68	Secure password detection with artificial neural networks. , 2008, , .		3
69	Area-efficient floorplans and interconnects for homogeneous multi-core architectures. International Journal of High Performance Systems Architecture, 2008, 1, 155.	0.3	3
70	A Cross-Layer Design Framework using Functional Components. International Journal of Business Data Communications and Networking, 2008, 4, 24-35.	0.7	0
71	Evaluating the performance of single and multiple core processors with PCMARKÂ®05 and benchmark analysis. Performance Evaluation Review, 2008, 35, 62-71.	0.6	10
72	Performance Effect of Localized Thread Schedules in Heterogeneous Multi-Core Processors. , 2007, , .		2

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73	3D Graphics Performance Scaling and Workload Decomposition and Analysis. , 2007, , .		4
74	A Functional Component Based Framework for Cross-Layer Design. , 2007, , .		5
75	On Making Intelligence Performance-Inconspicuous in 3D Games. , 2007, , .		0
76	Evaluation of I-Cache Locking Technique for Real-Time Embedded Systems. , 2007, , .		12
77	Development of a Virtual Memory Simulator to Analyze the Goodness of Page Replacement Algorithms. , 2007, , .		1
78	License Plate Localization in ALPR Systems. , 2007, , .		6
79	Performance analysis and workload characterization of the 3DMark05 benchmark on modern parallel computer platforms. Computer Architecture News, 2007, 35, 44-52.	2.5	3
80	Dissecting the PCMarkÂ®05 Benchmark and Assessing Performance Scaling. , 2006, , .		3
81	Optimal Clustering of Hierarchical Hyper-Ring Multicomputers. Journal of Supercomputing, 1999, 14, 53-76.	3.6	5
82	Performance of the hyper-ring multicomputer. , 1998, , .		4
83	The hyper-ring network. , 1998, , .		14
84	Multi-node communication in hyper-ring networks. , 1997, , .		5
85	On the impact of pipelined communication in hierarchical ring multicomputers. , 1997, , .		4
86	Distributed routing in the recursive diamond network. , 1997, , .		0
87	The Full-Cube topology: Properties and routing. Information Sciences, 1994, 76, 111-129.	6.9	2
88	RELCOMP: A reliability computation tool based on boolean and pattern matching techniques. Engineering Applications of Artificial Intelligence, 1992, 5, 407-411.	8.1	0
89	Design for testability of digital combinational circuits. Engineering Applications of Artificial Intelligence, 1991, 4, 229-234.	8.1	0