Mitsuru Sometani

List of Publications by Year in descending order

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		623734	580821
59	802	14	25
papers	citations	h-index	g-index
59	59	59	479
all docs	docs citations	times ranked	citing authors

#	Article	IF	Citations
1	Electrical detection of TV2a-type silicon vacancy spin defect in 4H-SiC MOSFETs. Applied Physics Letters, 2022, 120, 064001.	3.3	5
2	Impact of nitridation on the reliability of 4H-SiC(1121,0) MOS devices. Applied Physics Express, 2022, 15, 041002.	2.4	6
3	Comprehensive physical and electrical characterizations of NO nitrided SiO ₂ /4H-SiC(112Ì,,0) interfaces. Japanese Journal of Applied Physics, 2022, 61, SC1065.	1.5	7
4	Dipole scattering at the interface: The origin of low mobility observed in SiC MOSFETs. Journal of Applied Physics, 2022, 131, .	2.5	3
5	Investigation of reliability of NO nitrided SiC(1100) MOS devices. , 2022, , .		1
6	Impact of post-nitridation annealing in CO ₂ ambient on threshold voltage stability in 4H-SiC metal-oxide-semiconductor field-effect transistors. Applied Physics Express, 2022, 15, 061003.	2.4	2
7	Comparative Study of Performance of SiC SJ-MOSFETs Formed by Multi-epitaxial Growth and Trench-filling Epitaxial Growth. , 2022, , .		2
8	Free carrier density enhancement of 4H-SiC Si-face MOSFET by Ba diffusion process and NO passivation. Japanese Journal of Applied Physics, 2021, 60, SBBD08.	1.5	2
9	Accurate determination of threshold voltage shift during negative gate bias stress in 4H-SiC MOSFETs by fast on-the-fly method. Japanese Journal of Applied Physics, 2021, 60, 060901.	1.5	2
10	Negative Bias Temperature Instability in 4H-SiC MOSFETs Investigated by On-the-fly Methods., 2021,,.		0
11	Energy distribution of Al2O3/diamond interface states characterized by high temperature capacitance-voltage method. Carbon, 2020, 168, 659-664.	10.3	20
12	Difference in electron mobility at 4H–SiC/SiO2 interfaces with various crystal faces originating from effective-field-dependent scattering. Applied Physics Letters, 2020, 117, .	3.3	11
13	Insight into Al2O3/B-doped diamond interface states with high-temperature conductance method. Applied Physics Letters, 2020, 117, .	3.3	11
14	Low <i>V</i> _F 4H-SiC N-i-P diodes using newly developed low-resistivity p-type substrates. Japanese Journal of Applied Physics, 2020, 59, SGGD14.	1.5	0
15	Conduction mechanisms of oxide leakage current in p-channel 4H-SiC MOSFETs. Japanese Journal of Applied Physics, 2020, 59, 044003.	1.5	4
16	Carbon dangling-bond center (carbon <i>P</i> b center) at 4H-SiC(0001)/SiO2 interface. Applied Physics Letters, 2020, 116, .	3.3	20
17	Electrically detected magnetic resonance study on interface defects at nitrided Si-face, <i>and <i>m</i>-face 4H-SiC/SiO2 interfaces. Applied Physics Letters, 2020, 116, .</i>	3.3	10
18	Electron-spin-resonance and electrically detected-magnetic-resonance characterization on <i>P</i> bC center in various 4H-SiC(0001)/SiO2 interfaces. Journal of Applied Physics, 2020, 127, .	2.5	18

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19	Electrically detected-magnetic-resonance identifications of defects at 4H-SiC(000 1 \hat{A} -)/SiO2 interfaces with wet oxidation. Applied Physics Letters, 2019, 115, 151602.	3.3	8
20	Ideal phonon-scattering-limited mobility in inversion channels of 4H-SiC(0001) MOSFETs with ultralow net doping concentrations. Applied Physics Letters, 2019, 115, .	3.3	22
21	Mobility-limiting Coulomb scattering in nitrided 4H-SiC inversion channel on $11\hat{A}^-00$ m-face and $112\hat{A}^-0$ a-face characterized by Hall effect measurements. Applied Physics Letters, 2019, 115, 132106.	3.3	7
22	Impact of crystal faces of 4H-SiC in SiO ₂ /4H-SiC structures on interface trap densities and mobilities. Applied Physics Express, 2019, 12, 021003.	2.4	21
23	Hole trapping in SiC-MOS devices evaluated by fast-capacitance–voltage method. Japanese Journal of Applied Physics, 2018, 57, 04FR15.	1.5	8
24	Accurate evaluation of fast threshold voltage shift for SiC MOS devices under various gate bias stress conditions. Japanese Journal of Applied Physics, 2018, 57, 04FA07.	1.5	10
25	Improved channel mobility of 4H-SiC n-MOSFETs by ultrahigh-temperature gate oxidation with low-oxygen partial-pressure cooling. Japanese Journal of Applied Physics, 2018, 57, 120304.	1.5	6
26	Analysis of fast and slow responses in AC conductance curves for p-type SiC MOS capacitors. Japanese Journal of Applied Physics, 2018, 57, 06KA06.	1.5	7
27	Sub-nanometer-scale depth profiling of nitrogen atoms in SiO ₂ /4H-SiC structures treated with NO annealing. Applied Physics Express, 2018, 11, 101303.	2.4	17
28	Passive–active oxidation boundary for thermal oxidation of 4H-SiC(0001) surface in O ₂ /Ar gas mixture and its impact on SiO ₂ /SiC interface quality. Applied Physics Express, 2018, 11, 091301.	2.4	19
29	Insight into enhanced field-effect mobility of 4H-SiC MOSFET with Ba incorporation studied by Hall effect measurements. AIP Advances, 2018, 8, .	1.3	16
30	Impact of oxide thickness on the density distribution of near-interface traps in 4H-SiC MOS capacitors. Japanese Journal of Applied Physics, 2018, 57, 06KA04.	1.5	12
31	Interface carbon defects at 4H-SiC(0001)/SiO2 interfaces studied by electron-spin-resonance spectroscopy. Applied Physics Letters, 2018, 113, .	3.3	38
32	Effect of boron incorporation on slow interface traps in SiO2/4H-SiC structures. Applied Physics A: Materials Science and Processing, 2017, 123, 1.	2.3	16
33	Ultrahigh-Temperature Oxidation of 4H-SiC(0001) and an Impact of Cooling Process on SiO ₂ /SiC Interface Properties. Materials Science Forum, 2017, 897, 323-326.	0.3	3
34	Characterization of near-interface traps at 4H-SiC metal–oxide–semiconductor interfaces using modified distributed circuit model. Applied Physics Express, 2017, 10, 064101.	2.4	19
35	Characterization of traps at nitrided SiO ₂ /SiC interfaces near the conduction band edge by using Hall effect measurements. Applied Physics Express, 2017, 10, 046601.	2.4	96
36	Impact of rapid cooling process in ultrahigh-temperature oxidation of 4H-SiC(0001). Japanese Journal of Applied Physics, 2017, 56, 04CR04.	1.5	8

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37	Evaluation of drain current decrease by AC gate bias stress in commercially available SiC MOSFETs. , 2017, , .		2
38	Ultrahigh-temperature rapid thermal oxidation of 4H-SiC(0001) surfaces and oxidation temperature dependence of SiO2/SiC interface properties. Applied Physics Letters, 2016, 109, .	3.3	39
39	Self-aligned formation of the trench bottom shielding region in 4H-SiC trench gate MOSFET. Japanese Journal of Applied Physics, 2016, 55, 04ER02.	1.5	17
40	Threshold-voltage instability in 4H-SiC MOSFETs with nitrided gate oxide revealed by non-relaxation method. Japanese Journal of Applied Physics, 2016, 55, 04ER11.	1.5	28
41	3.3 kV-Class 4H-SiC UMOSFET by Double-Trench with Tilt Angle Ion Implantation. Materials Science Forum, 2016, 858, 974-977.	0.3	11
42	Characterization of traps in SiC/SiO ₂ interfaces close to the conduction band by deep-level transient spectroscopy. Japanese Journal of Applied Physics, 2015, 54, 111301.	1.5	12
43	Temperature-dependent analysis of conduction mechanism of leakage current in thermally grown oxide on 4H-SiC. Journal of Applied Physics, 2015, 117, .	2.5	48
44	Low R _{ons} in 3kV 4H-SiC UMOSFET with MeV Implanted Buried P-Base Region. Materials Science Forum, 2015, 821-823, 769-772.	0.3	3
45	Improved Channel Mobility in 4H-SiC MOSFETs by Boron Passivation. IEEE Electron Device Letters, 2014, 35, 1176-1178.	3.9	98
46	Dynamic characteristics of large current capacity module using 16-kV ultrahigh voltage SiC flip-type n-channel IE-IGBT. , 2014, , .		13
47	Conduction Mechanism of Leakage Current in Thermal Oxide on 4H-SiC. Materials Science Forum, 2014, 778-780, 579-582.	0.3	1
48	Variation of Chemical Vapor Deposited SiO\$_{2}\$ Density Due to Generation and Shrinkage of Open Space During Thermal Annealing. Japanese Journal of Applied Physics, 2012, 51, 021101.	1.5	9
49	Variation of Chemical Vapor Deposited SiO2Density Due to Generation and Shrinkage of Open Space During Thermal Annealing. Japanese Journal of Applied Physics, 2012, 51, 021101.	1.5	0
50	Long TDDB Lifetime of SiO2 Film by Controlling Degradation Rate and SiO2/Si Micro-roughness. ECS Transactions, 2009, 25, 371-378.	0.5	3
51	Improvement of Dielectric Properties on Deposited SiO2Caused by Stress Relaxation with Thermal Annealing. Japanese Journal of Applied Physics, 2009, 48, 05DB03.	1.5	9
52	Suppression of Leakage Current of Deposited SiO ₂ with Bandgap Increasing by High Temperature Annealing. ECS Transactions, 2009, 19, 403-413.	0.5	8
53	Annealing properties of open volumes in strained SiN films studied by monoenergetic positron beams. Journal of Applied Physics, 2007, 102, 064513.	2.5	5
54	Deep-Level Transient Spectroscopy Characterization of Interface States in SiO ₂ /4H-SiC Structures Close to the Conduction Band Edge. Materials Science Forum, 0, 778-780, 424-427.	0.3	0

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55	Exact Characterization of Threshold Voltage Instability in 4H-SiC MOSFETs by Non-Relaxation Method. Materials Science Forum, 0, 821-823, 685-688.	0.3	10
56	Dynamic Characterization of the Threshold Voltage Instability under the Pulsed Gate Bias Stress in 4H-SiC MOSFET. Materials Science Forum, 0, 897, 549-552.	0.3	7
57	Sub-nm-Scale Depth Profiling of Nitrogen in NO- and N ₂ -Annealed SiO ₂ /4H-SiC(0001) Structures. Materials Science Forum, 0, 963, 226-229.	0.3	2
58	Insight into Channel Conduction Mechanisms of 4H-SiC(0001) MOSFET Based on Temperature-Dependent Hall Effect Measurement. Materials Science Forum, 0, 1004, 620-626.	0.3	11
59	20 kV-Class Ultra-High Voltage 4H-SiC n-IE-IGBTs. Materials Science Forum, 0, 1004, 899-904.	0.3	9