Skyler Windh

List of Publications by Year
in descending order

7 papers	142 citations	2258059 3 h-index	2272923 4 g-index
7 all docs	7 docs citations	7 times ranked	160 citing authors

#	Article	IF	Citations
1	Efficient local locking for massively multithreaded in-memory hash-based operators. VLDB Journal, 2021, 30, 333-359.	4.1	0
2	Accelerating In-Memory Database Selections Using Latency Masking Hardware Threads. Transactions on Architecture and Code Optimization, 2019, 16, 1-28.	2.0	2
3	Performance Improvements and Congestion Reduction for Routing-Based Synthesis for Digital Microfluidic Biochips. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2017, 36, 41-54.	2.7	20
4	FPGA-accelerated group-by aggregation using synchronizing caches. , 2016, , .		11
5	CAMs as synchronizing caches for multithreaded irregular applications on FPGAs. , 2015, , .		4
6	High-Level Language Tools for Reconfigurable Computing. Proceedings of the IEEE, 2015, 103, 390-408.	21.3	55
7	An open-source compiler and PCB synthesis tool for digital microfluidic biochips. The Integration VLSI Journal, 2015, 51, 169-193.	2.1	50