

Eva Bestelink

List of Publications by Year in descending order

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papers

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20
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citing authors

#	ARTICLE	IF	CITATIONS
1	Compact Source-Gated Transistor Analog Circuits for Ubiquitous Sensors. IEEE Sensors Journal, 2020, 20, 14903-14913.	4.7	19
2	Versatile Thin-Film Transistor with Independent Control of Charge Injection and Transport for Mixed Signal and Analog Computation. Advanced Intelligent Systems, 2021, 3, 2000199.	6.1	17
3	The Secret Ingredient for Exceptional Contact-Controlled Transistors. Advanced Electronic Materials, 2022, 8, .	5.1	12
4	Turn-off mechanisms in thin-film source-gated transistors with applications to power devices and rectification. Applied Physics Letters, 2019, 114, .	3.3	10
5	Simulation Study of Overlap Capacitance in Source-Gated Transistors for Current-Mode Pixel Drivers. IEEE Electron Device Letters, 2019, 40, 1451-1454.	3.9	9
6	Multimodal transistors as ReLU activation functions in physical neural network classifiers. Scientific Reports, 2022, 12, 670.	3.3	8
7	P: Ultra-Compact Multi-Level Digital-to-Analog Converter based on Linear Multimodal Thin-Film Transistors. Digest of Technical Papers SID International Symposium, 2020, 51, 1375-1378.	0.3	4
8	49dB depletion-load amplifiers with polysilicon source-gated transistors. , 2019, , .		3
9	Suppression of Hot-Carrier Effects Facilitated by the Multimodal Thin-Film Transistor Architecture. Advanced Electronic Materials, 2021, 7, 2100533.	5.1	3
10	Contact Doping as a Design Strategy for Compact TFT-Based Temperature Sensing. IEEE Transactions on Electron Devices, 2021, 68, 4962-4965.	3.0	3
11	Compact Unipolar XNOR/XOR Circuit Using Multimodal Thin-Film Transistors. IEEE Transactions on Electron Devices, 2021, 68, 4951-4955.	3.0	2
12	Flexible Microcrystalline Silicon Source-Gated Transistors with Negligible DC Performance Degradation at 2.5 mm Bending Radius. , 2022, , .		2
13	31: Invited Paper: The Multimodal Thin-Film Transistor (MMT): A Versatile Low-Power and High-Gain Device with Inherent Linear Response. Digest of Technical Papers SID International Symposium, 2020, 51, 444-447.	0.3	1
14	P3: Investigation on ICP-CVD as a Polyvalent Low Cost Technology Dedicated to Low Temperature $\frac{1}{4}$ Si TFT Prototyping.. Digest of Technical Papers SID International Symposium, 2020, 51, 1538-1541.	0.3	1
15	Oxide transistors: unconventional architectures and their applications. , 2021, , .		0
16	22.2: Invited Paper: Opportunities for Multimodal Thin-Film Transistors in Displays and Beyond. Digest of Technical Papers SID International Symposium, 2021, 52, 294-297.	0.3	0
17	Suppression of Hot-Carrier Effects Facilitated by the Multimodal Thin-Film Transistor Architecture (Adv. Electron. Mater. 9/2021). Advanced Electronic Materials, 2021, 7, 2170038.	5.1	0
18	PÃ: <i>Late-News-Poster:</i> Data Retention in Pixel Drivers Based on Source-Gated Transistors. Digest of Technical Papers SID International Symposium, 2020, 51, 1397-1399.	0.3	0

#	ARTICLE	IF	CITATIONS
19	Engineering current-voltage linearity in TFTs for analog and neuromorphic computing. , 2021, , .		0
20	Total Gain Recovery in Floating Gate Thin-Film Transistors for Neuromorphic and Edge Computing. ECS Meeting Abstracts, 2020, MA2020-02, 1947-1947.	0.0	0