Yang Xie

List of Publications by Year
in descending order

11 papers	539 citations	1478505 6 h-index	1872680 6 g-index
11 all docs	11 docs citations	11 times ranked	299 citing authors

#	Article	IF	Citations
1	Keynote: A Disquisition on Logic Locking. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2020, 39, 1952-1972.	2.7	63
2	Anti-SAT: Mitigating SAT Attack on Logic Locking. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2019, 38, 199-207.	2.7	117
3	A Combined Optimization-Theoretic and Side- Channel Approach for Attacking Strong Physical Unclonable Functions. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 73-81.	3.1	23
4	GPU obfuscation. , 2018, , .		10
5	Security-Aware 2.5D Integrated Circuit Design Flow Against Hardware IP Piracy. Computer, 2017, 50, 62-71.	1.1	25
6	Delay Locking. , 2017, , .		77
7	Template Attack Based Deobfuscation of Integrated Circuits. , 2017, , .		5
8	3D/2.5D IC-Based Obfuscation. , 2017, , 291-314.		8
9	2.5D/3D Integration Technologies for Circuit Obfuscation. , 2016, , .		9
10	Security and Vulnerability Implications of 3D ICs. IEEE Transactions on Multi-Scale Computing Systems, 2016, 2, 108-122.	2.4	38
11	Mitigating SAT Attack on Logic Locking. Lecture Notes in Computer Science, 2016, , 127-146.	1.3	164