

# Tanesh Kumar

## List of Publications by Year in descending order

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55  
papers

1,505  
citations

623734

14  
h-index

610901

24  
g-index

58  
all docs

58  
docs citations

58  
times ranked

1120  
citing authors

#	ARTICLE	IF	CITATIONS
1	Emerging Technologies for Next Generation Remote Health Care and Assisted Living. IEEE Access, 2022, 10, 56094-56132.	4.2	17
2	Securing Edge Services for Future Smart Healthcare and Industrial IoT Applications. , 2022, , .		0
3	Resource-Aware Dynamic Service Deployment for Local IoT Edge Computing: Healthcare Use Case. IEEE Access, 2021, 9, 115868-115884.	4.2	13
4	The Challenges of Artificial Intelligence in Wireless Networks for the Internet of Things: Exploring Opportunities for Growth. IEEE Industrial Electronics Magazine, 2021, 15, 16-29.	2.6	27
5	Health-BlockEdge: Blockchain-Edge Framework for Reliable Low-Latency Digital Healthcare Applications. Sensors, 2021, 21, 2502.	3.8	35
6	An Overview of the Security Landscape of Virtual Mobile Networks. IEEE Access, 2021, 9, 169014-169030.	4.2	1
7	AGE: authentication in gadget-free healthcare environments. Information Technology and Management, 2020, 21, 95-114.	2.4	18
8	BlockEdge: Blockchain-Edge Framework for Industrial IoT Networks. IEEE Access, 2020, 8, 154166-154185.	4.2	61
9	Performance and Efficiency Optimization of Multi-layer IoT Edge Architecture. , 2020, , .		18
10	Decentralized IoT Edge Nanoservice Architecture for Future Gadget-Free Computing. IEEE Access, 2019, 7, 119856-119872.	4.2	34
11	Security for 5G and Beyond. IEEE Communications Surveys and Tutorials, 2019, 21, 3682-3722.	39.4	227
12	SEC-BlockEdge: Security Threats in Blockchain-Edge based Industrial IoT Networks. , 2019, , .		10
13	Docker Enabled Virtualized Nanoservices for Local IoT Edge Networks. , 2019, , .		12
14	Overview of 5G Security Challenges and Solutions. IEEE Communications Standards Magazine, 2018, 2, 36-43.	4.9	293
15	Towards gadget-free internet services: A roadmap of the Naked world. Telematics and Informatics, 2018, 35, 82-92.	5.8	28
16	Secure and Efficient Data Accessibility in Blockchain Based Healthcare Systems. , 2018, , .		71
17	Securing Gadget-Free Digital Services. Computer, 2018, 51, 66-77.	1.1	25
18	Blockchain Utilization in Healthcare: Key Requirements and Challenges. , 2018, , .		97

#	ARTICLE	IF	CITATIONS
19	5G Privacy: Scenarios and Solutions. , 2018, , .		40
20	Different I/O Standard and Technology Based Thermal Aware Energy Efficient Vedic Multiplier Design for Green Wireless Communication on FPGA. Wireless Personal Communications, 2017, 96, 3139-3158.	2.7	6
21	From gadget to gadget-free hyperconnected world: Conceptual analysis of user privacy challenges. , 2017, , .		11
22	Identity privacy preserving biometric based authentication scheme for Naked healthcare environment. , 2017, , .		33
23	Performance Evaluation of FIR Filter After Implementation on Different FPGA and SOC and Its Utilization in Communication and Network. Wireless Personal Communications, 2017, 95, 375-389.	2.7	20
24	5G security: Analysis of threats and solutions. , 2017, , .		97
25	FPGA Based Low Power DES Algorithm Design and Implementation using HTML Technology. International Journal of Software Engineering and Its Applications, 2016, 10, 81-92.	0.2	5
26	Anatomy and Utilities of an Artificial Intelligence Conversational Entity. , 2015, , .		6
27	CTHS Based Energy Efficient Thermal Aware Image ALU Design on FPGA. Wireless Personal Communications, 2015, 85, 671-696.	2.7	29
28	Performance Evaluation of Mobile Ad Hoc Routing Mechanisms. Wireless Personal Communications, 2015, 85, 377-392.	2.7	4
29	IoTs Enable Active Contour Modeling Based Energy Efficient and Thermal Aware Object Tracking on FPGA. Wireless Personal Communications, 2015, 85, 529-543.	2.7	28
30	Simulation of HSTL IO standard based energy efficient Punjabi Unicode reader on FPGA. , 2014, , .		4
31	Low Power Devnagari Unicode Checker Design Using CGVS Approach. Advanced Materials Research, 2014, 984-985, 1282-1285.	0.3	6
32	Frequency, Voltage and Temperature Sensor Design for Fire Detection in VLSI Circuit on FPGA. Communications in Computer and Information Science, 2014, , 121-133.	0.5	6
33	I/O standard based thermal/energy efficient green communication for Wi-Fi protected access on FPGA. , 2014, , .		6
34	LVTTTL based energy efficient watermark generator design and implementation on FPGA. , 2014, , .		3
35	Simulation of SSTL IO standard based power optimized parallel integrator design on FPGA. , 2014, , .		6
36	Output load capacitance based low power implementation of UART on FPGA. , 2014, , .		10

#	ARTICLE	IF	CITATIONS
37	Capacitance scaling based energy efficient FIR filter for digital signal processing. , 2014, , .		11
38	Frequency Scaling Based Green Mobile Battery Charge Controller Sensor Design on FPGA. Advanced Materials Research, 2014, 984-985, 1057-1062.	0.3	1
39	Mapping Based Energy Efficient Counter Design on FPGA. Advanced Materials Research, 2014, 984-985, 1085-1088.	0.3	1
40	Simulation of voltage based efficient fire sensor on FPGA using SSTL IO standards. , 2014, , .		12
41	Design of power optimized memory circuit using High Speed Transceiver Logic IO Standard on 28nm Field Programmable Gate Array. , 2014, , .		2
42	I/O standard based power optimized processor register design on ultra scale FPGA. , 2014, , .		10
43	Reliable ALU design with optimized voltage and implementation on 28nm FPGA. , 2014, , .		4
44	Thermal aware energy efficient bengali unicode reader in Text analysis. , 2014, , .		14
45	Simulation of energy efficient Bi-directional Visitor Counting Machine on FPGA. , 2014, , .		3
46	Mobile DDR IO Standard Based High Performance Energy Efficient Portable ALU Design on FPGA. Wireless Personal Communications, 2014, 76, 569-578.	2.7	51
47	Capacitance and frequency scaling based energy efficient image inverter design on FPGA. , 2013, , .		7
48	LVC MOS I/O standard and drive strength based energy efficient design on ultra scale FPGA. , 2013, , .		4
49	SSTL based green image ALU design on different FPGA. , 2013, , .		17
50	Voltage scaling based green design on ultra scale FPGA. , 2013, , .		2
51	64 bit green ALU design using clock gating technique on ultra scale FPGA. , 2013, , .		4
52	LVD CI I/O standard based green image ALU design on ultra scale FPGA. , 2013, , .		4
53	Digitally controlled impedance based green design on ultra scale FPGA. , 2013, , .		2
54	LVC MOS I/O standard based million MHz high performance energy efficient design on FPGA. , 2013, , .		3

#	ARTICLE	IF	CITATIONS
55	Thermal Mechanics Based Energy Efficient FIR Filter for Digital Signal Processing. Applied Mechanics and Materials, 0, 612, 65-70.	0.2	16