

Mihalis Psarakis

List of Publications by Year in descending order

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16
papers

490
citations

1478505

6
h-index

1372567

10
g-index

16
all docs

16
docs citations

16
times ranked

589
citing authors

#	ARTICLE	IF	CITATIONS
1	Configuration Memory Scrubbing of SRAM-Based FPGAs Using a Mixed 2-D Coding Technique. IEEE Transactions on Nuclear Science, 2022, 69, 871-882.	2.0	6
2	Single Event Effects Characterization of the Programmable Logic of Xilinx Zynq-7000 FPGA Using Very/Ultra High-Energy Heavy Ions. IEEE Transactions on Nuclear Science, 2021, 68, 36-45.	2.0	11
3	Risk Assessment for IoT-Enabled Cyber-Physical Systems. Learning and Analytics in Intelligent Systems, 2021, , 157-173.	0.6	4
4	Scrubbing-Aware Placement for Reliable FPGA Systems. IEEE Transactions on Emerging Topics in Computing, 2020, 8, 564-576.	4.6	7
5	On a Security-oriented Design Framework for Medical IoT Devices: The Hardware Security Perspective. , 2020, , .		8
6	An FPGA-Based Accelerated Optimization Algorithm for Real-Time Applications. Journal of Signal Processing Systems, 2020, 92, 1155-1176.	2.1	11
7	Analyzing the Resilience to SEUs of an Image Data Compression Core in a COTS SRAM FPGA. , 2019, , .		4
8	Guest Editorial Special Issue on Secure Embedded IoT Devices for Resilient Critical Infrastructures. IEEE Internet of Things Journal, 2019, 6, 7988-7991.	8.7	0
9	Advanced Persistent Threats and Zero-Day Exploits in Industrial Internet of Things. Advanced Sciences and Technologies for Security Applications, 2019, , 47-68.	0.5	11
10	Fast Tuning of the PID Controller in An HVAC System Using the Big Bang-“Big Crunch Algorithm and FPGA Technology. Algorithms, 2018, 11, 146.	2.1	28
11	A Survey of IoT-Enabled Cyberattacks: Assessing Attack Paths to Critical Infrastructures and Services. IEEE Communications Surveys and Tutorials, 2018, 20, 3453-3495.	39.4	261
12	An Efficient FPGA Implementation of the Big Bang-Big Crunch Optimization Algorithm. Lecture Notes in Computer Science, 2018, , 166-177.	1.3	2
13	Design and implementation of a self-healing processor on SRAM-based FPGAs. , 2014, , .		10
14	A Fault Tolerant Approach for FPGA Embedded Processors Based on Runtime Partial Reconfiguration. Journal of Electronic Testing: Theory and Applications (JETTA), 2013, 29, 805-823.	1.2	12
15	Fault tolerant FPGA processor based on runtime reconfigurable modules. , 2012, , .		25
16	Architectures for online error detection and recovery in multicore processors. , 2011, , .		90