Jaehoon Bong

List of Publications by Year in descending order

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759233 794594 22 524 12 19 h-index citations g-index papers 23 23 23 1230 docs citations times ranked citing authors all docs

#	Article	IF	CITATIONS
1	Synthesis of ultrathin polymer insulating layers by initiated chemical vapour deposition for low-power soft electronics. Nature Materials, 2015, 14, 628-635.	27.5	229
2	Direct Graphene Transfer and Its Application to Transfer Printing Using Mechanically Controlled, Large Area Graphene/Copper Freestanding Layer. Advanced Functional Materials, 2018, 28, 1707102.	14.9	40
3	Facile graphene n-doping by wet chemical treatment for electronic applications. Nanoscale, 2014, 6, 8503.	5.6	35
4	Functionalized Graphene as an Ultrathin Seed Layer for the Atomic Layer Deposition of Conformal High-k Dielectrics on Graphene. ACS Applied Materials & Samp; Interfaces, 2013, 5, 11515-11519.	8.0	31
5	Ultrathin graphene and graphene oxide layers as a diffusion barrier for advanced Cu metallization. Applied Physics Letters, 2015, 106, .	3.3	28
6	Alleviation of fermi-level pinning effect at metal/germanium interface by the insertion of graphene layers. Applied Physics Letters, 2014, 105 , .	3.3	25
7	Effect of ZrO2 interfacial layer on forming ferroelectric HfxZryOz on Si substrate. AIP Advances, 2019, 9, .	1.3	24
8	High performance graphene field effect transistors on an aluminum nitride substrate with high surface phonon energy. Applied Physics Letters, 2014, 104, 193112.	3.3	18
9	Work function tuning of metal/graphene stack electrode. Applied Physics Letters, 2014, 104, 083512.	3.3	18
10	A Highâ∈Performance Topâ∈Gated Graphene Fieldâ∈Effect Transistor with Excellent Flexibility Enabled by an iCVD Copolymer Gate Dielectric. Small, 2018, 14, 1703035.	10.0	14
11	Wrinkle-free graphene with spatially uniform electrical properties grown on hot-pressed copper. Nano Research, 2015, 8, 1075-1080.	10.4	13
12	Improved Drain Current Saturation and Voltage Gain in Graphene–on–Silicon Field Effect Transistors. Scientific Reports, 2016, 6, 25392.	3.3	12
13	A quantitative strain analysis of a flexible single-crystalline silicon membrane. Applied Physics Letters, 2017, 110, 033105.	3.3	10
14	Mechanical Stability Analysis via Neutral Mechanical Plane for Highâ€Performance Flexible Si Nanomembrane FDSOI Device. Advanced Materials Interfaces, 2017, 4, 1700618.	3.7	9
15	Reliability improvement of a flexible FD-SOI MOSFET via heat management. Applied Physics Letters, 2017, 110, 252101.	3.3	6
16	Analysis of fluorine effects on charge-trap flash memory of W/TiN/Al2O3/Si3N4/SiO2/poly-Si gate stack. Solid-State Electronics, 2020, 164, 107713.	1.4	4
17	Vertically Formed Graphene Stripe for 3D Fieldâ€Effect Transistor Applications. Small, 2017, 13, 1602373.	10.0	3
18	Performance Degradation of Flexible Si Nanomembrane Transistors With Al ₂ O ₃ and SiO ₂ Dielectrics Under Mechanical Stress. IEEE Transactions on Electron Devices, 2018, 65, 3069-3072.	3.0	2

#	Article	IF	CITATIONS
19	Minimally invasive medical catheter with highly flexible FDSOI-based integrated circuits. , 2019, , .		2
20	Influence of Self-Heating Effect on Interface Trap Generation in Highly Flexible Single-Crystalline Si Nanomembrane Transistors. Journal of Nanoscience and Nanotechnology, 2019, 19, 6481-6486.	0.9	1
21	Improvement of Gate Dielectric Integrity Using O ₂ Plasma Treatment Prior to Atomic Layer Deposition on Chemical Vapor Deposition Grown Graphene. Journal of Nanoscience and Nanotechnology, 2015, 15, 220-223.	0.9	O
22	Mechanical and Electrical Reliability Analysis of Flexible Si Complementary Metal-Oxide-Semiconductor Integrated Circuit. Journal of Nanoscience and Nanotechnology, 2019, 19, 6473-6480.	0.9	0