Byoung Hun Lee

List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/4569491/publications.pdf

Version: 2024-02-01

274 papers

9,482 citations

50276 46 h-index 49909 87 g-index

274 all docs

274 docs citations

times ranked

274

11265 citing authors

| # | Article | IF | CITATIONS |
|----|---|------|-----------|
| 1 | High-performance near-infrared photodetectors based on gate-controlled graphene–germanium Schottky junction with split active junction. Nanophotonics, 2022, 11, 1041-1049. | 6.0 | 6 |
| 2 | Performance enhancement of graphene/Ge near-infrared photodetector by modulating the doping level of graphene. APL Photonics, 2022, 7, . | 5.7 | 11 |
| 3 | Performance Evaluation of Scaled ZnO Stacked Nanosheet Channel Ternary Field Effect Transistor. IEEE Electron Device Letters, 2022, 43, 323-326. | 3.9 | 3 |
| 4 | Perovskite multifunctional logic gates via bipolar photoresponse of single photodetector. Nature Communications, 2022, 13, 720. | 12.8 | 53 |
| 5 | Effects of DC and AC stress on the VT shift of AlGaN/GaN MIS-HEMTs. Current Applied Physics, 2022, 39, 128-132. | 2.4 | 0 |
| 6 | Demonstration of Anti-ambipolar Switch and Its Applications for Extremely Low Power Ternary Logic Circuits. ACS Nano, 2022, 16, 10994-11003. | 14.6 | 11 |
| 7 | Impact of Post-Metal Annealing With Deuterium or Nitrogen for Curing a Gate Dielectric Using Joule Heat Driven by Punch-Through Current. IEEE Electron Device Letters, 2021, 42, 276-279. | 3.9 | 5 |
| 8 | High-responsivity PtSe2 photodetector enhanced by photogating effect. Applied Physics Letters, 2021, 118, . | 3.3 | 25 |
| 9 | Modulation of the Electronic Properties of MXene (Ti ₃ C ₂ T _{<i>x</i>}) <i>via</i> Surface-Covalent Functionalization with Diazonium. ACS Nano, 2021, 15, 1388-1396. | 14.6 | 100 |
| 10 | Highly responsive near-infrared photodetector with low dark current using graphene/germanium Schottky junction with Al ₂ O ₃ interfacial layer. Nanophotonics, 2021, 10, 1573-1579. | 6.0 | 39 |
| 11 | High Gain and Broadband Absorption Graphene Photodetector Decorated with Bi2Te3 Nanowires. Nanomaterials, 2021, 11, 755. | 4.1 | 15 |
| 12 | Copper-graphene heterostructure for back-end-of-line compatible high-performance interconnects. Npj 2D Materials and Applications, 2021, 5, . | 7.9 | 13 |
| 13 | Bias-controlled multi-functional transport properties of InSe/BP van der Waals heterostructures. Scientific Reports, 2021, 11, 7843. | 3.3 | 4 |
| 14 | Direct Measurement of Transient Charging and Dipole Alignment Speed in Ferroelectric Hf _{0.5} Zr _{0.5} O ₂ Gate Dielectric Using Graphene FETs. Advanced Electronic Materials, 2021, 7, 2100145. | 5.1 | 5 |
| 15 | Operation Principles of ZnO/Al ₂ O ₃ â€AlDMP/ZnO Stackedâ€Channel Ternary Thinâ€Film Transistor. Advanced Electronic Materials, 2021, 7, 2100247. | 5.1 | 9 |
| 16 | Demonstration of programmable ternary graphene field-effect transistor using ferroelectric polymer doping. Organic Electronics, 2021, 93, 106157. | 2.6 | 4 |
| 17 | A Facile Method for Improving Detectivity of Graphene/pâ€₹ype Silicon Heterojunction Photodetector. Laser and Photonics Reviews, 2021, 15, 2000557. | 8.7 | 24 |
| 18 | Non-destructive defect level analysis of graphene using amplitude-modulated discharge current analysis. Carbon, 2021, 179, 627-632. | 10.3 | 4 |

| # | Article | IF | Citations |
|----|---|------|-----------|
| 19 | A new route of synthesizing atomically thin 2D materials embedded in bulk oxides. Journal of Applied Physics, 2021, 130, 035302. | 2.5 | 0 |
| 20 | Direct Defect-Level Analysis of Metal–Insulator–Metal Capacitor Using Internal Photoemission Spectroscopy. IEEE Journal of the Electron Devices Society, 2021, 9, 424-428. | 2.1 | 2 |
| 21 | Unveiling the Role of Al ₂ O ₃ Interlayer in Indium–Gallium–Zinc–Oxide Transistors. Physica Status Solidi (A) Applications and Materials Science, 2021, 218, 2000621. | 1.8 | 4 |
| 22 | Deep-Ultraviolet (DUV)-Induced Doping in Single Channel Graphene for Pn-Junction. Nanomaterials, 2021, 11, 3003. | 4.1 | 1 |
| 23 | High-quality nitrogen-doped graphene films synthesized from pyridine via two-step chemical vapor deposition. Carbon, 2020, 159, 579-585. | 10.3 | 40 |
| 24 | A negative electrocaloric effect in an antiferroelectric zirconium dioxide thin film. Nanoscale, 2020, 12, 3894-3901. | 5.6 | 18 |
| 25 | Quantitative Analysis of High-Pressure Deuterium Annealing Effects on Vertically Stacked Gate-All-Around SONOS Memory. IEEE Transactions on Electron Devices, 2020, 67, 3903-3907. | 3.0 | 15 |
| 26 | Direct writing of graphite thin film by laser-assisted chemical vapor deposition. Carbon, 2020, 169, 163-171. | 10.3 | 5 |
| 27 | Dynamic band alignment modulation of ultrathin WO _x /ZnO stack for high on/off ratio field-effect switching applications. Nanoscale, 2020, 12, 16755-16761. | 5.6 | 1 |
| 28 | Al2O3-Induced Sub-Gap Doping on the IGZO Channel for the Detection of Infrared Light. ACS Applied Electronic Materials, 2020, 2, 1478-1483. | 4.3 | 19 |
| 29 | Performance Degradation in Graphene–ZnO Barristors Due to Graphene Edge Contact. ACS Applied Materials & Interfaces, 2020, 12, 28768-28774. | 8.0 | 0 |
| 30 | Channel Defect Profiling and Passivation for ZnO Thin-Film Transistors. Nanomaterials, 2020, 10, 1186. | 4.1 | 1 |
| 31 | Gate-Modulated Ultrasensitive Visible and Near-Infrared Photodetection of Oxygen Plasma-Treated WSe ₂ Lateral pn-Homojunctions. ACS Applied Materials & Diterfaces, 2020, 12, 23261-23271. | 8.0 | 41 |
| 32 | MXenes for future nanophotonic device applications. Nanophotonics, 2020, 9, 1831-1853. | 6.0 | 31 |
| 33 | Quantitative defect density extraction method for metal–insulator–metal capacitor. Semiconductor Science and Technology, 2020, 35, 115025. | 2.0 | 5 |
| 34 | Extreme Low Power Technology using Ternary Arithmetic Logic Circuits via Drastic Interconnect Length Reduction. , 2020, , . | | 8 |
| 35 | Avalanche Carrier Multiplication in Multilayer Black Phosphorus and Avalanche Photodetector. Small, 2019, 15, e1805352. | 10.0 | 25 |
| 36 | Enhanced Photoâ€Response of Mos 2 Photodetectors by a Laterally Aligned SiO 2 Nanoribbon Array Substrate. ChemNanoMat, 2019, 5, 1272-1279. | 2.8 | 2 |

| # | Article | lF | Citations |
|----|--|------|-----------|
| 37 | Demonstration of ternary devices and circuits using dual channel graphene barristors., 2019,,. | | 7 |
| 38 | CMOS technology on another level. Nature Electronics, 2019, 2, 272-273. | 26.0 | 1 |
| 39 | Plasmonic Transition Metal Carbide Electrodes for High-Performance InSe Photodetectors. ACS Nano, 2019, 13, 8804-8810. | 14.6 | 69 |
| 40 | Transitionâ€Metalâ€Carbide (Mo ₂ C) Multiperiod Gratings for Realization of Highâ€Sensitivity and Broadâ€Spectrum Photodetection. Advanced Functional Materials, 2019, 29, 1905384. | 14.9 | 57 |
| 41 | Hot-Carrier Degradation Estimation of a Silicon-on-Insulator Tunneling FET Using Ambipolar Characteristics. IEEE Electron Device Letters, 2019, 40, 1716-1719. | 3.9 | 7 |
| 42 | ZnO composite nanolayer with mobility edge quantization for multi-value logic transistors. Nature Communications, 2019, 10, 1998. | 12.8 | 67 |
| 43 | Interface state degradation during AC positive bias temperature instability stress. Solid-State Electronics, 2019, 158, 46-50. | 1.4 | 3 |
| 44 | Enhancement of Ferroelectric Properties of Superlattice-Based Epitaxial BiFeO ₃ Thin Films via Substitutional Doping Effect. Journal of Physical Chemistry C, 2019, 123, 11564-11571. | 3.1 | 5 |
| 45 | Threshold Voltage Modulation of a Graphene–ZnO Barristor Using a Polymer Doping Process. Advanced Electronic Materials, 2019, 5, 1800805. | 5.1 | 17 |
| 46 | Advantages of a buried-gate structure for graphene field-effect transistor. Semiconductor Science and Technology, 2019, 34, 055010. | 2.0 | 12 |
| 47 | Highâ€Responsivity Nearâ€Infrared Photodetector Using Gateâ€Modulated Graphene/Germanium Schottky Junction. Advanced Electronic Materials, 2019, 5, 1800957. | 5.1 | 54 |
| 48 | Piezoelectrically modulated touch pressure sensor using a graphene barristor. Japanese Journal of Applied Physics, 2019, 58, SBBH03. | 1.5 | 6 |
| 49 | Chemically doped graphene based ternary field effect transistors. Japanese Journal of Applied Physics, 2019, 58, SBBH04. | 1.5 | 8 |
| 50 | Low-Power Complementary Logic Circuit Using Polymer-Electrolyte-Gated Graphene Switching Devices. ACS Applied Materials & Samp; Interfaces, 2019, 11, 47247-47252. | 8.0 | 8 |
| 51 | Scalable Two-Dimensional Lateral Metal/Semiconductor Junction Fabricated with Selective Synthetic Integration of Transition-Metal-Carbide (Mo ₂ C)/-Dichalcogenide (MoS ₂). ACS Applied Materials & Dictance of the Applied Materials and Substitution (MoS ₂). ACS Applied Materials & Dictance of the Applied & Dictance of the Applied Materials & Dictance of the Applied Materials & Dictance of the Applied & Dictanc | 8.0 | 19 |
| 52 | Tunable AC/DC converter using graphene-germanium barristor based half-wave rectifier. AIP Advances, 2019, 9, 095009. | 1.3 | 0 |
| 53 | Contact Resistance Reduction of WS ₂ FETs Using High-Pressure Hydrogen Annealing. IEEE Journal of the Electron Devices Society, 2018, 6, 164-168. | 2.1 | 25 |
| 54 | Chemically induced Fermi level pinning effects of high-k dielectrics on graphene. Scientific Reports, 2018, 8, 2992. | 3.3 | 5 |

| # | Article | IF | Citations |
|----|--|------|-----------|
| 55 | Tunable graphene doping by modulating the nanopore geometry on a SiO ₂ /Si substrate. RSC Advances, 2018, 8, 9031-9037. | 3.6 | 8 |
| 56 | Graphene–ZnO:N barristor on a polyethylene naphthalate substrate. AIP Advances, 2018, 8, . | 1.3 | 5 |
| 57 | Flexible Transparent Nanogenerators Utilizing Shapeâ€Modulated ZnO Nanorod Arrays on Graphene Electrodes. Advanced Materials Technologies, 2018, 3, 1700355. | 5.8 | 10 |
| 58 | Generalized Scheme for High Performing Photodetectors with a pâ€Type 2D Channel Layer and nâ€Type Nanoparticles. Small, 2018, 14, 1703065. | 10.0 | 18 |
| 59 | Epitaxial Synthesis of Molybdenum Carbide and Formation of a Mo ₂ C/MoS ₂ Hybrid Structure <i>via</i> Chemical Conversion of Molybdenum Disulfide. ACS Nano, 2018, 12, 338-346. | 14.6 | 148 |
| 60 | Quantitative Analysis of Deuterium Annealing Effect on Poly-Si TFTs by Low Frequency Noise and DC $\{i\}$ \hat{a} \in " $\{V\}$ \$ Characterization. IEEE Transactions on Electron Devices, 2018, 65, 1640-1644. | 3.0 | 15 |
| 61 | Unique reliability characteristics of fully depleted silicon-on-insulator tunneling FET. Japanese Journal of Applied Physics, 2018, 57, 04FB02. | 1.5 | 1 |
| 62 | Charge transfer in graphene/polymer interfaces for CO2 detection. Nano Research, 2018, 11, 3529-3536. | 10.4 | 34 |
| 63 | High-pressure oxygen annealing of Al ₂ O ₃ passivation layer for performance enhancement of graphene field-effect transistors. Nanotechnology, 2018, 29, 055202. | 2.6 | 7 |
| 64 | Zero-Bias Operation of CVD Graphene Photodetector with Asymmetric Metal Contacts. ACS Photonics, 2018, 5, 365-370. | 6.6 | 28 |
| 65 | Facile process to clean PMMA residue on graphene using KrF laser annealing. AIP Advances, 2018, 8, . | 1.3 | 10 |
| 66 | Ternary Full Adder Using Multi-Threshold Voltage Graphene Barristors. IEEE Electron Device Letters, 2018, 39, 1948-1951. | 3.9 | 46 |
| 67 | HfO ₂ /HfS ₂ hybrid heterostructure fabricated <i>via</i> controllable chemical conversion of two-dimensional HfS ₂ . Nanoscale, 2018, 10, 18758-18766. | 5.6 | 48 |
| 68 | Veryâ€Lowâ€Temperature Integrated Complementary Grapheneâ€Barristorâ€Based Inverter for Thinâ€Film Transistor Applications. Annalen Der Physik, 2018, 530, 1800224. | 2.4 | 5 |
| 69 | Operation Mechanism of a MoS2/BP Heterojunction FET. Nanomaterials, 2018, 8, 797. | 4.1 | 11 |
| 70 | Contact resistance reduction of ZnO thin film transistors (TFTs) with saw-shaped electrode. Nanotechnology, 2018, 29, 325202. | 2.6 | 7 |
| 71 | Tailoring Crystallographic Orientations to Substantially Enhance Charge Separation Efficiency in Anisotropic BiVO ₄ Photoanodes. ACS Catalysis, 2018, 8, 5952-5962. | 11.2 | 85 |
| 72 | Effect of ribbon width on electrical transport properties of graphene nanoribbons. Nano Convergence, 2018, 5, 7. | 12.1 | 14 |

| # | Article | IF | CITATIONS |
|----|---|-------------|-----------|
| 73 | Dielectric Dispersion and High Field Response of Multilayer Hexagonal Boron Nitride. Advanced Functional Materials, 2018, 28, 1804235. | 14.9 | 38 |
| 74 | Reliability characteristics of MIM capacitor studied with \hat{l} "C-F characteristics. , 2018, , . | | 3 |
| 75 | Gateâ€Controlled Graphene–Silicon Schottky Junction Photodetector. Small, 2018, 14, e1801182. | 10.0 | 53 |
| 76 | Growth of Centimeterâ€Scale Monolayer and Few‣ayer WSe ₂ Thin Films on SiO ₂ /Si Substrate via Pulsed Laser Deposition. Advanced Materials Interfaces, 2018, 5, 1800524. | 3.7 | 23 |
| 77 | A graphene barristor using nitrogen profile controlled ZnO Schottky contacts. Nanoscale, 2017, 9, 2442-2448. | 5.6 | 22 |
| 78 | Multifunctional Homogeneous Lateral Black Phosphorus Junction Devices. Chemistry of Materials, 2017, 29, 3143-3151. | 6.7 | 23 |
| 79 | Sulfur vacancy-induced reversible doping of transition metal disulfides via hydrazine treatment. Nanoscale, 2017, 9, 9333-9339. | 5. 6 | 66 |
| 80 | Wafer-Scale Integration of Highly Uniform and Scalable MoS ₂ Transistors. ACS Applied Materials & ACS Applied Materi | 8.0 | 32 |
| 81 | Pulsed KrF laser-assisted direct deposition of graphitic capping layer for Cu interconnect. Carbon, 2017, 123, 307-310. | 10.3 | 8 |
| 82 | Template-engineered epitaxial BiVO ₄ photoanodes for efficient solar water splitting. Journal of Materials Chemistry A, 2017, 5, 18831-18838. | 10.3 | 42 |
| 83 | Time Domain Reflectometry Analysis of the Dispersion of Metal–Insulator–Metal Capacitance. IEEE Electron Device Letters, 2017, 38, 521-524. | 3.9 | 4 |
| 84 | Fermi level modulation at the interface of graphene and metal., 2017,,. | | 0 |
| 85 | Two-Dimensional Atomic-Layered Alloy Junctions for High-Performance Wearable Chemical Sensor. ACS Applied Materials & Diterfaces, 2016, 8, 19635-19642. | 8.0 | 83 |
| 86 | Demonstration of Complementary Ternary Graphene Field-Effect Transistors. Scientific Reports, 2016, 6, 39353. | 3.3 | 42 |
| 87 | Ultra-thin SiO $<$ sub $>$ 2 $<$ /sub $>$ dielectric characteristics using E-beam evaporated system on HOPG and CVD graphene. , 2016, , . | | 2 |
| 88 | A robust method for extracting the mechanical properties of thin films with rough surfaces by nanoindentation. Journal of Materials Research, 2016, 31, 3777-3785. | 2.6 | 5 |
| 89 | Highly Bendable In-Ga-ZnO Thin Film Transistors by Using a Thermally Stable Organic Dielectric Layer. Scientific Reports, 2016, 6, 37764. | 3.3 | 35 |
| 90 | Monolayer MoS2 metal insulator transition based memcapacitor modeling with extension to a ternary device. AIP Advances, 2016, 6, . | 1.3 | 15 |

| # | Article | IF | Citations |
|-----|--|------|-----------|
| 91 | Design of Ratioless Ternary Inverter Using Graphene Barristor. , 2016, , . | | 6 |
| 92 | Reduction of lowâ€frequency noise in multilayer MoS ₂ FETs using a Fermiâ€level depinning layer. Physica Status Solidi - Rapid Research Letters, 2016, 10, 634-638. | 2.4 | 14 |
| 93 | Alloyed 2D Metal–Semiconductor Heterojunctions: Origin of Interface States Reduction and Schottky Barrier Lowering. Nano Letters, 2016, 16, 5928-5933. | 9.1 | 57 |
| 94 | Robust and stretchable indium gallium zinc oxide-based electronic textiles formed by cilia-assisted transfer printing. Nature Communications, 2016, 7, 11477. | 12.8 | 73 |
| 95 | Origin of the channel width dependent field effect mobility of graphene field effect transistors. Microelectronic Engineering, 2016, 163, 55-59. | 2.4 | 4 |
| 96 | Barrier height reconfiguration of graphene/ZnO:N barristor using ferroelectric polymer., 2016,,. | | 0 |
| 97 | Complementary Unipolar WS ₂ Fieldâ€Effect Transistors Using Fermiâ€Level Depinning Layers. Advanced Electronic Materials, 2016, 2, 1500278. | 5.1 | 28 |
| 98 | Alloyed 2D Metal–Semiconductor Atomic Layer Junctions. Nano Letters, 2016, 16, 1890-1895. | 9.1 | 77 |
| 99 | Hot-Carrier Instability of nMOSFETs Under Pseudorandom Bit Sequence Stress. IEEE Electron Device Letters, 2016, 37, 366-368. | 3.9 | 7 |
| 100 | Shear stress-induced enhancement of the piezoelectric properties of PVDF-TrFE thin films. Organic Electronics, 2016, 28, 67-72. | 2.6 | 29 |
| 101 | Study on future electronic device using graphene. Vacuum Magazine, 2016, 3, 22-31. | 0.0 | 0 |
| 102 | Low-temperature-grown continuous graphene films from benzene by chemical vapor deposition at ambient pressure. Scientific Reports, 2015, 5, 17955. | 3.3 | 108 |
| 103 | Metal Decoration Effects on the Gas-Sensing Properties of 2D Hybrid-Structures on Flexible Substrates. Sensors, 2015, 15, 24903-24913. | 3.8 | 41 |
| 104 | Patterned catalyst arrays of Pd/SnO ₂ core–shell nanowires for electrooxidations of biomass-derived alcohols. Journal of Materials Chemistry A, 2015, 3, 13492-13499. | 10.3 | 13 |
| 105 | Graphene transfer in vacuum yielding a high quality graphene. Carbon, 2015, 93, 286-294. | 10.3 | 33 |
| 106 | Oxide based nanoscale analog synapse device for neural signal recognition system., 2015,,. | | 30 |
| 107 | The variation of the enhanced PL efficiency of Y 2 O 3 :Eu 3+ phosphor films with the height to the ZrO 2 nanoparticle-assisted 2D PCL by reverse nano-imprint lithography. Microelectronic Engineering, 2015, 136, 48-50. | 2.4 | 3 |
| 108 | A facile process to achieve hysteresis-free and fully stabilized graphene field-effect transistors. Nanoscale, 2015, 7, 4013-4019. | 5.6 | 25 |

| # | Article | IF | Citations |
|-----|---|------|-----------|
| 109 | Bifunctional Sensing Characteristics of Chemical Vapor Deposition Synthesized Atomic-Layered MoS ₂ . ACS Applied Materials & Interfaces, 2015, 7, 2952-2959. | 8.0 | 162 |
| 110 | Extraction of the Interface State Density of Top-Gate Graphene Field-Effect Transistors. IEEE Electron Device Letters, 2015, 36, 408-410. | 3.9 | 16 |
| 111 | Charge-transfer-based Gas Sensing Using Atomic-layer MoS2. Scientific Reports, 2015, 5, 8052. | 3.3 | 489 |
| 112 | Extraction of Effective Mobility from nMOSFETs With Leaky Gate Dielectric Using Time Domain Reflectometry. IEEE Transactions on Electron Devices, 2015, 62, 1092-1097. | 3.0 | 1 |
| 113 | Chemical Sensing of 2D Graphene/MoS ₂ Heterostructure device. ACS Applied Materials & amp; Interfaces, 2015, 7, 16775-16780. | 8.0 | 375 |
| 114 | Electronic system with memristive synapses for pattern recognition. Scientific Reports, 2015, 5, 10123. | 3.3 | 133 |
| 115 | Contact resistance improvement by the modulation of peripheral length to area ratio of graphene contact pattern. Applied Physics Letters, 2015, 106, . | 3.3 | 11 |
| 116 | Neuromorphic Hardware System for Visual Pattern Recognition With Memristor Array and CMOS Neuron. IEEE Transactions on Industrial Electronics, 2015, 62, 2410-2419. | 7.9 | 231 |
| 117 | Quantitatively estimating defects in graphene devices using discharge current analysis method. Scientific Reports, 2015, 4, 4886. | 3.3 | 15 |
| 118 | Leakage current limit of time domain reflectometry in ultrathin dielectric characterization. Japanese Journal of Applied Physics, 2014, 53, 08LC02. | 1.5 | 2 |
| 119 | Towards three-dimensional integration of two-dimensional active logic circuits using low temperature multilayer stacking of GFETs. , 2014, , . | | 0 |
| 120 | Performance prospect of graphene barristor with high on-off ratio (∼10 ⁷)., 2014,,. | | 1 |
| 121 | Optimized integration processes to achieve highly stable CVD graphene FETs. , 2014, , . | | 1 |
| 122 | Ultraviolet emission from a multi-layer graphene/MgZnO/ZnO light-emitting diode. Applied Physics Letters, 2014, 104, 051120. | 3.3 | 18 |
| 123 | A nitrogen-treated memristive device for tunable electronic synapses. Semiconductor Science and Technology, 2014, 29, 104006. | 2.0 | 10 |
| 124 | Highly sensitive wide bandwidth photodetectors using chemical vapor deposited graphene. Applied Physics Letters, 2014, 104, . | 3.3 | 20 |
| 125 | Rigid substrate process to achieve high mobility in graphene field-effect transistors on a flexible substrate. Carbon, 2014, 68, 791-797. | 10.3 | 23 |
| 126 | Triangular-Pulse Measurement for Hysteresis of High-Performance and Flexible Graphene Field-Effect Transistors. IEEE Electron Device Letters, 2014, 35, 277-279. | 3.9 | 1 |

| # | Article | IF | Citations |
|-----|---|------|-----------|
| 127 | A Correlation Between Oxygen Vacancies and Reliability Characteristics in a Single Zirconium Oxide Metal-Insulator-Metal Capacitor. IEEE Transactions on Electron Devices, 2014, 61, 2619-2627. | 3.0 | 11 |
| 128 | Quantitative analysis of interfacial reactions at a graphene/SiO2 interface using the discharge current analysis method. Applied Physics Letters, 2014, 104, 151604. | 3.3 | 6 |
| 129 | Neuromorphic speech systems using advanced ReRAM-based synapse. , 2013, , . | | 118 |
| 130 | Quantitative analysis of hysteretic reactions at the interface of graphene and SiO2 using the short pulse l–V method. Carbon, 2013, 60, 453-460. | 10.3 | 51 |
| 131 | Mechanism of the effects of low temperature Al2O3 passivation on graphene field effect transistors. Carbon, 2013, 53, 182-187. | 10.3 | 53 |
| 132 | Characteristics of a pressure sensitive touch sensor using a piezoelectric PVDF-TrFE/MoS ₂ stack. Nanotechnology, 2013, 24, 475501. | 2.6 | 39 |
| 133 | Novel multi-bit memory device using metal/PVDF–TrFE/graphene stack. Microelectronic Engineering, 2013, 109, 87-89. | 2.4 | 11 |
| 134 | Correlation between the hysteresis and the initial defect density of graphene. Applied Physics Letters, 2013, 103, 083110. | 3.3 | 14 |
| 135 | Subâ€10 nm Graphene Nanoribbon Array Fieldâ€Effect Transistors Fabricated by Block Copolymer Lithography. Advanced Materials, 2013, 25, 4723-4728. | 21.0 | 150 |
| 136 | Intrinsic photocurrent characteristics of graphene photodetectors passivated with Al_2O_3. Optics Express, 2013, 21, 23391. | 3.4 | 28 |
| 137 | Development of a Semiempirical Compact Model for DC/AC Cell Operation of <formula formulatype="inline"><tex notation="TeX">\${m HfO}_{m{x}}\$</tex></formula> -Based ReRAMs. IEEE Electron Device Letters, 2013, 34, 1133-1135. | 3.9 | 14 |
| 138 | Comprehensive study for RF interference limited 3D TSV optimization. , 2013, , . | | 3 |
| 139 | Highly Flexible and Transparent Multilayer MoS ₂ Transistors with Graphene Electrodes. Small, 2013, 9, 3295-3300. | 10.0 | 189 |
| 140 | Influence of extrinsic factors on accuracy of mobility extraction in graphene metal-oxide-semiconductor field effect transistors. Applied Physics Letters, 2013, 102, . | 3.3 | 16 |
| 141 | Effects of multi-layer graphene capping on Cu interconnects. Nanotechnology, 2013, 24, 115707. | 2.6 | 66 |
| 142 | Ferroelectric polymer-gated graphene memory with high speed conductivity modulation. Nanotechnology, 2013, 24, 175202. | 2.6 | 30 |
| 143 | Nanoscale RRAM-based synaptic electronics: toward a neuromorphic computing device. Nanotechnology, 2013, 24, 384009. | 2.6 | 103 |
| 144 | Intrinsic Time Zero Dielectric Breakdown Characteristics of HfAlO Alloys. IEEE Transactions on Electron Devices, 2013, 60, 3683-3689. | 3.0 | 11 |

| # | Article | IF | Citations |
|-----|---|------|-----------|
| 145 | Characteristics of light-induced electron transport from P3HT to ZnO-nanowire field-effect transistors. Applied Physics Letters, 2013, 103, 223305. | 3.3 | 9 |
| 146 | Indicators of mobility extraction error in bottom gate CdS metal-oxide-semiconductor field-effect transistors. Applied Physics Letters, 2012, 101, . | 3.3 | 4 |
| 147 | Capacitance Analysis of Highly Leaky \$hbox{Al}_{2} hbox{O}_{3}\$ MIM Capacitors Using Time Domain Reflectometry. IEEE Electron Device Letters, 2012, 33, 1303-1305. | 3.9 | 3 |
| 148 | Selfâ€formed Schottky barrier induced selectorâ€less RRAM for crossâ€point memory applications. Physica Status Solidi - Rapid Research Letters, 2012, 6, 454-456. | 2.4 | 31 |
| 149 | RRAM-based synapse for neuromorphic system with pattern recognition function. , 2012, , . | | 108 |
| 150 | Effects of gate process on NBTI characteristics of TiN gate FinFET., 2012,,. | | 2 |
| 151 | Flexible organic solar cells composed of P3HT:PCBM using chemically doped graphene electrodes. Nanotechnology, 2012, 23, 344013. | 2.6 | 119 |
| 152 | Process-Dependent N/PBTI Characteristics of TiN Gate FinFETs. IEEE Electron Device Letters, 2012, 33, 937-939. | 3.9 | 17 |
| 153 | Au nanoparticle-decorated graphene electrodes for GaN-based optoelectronic devices. Applied Physics Letters, 2012, 101, . | 3.3 | 48 |
| 154 | Correlation of low frequency noise characteristics with the interfacial charge exchange reaction at graphene devices. Carbon, 2012, 50, 4046-4051. | 10.3 | 13 |
| 155 | Variability and feasibility of CVD graphene interconnect. , 2011, , . | | 5 |
| 156 | Enhanced Current Drivability of CVD Graphene Interconnect in Oxygen-Deficient Environment. IEEE Electron Device Letters, 2011, 32, 1591-1593. | 3.9 | 24 |
| 157 | Feasibility Study of $\frac{Mo}{SiO}_{x}/hbox{Pt}$ Resistive Random Access Memory in an Inverter Circuit for FPGA Applications. IEEE Electron Device Letters, 2011, 32, 1665-1667. | 3.9 | 4 |
| 158 | Fast transient charging at the graphene/SiO2 interface causing hysteretic device characteristics. Applied Physics Letters, 2011, 98, . | 3.3 | 122 |
| 159 | Selected Peer-Reviewed Articles from 2010 International Conference on Nanoscience and Nanotechnology (ICNST 2010). Journal of Nanoscience and Nanotechnology, 2011, 11, 7050-7052. | 0.9 | 0 |
| 160 | A comparative study of depth profiling of interface states using charge pumping and low frequency noise measurement in SiO2/HfO2 gate stack nMOSFETs. Microelectronic Engineering, 2011, 88, 3411-3414. | 2.4 | 0 |
| 161 | Excellent resistive switching in nitrogen-doped Ge2Sb2Te5 devices for field-programmable gate array configurations. Applied Physics Letters, 2011, 99, 192110. | 3.3 | 21 |
| 162 | Electrical characteristics of wrinkle-free graphene formed by laser graphitization of 4H-SiC. Applied Physics Letters, 2011, 99, 082111. | 3.3 | 13 |

| # | Article | IF | Citations |
|-----|--|-----|-----------|
| 163 | Nano-Electromechanical Switch-CMOS Hybrid Technology and Its Applications. Journal of Nanoscience and Nanotechnology, 2011, 11, 256-261. | 0.9 | 1 |
| 164 | Enhanced characteristics of pentacene field-effect transistors with graphene electrodes and substrate treatments. Applied Physics Letters, 2011, 99, 083306. | 3.3 | 24 |
| 165 | Investigation of Random Telegraph Noise in Gate-Induced Drain Leakage and Gate Edge Direct Tunneling Currents of High-\$k\$ MOSFETs. IEEE Transactions on Electron Devices, 2010, 57, 913-918. | 3.0 | 21 |
| 166 | Comparison of Low-Frequency Noise in Channel and Gate-Induced Drain Leakage Currents of High-\$k\$ nFETs. IEEE Electron Device Letters, 2010, 31, 1086-1088. | 3.9 | 8 |
| 167 | New Hot-Carrier Injection Mechanism at Source Side in Nanoscale Floating-Body MOSFETs. IEEE Electron Device Letters, 2009, 30, 54-56. | 3.9 | 1 |
| 168 | Effective Carrier Mobility Extraction Based on RF Modeling for Highly Leaky MOSFET Devices with Short Channel Length and Small Area. , 2009, , . | | 1 |
| 169 | Performance and reliability improvement of HfSiON gate dielectrics using chlorine plasma treatment. Applied Physics Letters, 2009, 94, 042911. | 3.3 | 2 |
| 170 | Unified TDDB model for stacked high-k dielectrics. , 2009, , . | | 4 |
| 171 | Hot carrier degradation in HfSiONâ^•TiN fin shaped field effect transistor with different substrate orientations. Journal of Vacuum Science & Technology B, 2009, 27, 468. | 1.3 | 19 |
| 172 | Pulsed \$I_{d}\$– \$V_{g}\$ Methodology and Its Application to Electron-Trapping Characterization and Defect Density Profiling. IEEE Transactions on Electron Devices, 2009, 56, 1322-1329. | 3.0 | 65 |
| 173 | Comparison of La-based high-k dielectrics: HfLaSiON and HfLaON. Microelectronic Engineering, 2009, 86, 268-271. | 2.4 | 3 |
| 174 | A Study of Strain Engineering Using CESL Stressor on Reliability Comparing Effect of Intrinsic Mechanical Stress. IEEE Electron Device Letters, 2009, 30, 760-762. | 3.9 | 15 |
| 175 | Rotor saliency improved structural design for cost reduction in single-phase line-start permanent magnet motor., 2009,,. | | 10 |
| 176 | Estimation of Magnet Reduction in Single-Phase Line-Start Permanent Magnet Synchronous Motor. , 2009, , . | | 4 |
| 177 | Dominant Device Instability Mechanism in Scaled Metal–Oxide–Semiconductor Field-Effect Transistors with Hafnium Oxide Dielectric. Japanese Journal of Applied Physics, 2009, 48, 091404. | 1.5 | 2 |
| 178 | Time-Dependent Dielectric Breakdown of <formula formulatype="inline"><tex Notation="TeX">\$hbox{La}_{2} hbox{O}_{3}\$</tex </formula> -Doped High- <formula formulatype="inline"><tex notation="TeX">\$k\$</tex>/Metal Gate Stacked NMOSFETs. IEEE Electron Device Letters, 2009, 30, 298-301.</formula | 3.9 | 7 |
| 179 | Reliability of La-Doped Hf-Based Dielectrics nMOSFETs. IEEE Transactions on Device and Materials Reliability, 2009, 9, 171-179. | 2.0 | 20 |
| 180 | Stress field analysis to understand the breakdown characteristics of stacked high-k dielectrics. Applied Physics Letters, 2009, 94, 162904. | 3.3 | 17 |

| # | Article | IF | CITATIONS |
|-----|--|-----|-----------|
| 181 | Effect of Si interlayer thickness and post-metallization annealing on Ge MOS capacitor on Ge-on-Si substrate. Materials Science and Engineering B: Solid-State Materials for Advanced Technology, 2008, 154-155, 102-105. | 3.5 | 6 |
| 182 | Metal Electrode/High-\$k\$ Dielectric Gate-Stack Technology for Power Management. IEEE Transactions on Electron Devices, 2008, 55, 8-20. | 3.0 | 56 |
| 183 | Tunnel Oxide Dipole Engineering in TANOS Flash Memory for Fast Programming with Good Retention and Endurance. International Power Modulator Symposium and High-Voltage Workshop, 2008, , . | 0.0 | 3 |
| 184 | Effect of the Interfacial $\frac{50}{2}$ Layer in High- $\frac{9}{40}$ Gate Stacks on NBTI. IEEE Transactions on Device and Materials Reliability, 2008, 8, 47-61. | 2.0 | 32 |
| 185 | A comparative study of reliability and performance of strain engineering using CESL stressor and mechanical strain., 2008, , . | | 6 |
| 186 | Device and reliability improvement of HfSiON+LaOx/metal gate stacks for 22nm node application. , 2008, , . | | 6 |
| 187 | Performance and reliability characteristics of the band edge high-k/metal gate nMOSFETs with La-doped Hf-silicate gate dielectrics. , 2008, , . | | 4 |
| 188 | The Effect of Nanoscale Nonuniformity of Oxygen Vacancy on Electrical and Reliability Characteristics of \$hbox{HfO}_{2}\$ MOSFET Devices. IEEE Electron Device Letters, 2008, 29, 54-56. | 3.9 | 12 |
| 189 | Breakdown in the metal/high-k gate stack: Identifying the "weak link" in the multilayer dielectric. , 2008, , . | | 55 |
| 190 | Modulation of TiSiN effective work function using high-pressure postmetallization annealing in dilute oxygen ambient. Applied Physics Letters, 2008, 92, 263505. | 3.3 | 5 |
| 191 | Effective surface passivation methodologies for high performance germanium metal oxide semiconductor field effect transistors. Applied Physics Letters, 2008, 93, 192115. | 3.3 | 6 |
| 192 | Improved Hot Carrier Reliability Characteristics of Metal Oxide Semiconductor Field Effect Transistors with High-k Gate Dielectric by Using High Pressure Deuterium Post Metallization Annealing. Japanese Journal of Applied Physics, 2007, 46, L786. | 1.5 | 5 |
| 193 | Effects of gate edge profile on off-state leakage suppresion in metal gate/high-k dielectric n-type metal-oxide-semiconductor field effect transistors. Applied Physics Letters, 2007, 90, 183501. | 3.3 | 3 |
| 194 | Issues associated with p-type band-edge effective work function metal electrodes: Fermi-level pinning and flatband roll-off. , 2007, , . | | 1 |
| 195 | Electric-field-driven dielectric breakdown of metal-insulator-metal hafnium silicate. Applied Physics Letters, 2007, 91, . | 3.3 | 26 |
| 196 | Effects of metal gate-induced strain on the performance of metal-oxide-semiconductor field effect transistors with titanium nitride gate electrode and hafnium oxide dielectric. Applied Physics Letters, 2007, 91, . | 3.3 | 18 |
| 197 | Mechanism of Electron Trapping and Characteristics of Traps in \$hbox{HfO}_{2}\$ Gate Stacks. IEEE Transactions on Device and Materials Reliability, 2007, 7, 138-145. | 2.0 | 128 |
| 198 | Acousto-optic polarization-dependent mode coupling in a dual-mode hollow optical fiber. Optics Letters, 2007, 32, 1305. | 3.3 | 3 |

| # | Article | IF | Citations |
|-----|---|-----|-----------|
| 199 | Impact of Bottom Interfacial Layer on the Threshold Voltage and Device Reliability of Fluorine Incorporated PMOSFETS with High-K/Metal Gate., 2007,,. | | 5 |
| 200 | Reliability Assessment on Highly Manufacturable MOSFETs with Metal Gate and Hf based Gate Dielectrics. , 2007, , . | | 1 |
| 201 | Higher Permittivity Rare Earth-Doped HfO2 and ZrO2 Dielectrics for Logic and Memory Applications. , 2007, , . | | 2 |
| 202 | Gate First Metal-Aluminum-Nitride PMOS Electrodes for 32nm Low Standby Power Applications. , 2007, , . | | 9 |
| 203 | Electrical characterization and analysis techniques for the high- \hat{l}^2 era. Microelectronics Reliability, 2007, 47, 479-488. | 1.7 | 18 |
| 204 | Gate stack technology for nanoscale devices. , 2006, , . | | 0 |
| 205 | Intrinsic Threshold Voltage Instability of the HFO2 NMOS Transistors. , 2006, , . | | 26 |
| 206 | Effective work function modification of atomic-layer-deposited-TaN film by capping layer. Applied Physics Letters, 2006, 89, 032113. | 3.3 | 41 |
| 207 | High Performance Metal Gate CMOSFETs with Aggressively Scaled Hf-Based High-k. ECS Transactions, 2006, 1, 609-623. | 0.5 | 6 |
| 208 | Effects of ALD TiN Metal Gate Thickness on Metal Gate /High-k Dielectric SOI FinFET Characteristics. SOI Conference, Proceedings of the IEEE International, 2006, , . | 0.0 | 6 |
| 209 | Impact of Nitrogen on PBTI Characteristics of HfSiON/TiN Gate Stacks. , 2006, , . | | 5 |
| 210 | Systematic Gate Stack Optimization to Maximize Mobility with HfSiON EOT Scaling. , 2006, , . | | 0 |
| 211 | Decoupling of cold-carrier effects in hot-carrier reliability assessment of HfO/sub 2/ gated nMOSFETs. IEEE Electron Device Letters, 2006, 27, 662-664. | 3.9 | 15 |
| 212 | Relationship of HfO2 Material Properties and Transistor Performance. International Power Modulator Symposium and High-Voltage Workshop, 2006, , . | 0.0 | 3 |
| 213 | A novel in situ plasma treatment for damage-free metal/high-k gate stack RIE process. , 2006, , . | | 4 |
| 214 | Electron Trapping Processes in High- \hat{A}_i Gate Dielectrics and Nature of Traps. International Power Modulator Symposium and High-Voltage Workshop, 2006, , . | 0.0 | 1 |
| 215 | A Novel Bias Temperature Instability Characterization Methodology for High-k MOSFETs. Solid-State Device Research Conference, 2008 ESSDERC 2008 38th European, 2006, , . | 0.0 | 6 |
| 216 | Impact of Metal Wet Etch on Device Characteristics and Reliability for Dual Metal Gate/High-k CMOS. , 2006, , . | | 1 |

| # | Article | IF | CITATIONS |
|-----|--|------|-----------|
| 217 | Comparison of effective work function extraction methods using capacitance and current measurement techniques. IEEE Electron Device Letters, 2006, 27, 598-601. | 3.9 | 32 |
| 218 | Negative oxygen vacancies in HfO2 as charge traps in high-k stacks. Applied Physics Letters, 2006, 89, 082908. | 3.3 | 295 |
| 219 | The effect of interfacial layer properties on the performance of Hf-based gate stack devices. Journal of Applied Physics, 2006, 100, 094108. | 2.5 | 135 |
| 220 | Gate stack technology for nanoscale devices. Materials Today, 2006, 9, 32-40. | 14.2 | 463 |
| 221 | Characterization and reliability measurement issues in devices with novel gate stack devices. Thin Solid Films, 2006, 504, 223-226. | 1.8 | 2 |
| 222 | Highly manufacturable advanced gate-stack technology for sub-45-nm self-aligned gate-first CMOSFETs. IEEE Transactions on Electron Devices, 2006, 53, 979-989. | 3.0 | 18 |
| 223 | Negative Bias Temperature Instability Dependence on Dielectric Thickness and Nitrogen Concentration in Ultra-scaled HfSiON Dielectric/TiN Gate Stacks. Japanese Journal of Applied Physics, 2006, 45, 2945-2948. | 1.5 | 6 |
| 224 | Atomic Layer Deposited HfO2 and HfSiO to Enable CMOS Gate Dielectric Scaling, Mobility, and VTH Stability. ECS Transactions, 2006, 1 , 15 -28. | 0.5 | 3 |
| 225 | Challenges in Dual Workfunction Metal Gate CMOS Integration. ECS Transactions, 2006, 3, 263-274. | 0.5 | 0 |
| 226 | Tunable bandpass filter by concatenated microstructured fiber-hollow optical fiber based on bend-loss edge shift. , 2006, , . | | 0 |
| 227 | | | |
| | | | |

| # | Article | IF | CITATIONS |
|-----|--|-----|-----------|
| 235 | Probing stress effects in HfO2 gate stacks with time dependent measurements. Microelectronics Reliability, 2005, 45, 806-810. | 1.7 | 13 |
| 236 | Enhanced Surface Preparation Techniques for the Si/High-k Interface. Solid State Phenomena, 2005, 103-104, 11-14. | 0.3 | 3 |
| 237 | Trapping/De-Trapping Gate Bias Dependence of Hf-Silicate Dielectrics with Poly and TiN Gate Electrode. Japanese Journal of Applied Physics, 2005, 44, 2420-2423. | 1.5 | 14 |
| 238 | Temperature Effects of Constant Bias Stress on n-Channel FETs with Hf-based Gate Dielectric. Japanese Journal of Applied Physics, 2005, 44, 2201-2204. | 1.5 | 1 |
| 239 | Atomic Layer Deposition of High k Dielectric and Metal Gate Stacks for MOS Devices. AIP Conference Proceedings, 2005, , . | 0.4 | 5 |
| 240 | Charge trapping and detrapping characteristics in hafnium silicate gate stack under static and dynamic stress. IEEE Electron Device Letters, 2005, 26, 197-199. | 3.9 | 41 |
| 241 | Charge trapping and detrapping characteristics in hafnium silicate gate dielectric using an inversion pulse measurement technique. Applied Physics Letters, 2005, 87, 122901. | 3.3 | 25 |
| 242 | Thermal response of Ru electrodes in contact with SiO2 and Hf-based high-k gate dielectrics. Journal of Applied Physics, 2005, 98, 043520. | 2.5 | 24 |
| 243 | Nickel-silicide phase effects on flatband voltage shift and equivalent oxide thickness decrease of hafnium silicon oxynitride metal-silicon-oxide capacitors. Applied Physics Letters, 2005, 86, 222906. | 3.3 | 21 |
| 244 | Modulation of the work function of silicon gate electrode using thin TaN interlayers. Applied Physics Letters, 2005, 87, 052109. | 3.3 | 6 |
| 245 | Mobility evaluation in transistors with charge-trapping gate dielectrics. Applied Physics Letters, 2005, 87, 042905. | 3.3 | 37 |
| 246 | Effects of boron diffusion in pMOSFETs with TiN-HfSiO gate stack. IEEE Electron Device Letters, 2005, 26, 366-368. | 3.9 | 13 |
| 247 | Improved interface quality and charge-trapping characteristics of MOSFETs with high-/spl kappa/ gate dielectric. IEEE Electron Device Letters, 2005, 26, 725-727. | 3.9 | 34 |
| 248 | Validity of constant voltage stress based reliability assessment of high-/spl kappa/ devices. IEEE Transactions on Device and Materials Reliability, 2005, 5, 20-25. | 2.0 | 43 |
| 249 | Transient Charging and Relaxation in High-k Gate Dielectrics and Their Implications. Japanese Journal of Applied Physics, 2005, 44, 2415-2419. | 1.5 | 17 |
| 250 | Hot carrier degradation of HfSiON gate dielectrics with TiN electrode. IEEE Transactions on Device and Materials Reliability, 2005, 5, 177-182. | 2.0 | 38 |
| 251 | Charge trapping effects in high-k transistors. ECS Meeting Abstracts, 2005, , . | 0.0 | 0 |
| 252 | Effects of Structural Properties of Hf-Based Gate Stack on Transistor Performance. Materials Research Society Symposia Proceedings, 2004, 811, 176. | 0.1 | 27 |

| # | Article | IF | CITATIONS |
|-----|--|-----|-----------|
| 253 | Interfacial Layer-Induced Mobility Degradation in High-kTransistors. Japanese Journal of Applied Physics, 2004, 43, 7899-7902. | 1.5 | 70 |
| 254 | Resonant optical nonlinearity measurement of Yb^3+/Al^3+ codoped optical fibers by use of a long-period fiber grating pair. Optics Letters, 2002, 27, 580. | 3.3 | 37 |
| 255 | Spectroscopic ellipsometry characterization of high-k dielectric HfO2 thin films and the high-temperature annealing effects on their optical properties. Applied Physics Letters, 2002, 80, 1249-1251. | 3.3 | 148 |
| 256 | <title>Torsion sensing characteristics of optical fiber with a long-period grating pair <math display="inline"></math> /title>. , 2001, 4579, 154.</td><td></td><td>5</td></tr><tr><td>257</td><td>Fibre-optic sensing applications of a pair of long-period fibre gratings. Measurement Science and Technology, 2001, 12, 778-781.</td><td>2.6</td><td>77</td></tr><tr><td>258</td><td>Ultrathin zirconium silicate film with good thermal stability for alternative gate dielectric application. Applied Physics Letters, 2000, 77, 1704-1706.</td><td>3.3</td><td>108</td></tr><tr><td>259</td><td>Electrical and reliability characteristics of ZrO2 deposited directly on Si for gate dielectric application. Applied Physics Letters, 2000, 77, 3269-3271.</td><td>3.3</td><td>165</td></tr><tr><td>260</td><td>Electrical characteristics of highly reliable ultrathin hafnium oxide gate dielectric. IEEE Electron Device Letters, 2000, 21, 181-183.</td><td>3.9</td><td>214</td></tr><tr><td>261</td><td>Thermal stability and electrical characteristics of ultrathin hafnium oxide gate dielectric reoxidized with rapid thermal annealing. Applied Physics Letters, 2000, 76, 1926-1928.</td><td>3.3</td><td>581</td></tr><tr><td>262</td><td>Effects of interfacial layer growth on the electrical characteristics of thin titanium oxide films on silicon. Applied Physics Letters, 1999, 74, 3143-3145.</td><td>3.3</td><td>114</td></tr><tr><td>263</td><td>Nitrogen (N<sub>2</sub>) Implantation to Suppress Growth of Interfacial Oxide in Mocvd Bst and Sputtered Bst Films. Materials Research Society Symposia Proceedings, 1999, 567, 521.</td><td>0.1</td><td>6</td></tr><tr><td>264</td><td>Study on ZrO2 Deposited Directly on Si as an Alternative Gate Dielectric Material. Materials Research Society Symposia Proceedings, 1999, 606, 263.</td><td>0.1</td><td>1</td></tr><tr><td>265</td><td>Alternative Gate Dielectrics with BST/TIO2/(Barrier Oxide) Stacked Structure. Materials Research Society Symposia Proceedings, 1998, 525, 193.</td><td>0.1</td><td>3</td></tr><tr><td>266</td><td>Effective minimization of charge trapping in high-K gate dielectrics with an ultra-short pulse technique. , <math>0</math>, , .</td><td></td><td>1</td></tr><tr><td>267</td><td>Effect of barrier layer on the electrical and reliability characteristics of high-k gate dielectric films. , 0, , .</td><td></td><td>8</td></tr><tr><td>268</td><td>Ultrathin hafnium oxide with low leakage and excellent reliability for alternative gate dielectric application. , <math>0</math>, , .</td><td></td><td>45</td></tr><tr><td>269</td><td>Performance of MOSFETs with ultra thin ZrO/sub 2/ and Zr silicate gate dielectrics. , 0, , .</td><td></td><td>10</td></tr><tr><td>270</td><td>Single-layer thin HfO/sub 2/ gate dielectric with n+-polysilicon gate., 0,,.</td><td></td><td>19</td></tr></tbody></table></title> | | |

| # | Article | IF | CITATIONS |
|-----|--|----|-----------|
| 271 | MOSFET devices with polysilicon on single-layer HfO/sub 2/ high-K dielectrics. , 0, , . | | 31 |
| 272 | Integration issues of high-k gate stack: Process-induced charging. , 0, , . | | 7 |
| 273 | Localized transient charging and it's implication on the hot carrier reliability of HfSiON MOSFETs. , 0, | | 14 |
| 274 | Charge Trapping Dependence on the Physical Structure of Ultra-thin ALD-HfSiON/TiN Gate Stacks. , 0, , . | | 1 |