

# Kiat Seng Yeo

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/4561755/publications.pdf>

Version: 2024-02-01

483  
papers

5,759  
citations

81900

39  
h-index

144013

57  
g-index

487  
all docs

487  
docs citations

487  
times ranked

3646  
citing authors

#	ARTICLE	IF	CITATIONS
1	Nanowatt Receiver for High-Data-Rate Advanced Internet of Things and Microwave Applications: A Novel Exploitation of Body Bias and Stage Ratios in a Dickson Detector.. IEEE Nanotechnology Magazine, 2022, , 2-11.	1.3	0
2	Current Harmonics Analysis and Design for Load-Independent ZVS Single-Switch Resonant DC/DC Converter. IEEE Transactions on Power Electronics, 2022, 37, 10877-10888.	7.9	2
3	A Large Dynamic Range Reconfigurable Interpolation Digital Transmitter for NB-IoT Applications. IEEE Microwave and Wireless Components Letters, 2022, 32, 744-747.	3.2	2
4	A Monolithically Integrated Single-Input Load-Modulated Balanced Amplifier With Enhanced Efficiency at Power Back-Off. IEEE Journal of Solid-State Circuits, 2021, 56, 1553-1564.	5.4	17
5	Decentralized and Lightweight Approach to Detect Eclipse Attacks on Proof of Work Blockchains. IEEE Transactions on Network and Service Management, 2021, 18, 1659-1672.	4.9	19
6	A 60 GHz 8-Way Combined Power Amplifier in 0.18 $\mu\text{m}$ SiGe BiCMOS. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 1847-1851.	3.0	5
7	Ku-Band Bidirectional Mixer with Directional Control. , 2021, , .		0
8	A digital background calibration scheme for non-linearity of SAR ADC using back-propagation algorithm. Microelectronics Journal, 2021, 114, 105113.	2.0	7
9	Heterogeneous Integration: A Promising Technology to Future Integrated Power Conversion Electronics. IEEE Power Electronics Magazine, 2021, 8, 37-47.	0.7	7
10	A 24.6-32.5 GHz Millimeter-wave Frequency Synthesizer for 5G Wireless and 60 GHz Applications. , 2021, , .		3
11	A New Degeneration Technique for 60 GHz Triple Cascode Wideband Low Noise Amplifier. , 2021, , .		0
12	A 4 GHz Single-to-Differential Cross-Coupled Variable-Gain Transimpedance Amplifier for Optical Communication. Electronics (Switzerland), 2021, 10, 3042.	3.1	2
13	Ka-Band Marchand Balun with Edge- and Broadside-Coupled Hybrid Configuration. Electronics (Switzerland), 2020, 9, 1116.	3.1	5
14	Design of Differential Variable-Gain Transimpedance Amplifier in 0.18 $\mu\text{m}$ SiGe BiCMOS. Electronics (Switzerland), 2020, 9, 1058.	3.1	4
15	Design of a Ka-Band U-Shaped Bandpass Filter with 20-GHz Bandwidth in 0.13- $\mu\text{m}$ BiCMOS Technology. Electronics (Switzerland), 2020, 9, 1608.	3.1	3
16	A Two-Stage X-Band 20.7-dBm Power Amplifier in 40-nm CMOS Technology. Electronics (Switzerland), 2020, 9, 2198.	3.1	3
17	92.5% Average Power Efficiency Fully Integrated Floating Buck Quasi-Resonant LED Drivers Using GaN FETs. Electronics (Switzerland), 2020, 9, 575.	3.1	3
18	Design of a Wideband Variable-Gain Amplifier With Self-Compensated Transistor for Accurate dB-Linear Characteristic in 65 nm CMOS Technology. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 4187-4198.	5.4	18

#	ARTICLE	IF	CITATIONS
19	Robustness-oriented Pâ€band phased array radar frontâ€end with high phase and gain resolution in 0.18 BiCMOS. IET Microwaves, Antennas and Propagation, 2020, 14, 960-966.	1.4	1
20	A data-dependent energy reduction algorithm for SAR ADC using self-adaptive window. Microelectronics Journal, 2020, 100, 104754.	2.0	7
21	Ultra-Low Power Receiver Architecture with Enhanced Input Signal Swing for Improved Sensitivity. , 2020, , .		0
22	CMOS Transformer Design for X-band Power Amplifier Applications. , 2020, , .		2
23	A Multi-Mode Multi-Coil Coupled Tuned Inductive Peaking ILFD for Low Injected Power With Compact Size. IEEE Access, 2019, 7, 59059-59068.	4.2	4
24	Design of Reconfigurable dB-Linear Variable-Gain Amplifier and Switchable-Order $g_{\{m\}}$ -C Filter in 65-nm CMOS Technology. IEEE Transactions on Microwave Theory and Techniques, 2019, 67, 5148-5158.	4.6	20
25	Millimeter-wave Sine Corrugated Fermi Tapered Slot Antenna Array Based on Partial Synthesized Dielectric. , 2019, , .		0
26	Design and Characterization of Micro-LED Matrix Display With Heterogeneous Integration of GaN and BCD Technologies. IEEE Transactions on Electron Devices, 2019, 66, 4221-4227.	3.0	24
27	A Reliability-Oriented Startup Analysis of Injection-Locked Frequency Divider Based on Broken Symmetry Theory. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 2954-2958.	3.1	3
28	A Wideband dB-Linear VGA With Temperature Compensation and Active Load. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 3279-3287.	5.4	20
29	An Inductorless 6-GHz Variable Gain Differential Transimpedance Amplifier in 0.18- $\frac{1}{4}$ m SiGe BiCMOS. , 2019, , .		1
30	Design of a Voltage-Controlled Programmable-Gain Amplifier in 65-nm CMOS Technology. , 2019, , .		2
31	Dual-band Bandpass Filter Design with Novel Double-layer Mixed Coupled SIR/CPW-SIR Resonators. , 2019, , .		2
32	Heterogeneous Integration of GaN and BCD Technologies. Electronics (Switzerland), 2019, 8, 351.	3.1	7
33	An Inductorless 5-GHz Differential Dual Regulated Cross-Cascode Transimpedance Amplifier using 40 nm CMOS. , 2019, , .		3
34	A K-Band Differential SiGe Stacked Power Amplifier Based on Capacitive Compensation Techniques for Gain Enhancements. , 2019, , .		1
35	A high gain 60 GHz antipodal Fermi-tapered slot antenna based on robust synthesized dielectric. Microwave and Optical Technology Letters, 2019, 61, 761-765.	1.4	4
36	Monolithic Sub-Terahertz SPDT Switches With Low Insertion Loss and Enhanced Isolation. IEEE Transactions on Terahertz Science and Technology, 2018, 8, 192-200.	3.1	22

#	ARTICLE	IF	CITATIONS
37	Design and optimization of the ring oscillator based injection locked frequency dividers. <i>Microelectronics Journal</i> , 2018, 72, 40-48.	2.0	1
38	Precompliance Test Setup for Pyroelectric Sensor Devices in IoT Applications. , 2018, , .		1
39	Real-Time Audio Transmission Using Visible Light Communication. , 2018, , .		6
40	Evaluation of Low Voltage Rectifier Design Using IGBT, MOSFET, and GaN FETs. , 2018, , .		4
41	Low-cost Real-time Video Streaming System Using Off-the-Shelf LEDs. , 2018, , .		0
42	A $V_{\text{GS}}$ -Band Wide Locking Range Divide-by-4 Injection-Locked Frequency Divider. <i>IEEE Microwave and Wireless Components Letters</i> , 2018, 28, 1020-1022.	3.2	9
43	Miniaturized Wideband Coupler for 60-GHz Band in 65-nm CMOS Technology. <i>IEEE Microwave and Wireless Components Letters</i> , 2018, 28, 1089-1091.	3.2	6
44	A Multi-Mode Compact Size Multi-Coil Tuned Inductive Peaking ILFD for Low Injected Power Level. , 2018, , .		2
45	Miniature Wind Energy Harvester Based on Voltage Multipliers. , 2018, , .		2
46	A wide locking range harmonic enhanced injection locked frequency divide-by-4 with low injected power level (Invited). , 2018, , .		2
47	Heterogeneous Integration of GaN LED on CMOS Driver Circuit for Mobile Phone Applications. , 2018, , .		4
48	A 2.4-mW 2.5-GHz multi-phase clock generator with duty cycle imbalance correction in 0.13- $\mu\text{m}$ CMOS. <i>The Integration VLSI Journal</i> , 2018, 63, 87-92.	2.1	1
49	A 45-75 GHz Vector Modulator MMIC With Built-In Voltage Converter. <i>IEEE Microwave and Wireless Components Letters</i> , 2017, 27, 515-517.	3.2	6
50	Ultra-Wideband Low-Loss Switch Design in High-Resistivity Trap-Rich SOI With Enhanced Channel Mobility. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2017, 65, 3937-3949.	4.6	45
51	Wideband millimetre-wave CMOS power amplifier using transistor-based inductive source degeneration and specially shielded transformer. <i>IET Microwaves, Antennas and Propagation</i> , 2017, 11, 410-416.	1.4	4
52	A 30-GHz Power-Efficient PLL Frequency Synthesizer for 60-GHz Applications. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2017, 65, 4165-4175.	4.6	24
53	A 60-GHz Coplanar Waveguide-Based Bidirectional LNA in SiGe BiCMOS. <i>IEEE Microwave and Wireless Components Letters</i> , 2017, 27, 742-744.	3.2	13
54	DC-30 GHz DPDT Switch Matrix Design in High Resistivity Trap-Rich SOI. <i>IEEE Transactions on Electron Devices</i> , 2017, 64, 3548-3554.	3.0	17

#	ARTICLE	IF	CITATIONS
55	A wideband BiCMOS variable gain amplifier with novel continuous dB-linear gain control and temperature compensation. <i>Analog Integrated Circuits and Signal Processing</i> , 2017, 90, 499-506.	1.4	4
56	The Investigation and Optimisation of Phase-Induced Amplitude Attenuation in the Injection-Locked Ring Oscillators-Based Receiver. <i>Circuits, Systems, and Signal Processing</i> , 2017, 36, 1818-1835.	2.0	1
57	Review of high efficiency integrated LED lighting. , 2017, , .		10
58	Silicon based MM wave IC design for 60 GHz and beyond. , 2017, , .		0
59	RF mixer design techniques using GaAs process. , 2017, , .		1
60	Predistortion Linearizer for Wideband AM/PM Cancellation With Left-Handed Delay Line. <i>IEEE Microwave and Wireless Components Letters</i> , 2017, 27, 794-796.	3.2	12
61	Micro-LED arrays for display and communication: Device structure and driver architecture. , 2017, , .		9
62	A Cross-Coupled Ka-Band GaAs-pHEMT Based VCO. , 2017, , .		1
63	Linear-in-decibel variable gain amplifier design in 0.18 $\mu$ m SIGE BICMOS technology. , 2016, , .		0
64	A Hybrid Pad-Line-Finger De-Embedding Technique for Broadband Modeling of CMOS Transistor. <i>IEEE Microwave and Wireless Components Letters</i> , 2016, 26, 507-509.	3.2	10
65	Millimetre-wave performance of passive microstrip bandpass filters based on 40nm CMOS technology. , 2016, , .		0
66	Millimeter-wave IC design techniques for beam-forming applications. , 2016, , .		1
67	Design of millimeter-wave transformer balun with isolation circuit in silicon based technology. , 2016, , .		1
68	A wideband digital variable gain amplifier with DC offset cancellation in SiGe 0.18 $\mu$ m BiCMOS technology. , 2016, , .		0
69	DC-50 GHz low loss switch matrix design in high resistivity trap-rich SOI. , 2016, , .		1
70	A 60-GHz power amplifier with efficiency enhancement at power back-off. , 2016, , .		2
71	Concentric Parallel Combining Balun for Millimeter-Wave Power Amplifier in Low-Power CMOS with High-Power Density. <i>Journal of Infrared, Millimeter, and Terahertz Waves</i> , 2016, 37, 1096-1105.	2.2	0
72	A multi-mode 30 GHz 2 degree RMS power efficient phase-locked loop frequency synthesizer. , 2016, , .		7

#	ARTICLE	IF	CITATIONS
73	A Compact 57-67 GHz Bidirectional LNAPA in 65-nm CMOS Technology. IEEE Microwave and Wireless Components Letters, 2016, 26, 628-630.	3.2	25
74	A 60-GHz bi-directional variable gain amplifier with microstrip-line interconnect in 65 nm CMOS. , 2016, , .		6
75	A VCO phase noise reduction technique to suppress the active device contribution. , 2016, , .		6
76	A 11.2-48 GHz Low Noise Amplifier in 65-nm CMOS Technology. Circuits, Systems, and Signal Processing, 2016, 35, 1531-1543.	2.0	2
77	A 57-to-64-GHz 0.094-mm <sup>2</sup> 5-bit Passive Phase Shifter in 65-nm CMOS. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2016, 24, 1917-1925.	3.1	54
78	Design of a 60-GHz Quasi-Yagi Antenna With Novel Ladder-Like Directors for Gain and Bandwidth Enhancements. IEEE Antennas and Wireless Propagation Letters, 2016, 15, 682-685.	4.0	41
79	A Compact High-Performance Patch Antenna Array for 60-GHz Applications. IEEE Antennas and Wireless Propagation Letters, 2016, 15, 313-316.	4.0	59
80	Coupled inductive peaking injection locked frequency divide-by-2 with wide locking range and compact size. , 2015, , .		1
81	A Gm3 cancellation bias for 60GHz Doherty Power Amplifier. , 2015, , .		0
82	A study of CMOS SOI for RF, Microwave and millimeter wave applications. , 2015, , .		4
83	A 57 to 66 GHz novel six-port correlator. , 2015, , .		0
84	Generalized multiple coupled tanks for silicon based RF/mm-wave IC (Invited). , 2015, , .		4
85	A DC-50 GHz SPDT switch with maximum insertion loss of 1.9 dB in a commercial 0.13- $\mu$ m SOI technology. , 2015, , .		8
86	Development of a miniaturized stimulation device for electrical stimulation of cells. Journal of Biological Engineering, 2015, 9, 14.	4.7	29
87	Transformer-based class-E CMOS PA with shunt LC network. , 2015, , .		0
88	A wideband BiCMOS thermal noise canceling low noise amplifier with temperature compensation. Microwave and Optical Technology Letters, 2015, 57, 2121-2125.	1.4	0
89	Novel Q-factor enhancement technique for on-chip spiral inductors and its application to cmos low-noise amplifier designs. Microwave and Optical Technology Letters, 2015, 57, 2883-2886.	1.4	2
90	A 40 GHz 65 nm CMOS Phase-Locked Loop With Optimized Shunt-Peaked Buffer. IEEE Microwave and Wireless Components Letters, 2015, 25, 34-36.	3.2	6

#	ARTICLE	IF	CITATIONS
91	A low noise amplifier in 130 nm SOI CMOS for ISM applications. , 2015, , .		0
92	A Reconfigurable K-/Ka-Band Power Amplifier With High PAE in 0.18- $\mu\text{m}$ SiGe BiCMOS for Multi-Band Applications. IEEE Transactions on Microwave Theory and Techniques, 2015, 63, 4395-4405.	4.6	24
93	A 32kb 9T SRAM with PVT-tracking read margin enhancement for ultra-low voltage operation. , 2015, , .		6
94	A compact 60 GHz LNA design with enhanced stability by layout technique in 65 nm CMOS. , 2015, , .		2
95	A 4 GHz 60 dB Variable Gain Amplifier With Tunable DC Offset Cancellation in 65 nm CMOS. IEEE Microwave and Wireless Components Letters, 2015, 25, 37-39.	3.2	25
96	High-Frequency Noise Modeling of MOSFETs for Ultra Low-Voltage RF Applications. IEEE Transactions on Microwave Theory and Techniques, 2015, 63, 141-154.	4.6	10
97	Cell-Based Variable-Gain Amplifiers With Accurate dB-Linear Characteristic in 0.18 $\mu\text{m}$ CMOS Technology. IEEE Journal of Solid-State Circuits, 2015, 50, 586-596.	5.4	91
98	A 26.8 dB Gain 19.7 dBm CMOS Power Amplifier Using 4-way Hybrid Coupling Combiner. IEEE Microwave and Wireless Components Letters, 2015, 25, 43-45.	3.2	12
99	Novel Defected Ground Structure and Two-Side Loading Scheme for Miniaturized Dual-Band SIW Bandpass Filter Designs. IEEE Microwave and Wireless Components Letters, 2015, 25, 217-219.	3.2	106
100	6.2 GHz 0.5 mW two-dimensional oscillator array-based injection-locked frequency divider in 0.18 $\mu\text{m}$ CMOS. Electronics Letters, 2015, 51, 62-63.	1.0	3
101	Imparting electroactivity to polycaprolactone fibers with heparin-doped polypyrrole: Modulation of hemocompatibility and inflammatory responses. Acta Biomaterialia, 2015, 23, 240-249.	8.3	23
102	A 3 mW 54 GHz 0.18 $\mu\text{m}$ BiCMOS voltage controlled oscillator with supply injection locking. Microwave and Optical Technology Letters, 2015, 57, 1912-1914.	1.4	1
103	2.3 A 130-to-180GHz 0.0035mm <sup>2</sup> SPDT switch with 3.3dB loss and 23.7dB isolation in 65nm bulk CMOS. , 2015, , .		13
104	A 65 nm CMOS Power Amplifier With Peak PAE above 18.9% From 57 to 66 GHz Using Synthesized Transformer-Based Matching Network. IEEE Transactions on Circuits and Systems I: Regular Papers, 2015, 62, 2533-2543.	5.4	43
105	A 220-285 GHz SPDT Switch in 65-nm CMOS Using Switchable Resonator Concept. IEEE Transactions on Terahertz Science and Technology, 2015, 5, 649-651.	3.1	24
106	Low-Crosstalk Semi-Trench-Assisted Multicore Flat Fiber. , 2015, , .		2
107	Design of a hybrid neural spike detection algorithm for implantable integrated brain circuits. , 2015, , .		4
108	Modified Inductive Peaking Direct Injection ILFD With Multi-Coupled Coils. IEEE Microwave and Wireless Components Letters, 2015, 25, 379-381.	3.2	7

#	ARTICLE	IF	CITATIONS
109	A Low Phase Noise and Wide Tuning Range Millimeter-Wave VCO Using Switchable Coupled VCO-Cores. IEEE Transactions on Circuits and Systems I: Regular Papers, 2015, 62, 554-563.	5.4	52
110	Internet of Things: Trends, challenges and applications. , 2014, , .		23
111	A hybrid CMOS clock divider for PLL of 60GHz transceiver. , 2014, , .		0
112	A 1.2-GHz bandwidth variable gain amplifier with continuous dB-linear control in 65nm CMOS. , 2014, , .		1
113	DESIGN AND ANALYSIS OF A 2.4 GHz HYBRID TYPE AUTOMATIC AMPLITUDE CONTROL VCO WITH FORWARD NOISE REDUCTION. Journal of Circuits, Systems and Computers, 2014, 23, 1450048.	1.5	0
114	A transformed radial stub low-pass filter using defected ground structure for stopband extension. , 2014, , .		0
115	Design and Optimization of a Milli-Meter Wave Amplifier Using Nano-Scale CMOS Devices. Nanoscience and Nanotechnology Letters, 2014, 6, 805-811.	0.4	0
116	Area efficient inter-coupled differential injection enhancement wide locking range injection locked frequency divider. , 2014, , .		0
117	CMOS 1:1 Transformer design for millimeter wave application. , 2014, , .		1
118	A 60-GHz on-chip antenna over an AMC using a standard 65-nm CMOS technology. , 2014, , .		3
119	A wideband 60 GHz VCO with linear tuning range. , 2014, , .		0
120	Millimeter-wave beam forming on silicon. , 2014, , .		0
121	Broadband 60GHz 32-way ring-cavity power combiner. , 2014, , .		1
122	Low power digitally variable gain amplifier techniques based on SiGe BiCMOS technology. , 2014, , .		1
123	State of the art ML sensing schemes for low-power CAM in nano-scale CMOS technologies. , 2014, , .		0
124	A 60-GHz 26.3-dB gain 5.3-dB NF low-noise amplifier in 65-nm CMOS using Q-factor enhanced inductors. , 2014, , .		2
125	Miniaturized 3-bit Phase Shifter for 60 GHz Phased-Array in 65 nm CMOS Technology. IEEE Microwave and Wireless Components Letters, 2014, 24, 50-52.	3.2	36
126	Design and Analysis of Wide Frequency-Tuning-Range CMOS 60 GHz VCO by Switching Inductor Loaded Transformer. IEEE Transactions on Circuits and Systems I: Regular Papers, 2014, 61, 699-711.	5.4	49



#	ARTICLE	IF	CITATIONS
127	Analysis and Design of Ultra-Wideband Low-Noise Amplifier With Input/Output Bandwidth Optimization and Single-Ended/Differential-Input Reconfigurability. IEEE Transactions on Industrial Electronics, 2014, 61, 5672-5680.	7.9	22
128	A hybrid NEO-based spike detection algorithm for implantable brain-IC interface applications. , 2014, , .		4
129	A 35-mW 30-dB Gain Control Range Current Mode Linear-in-Decibel Programmable Gain Amplifier With Bandwidth Enhancement. IEEE Transactions on Microwave Theory and Techniques, 2014, 62, 3465-3475.	4.6	16
130	Miniaturized 40â€“60 GHz On-Chip Balun With Capacitive Loading Compensation. IEEE Electron Device Letters, 2014, 35, 434-436.	3.9	12
131	A 35 mW 30 dB gain control range current mode programmable gain amplifier with DC offset cancellation. , 2014, , .		9
132	Substrate-Induced Noise Model and Parameter Extraction for High-Frequency Noise Modeling of Sub-Micron MOSFETs. IEEE Transactions on Microwave Theory and Techniques, 2014, 62, 1973-1985.	4.6	12
133	0.77 fJ/bit/search Content Addressable Memory Using Small Match Line Swing and Automated Background Checking Scheme for Variation Tolerance. IEEE Journal of Solid-State Circuits, 2014, 49, 1487-1498.	5.4	37
134	An integrated 60GHz low power two-chip wireless system based on IEEE802.11ad standard. , 2014, , .		8
135	A 24 GHz low power low phase noise dual-mode phase locked loop frequency synthesizer for 60 GHz applications. , 2014, , .		5
136	Coupled Dual LC Tanks Based ILFD With Low Injection Power and Compact Size. IEEE Microwave and Wireless Components Letters, 2014, 24, 105-107.	3.2	25
137	A miniaturized 28mW 60GHz differential quadrature sub-harmonic QPSK modulator in 0.18um SiGe BiCMOS. , 2014, , .		6
138	Temperature-Compensated dB-linear Digitally Controlled Variable Gain Amplifier With DC Offset Cancellation. IEEE Transactions on Microwave Theory and Techniques, 2013, 61, 2648-2661.	4.6	43
139	A Compact Coupling Controllable Elliptical Filter Based on Multilayer LTCC. Microwave and Optical Technology Letters, 2013, 55, 1789-1792.	1.4	4
140	A 1-mW K-band gate AC-coupled VCO with 0.25V supply voltage. Analog Integrated Circuits and Signal Processing, 2013, 77, 87-91.	1.4	1
141	A 76 GHz oscillator by high-Q differential transmission line loaded with split ring resonator in 65-nm CMOS. , 2013, , .		2
142	THRU-Based Cascade De-embedding Technique for On-Wafer Characterization of RF CMOS Devices. IEEE Transactions on Electron Devices, 2013, 60, 2892-2899.	3.0	11
143	A current-mode stimulator circuit with two-step charge balancing background calibration. , 2013, , .		0
144	A compact dual-band meander-line antenna for biomedical applications. , 2013, , .		9

#	ARTICLE	IF	CITATIONS
145	Improved inverter-based readout scheme for low-power ISFET sensing array. Electronics Letters, 2013, 49, 1517-1518.	1.0	3
146	A 12-mW 40–60-GHz 0.18- $\mu$ BiCMOS Oscillator-Less Self-Demodulator for Short-Range Software-Defined Transceivers. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2013, 3, 521-530.	3.6	2
147	A 60GHz power amplifier with 12.1 dBm & P1dBCP in 0.18um SiGe BiCMOS process. , 2013, , .		0
148	Design and array implementation a cantilever-based non-volatile memory utilizing vibrational reset. , 2013, , .		0
149	A New Millimeter-Wave Fixture Deembedding Method Based on Generalized Cascade Network Model. IEEE Electron Device Letters, 2013, 34, 447-449.	3.9	10
150	A 24 GHz reconfigurable frequency synthesizer for 60 GHz WPAN. , 2013, , .		0
151	\$K\$-band High-PAE Wide-Tuning-Range VCO Using Triple-Coupled \$LC\$ Tanks. IEEE Transactions on Circuits and Systems II: Express Briefs, 2013, 60, 736-740.	3.0	28
152	Class-D Amplifier Power Stage With PWM Feedback Loop. IEEE Transactions on Power Electronics, 2013, 28, 3870-3881.	7.9	21
153	A Dividerless PLL With Low Power and Low Reference Spur by Aperture-Phase Detector and Phase-to-Analog Converter. IEEE Transactions on Circuits and Systems I: Regular Papers, 2013, 60, 37-50.	5.4	19
154	A 2-D Distributed Power Combining by Metamaterial-Based Zero Phase Shifter for 60-GHz Power Amplifier in 65-nm CMOS. IEEE Transactions on Microwave Theory and Techniques, 2013, 61, 505-516.	4.6	51
155	High-frequency low-power fractional frequency multiplier. Microwave and Optical Technology Letters, 2013, 55, 146-149.	1.4	0
156	A Miniaturized Millimeter-Wave Standing-Wave Filtering Switch With High P1dB. IEEE Transactions on Microwave Theory and Techniques, 2013, 61, 1505-1515.	4.6	40
157	Design of High-Q Millimeter-Wave Oscillator by Differential Transmission Line Loaded With Metamaterial Resonator in 65-nm CMOS. IEEE Transactions on Microwave Theory and Techniques, 2013, 61, 1892-1902.	4.6	40
158	A Compact 2.1–39 GHz Self-Biased Low-Noise Amplifier in 65 nm CMOS Technology. IEEE Microwave and Wireless Components Letters, 2013, 23, 662-664.	3.2	26
159	Cross-Coupled Current Conveyor Based CMOS Transimpedance Amplifier for Broadband Data Transmission. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2013, 21, 1516-1525.	3.1	46
160	Embedded Transformed Radial Stub Cell for BPF With Spurious-Free Above Ten Octaves. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2013, 3, 1597-1603.	2.5	8
161	Optical infrastructure for visible light communication for public housing and commercial buildings. , 2013, , .		0
162	Miniaturized 60-GHz On-Chip Multimode Quasi-Elliptical Bandpass Filter. IEEE Electron Device Letters, 2013, 34, 945-947.	3.9	70

#	ARTICLE	IF	CITATIONS
163	MIMO-diversity switching techniques for digital transmission in visible light communication. , 2013, , .		2
164	A High Speed Low Power CAM With a Parity Bit and Power-Gated ML Sensing. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2013, 21, 151-156.	3.1	38
165	0.6mW 6.3GHz 40nm CMOS divide-by-3 prescaler using heterodyne phase-locking technique. Electronics Letters, 2013, 49, 471-472.	1.0	4
166	A CMOS LOW-POWER TEMPERATURE-ROBUST RSSI USING WEAK-INVERSION LIMITING AMPLIFIERS. Journal of Circuits, Systems and Computers, 2013, 22, 1340034.	1.5	5
167	A LOW POWER PUSH-PUSH VCO USING MULTI-COUPLED LC TANKS. Journal of Circuits, Systems and Computers, 2013, 22, 1340035.	1.5	0
168	An improved read/write scheme for anchorless NEMS-CMOS non-volatile memory. , 2013, , .		0
169	MODELING AND LAYOUT OPTIMIZATION TECHNIQUES FOR SILICON-BASED SYMMETRICAL SPIRAL INDUCTORS. Progress in Electromagnetics Research, 2013, 143, 1-18.	4.4	10
170	High-speed CMOS image sensor for high-throughput lensless microfluidic imaging system. Proceedings of SPIE, 2012, , .	0.8	8
171	A Cross-Coupled LPF Topology and Design for Millimeter-Wave RFIC Applications. IEEE Transactions on Electron Devices, 2012, 59, 2902-2909.	3.0	10
172	A 12-GHz high output power amplifier using 0.18µm SiGe BiCMOS for low power applications. , 2012, , .		1
173	Transformed radial stub cell embedded resonator for high performance filter applications. , 2012, , .		1
174	An optimum RF link for implantable devices with rectification of transmission errors. , 2012, , .		4
175	A 1-V CMOS Ultralow-Power Receiver Front End for the IEEE 802.15.4 Standard Using Tuned Passive Mixer Output Pole. International Federation for Information Processing, 2012, , 1-21.	0.4	3
176	DGS embedded transformed radial stub for ultra-wide stopband lowpass filter. Electronics Letters, 2012, 48, 1473.	1.0	14
177	A 44-to-60GHz, 9.7dBm P1dB, 7.1% PAE power amplifier with 2D distributed power combining by metamaterial-based zero-phase-shifter in 65nm CMOS. , 2012, , .		4
178	Sensing Margin Enhancement Techniques for Ultra-Low-Voltage SRAMs Utilizing a Bitline-Boosting Current and Equalized Bitline Leakage. IEEE Transactions on Circuits and Systems II: Express Briefs, 2012, 59, 868-872.	3.0	11
179	On-chip tunable low pass filter with improved stopband using new cross coupled topology. , 2012, , .		0
180	Bidirectional Diode-Triggered Silicon-Controlled Rectifiers for Low-Voltage ESD Protection. IEEE Electron Device Letters, 2012, 33, 1360-1362.	3.9	27

#	ARTICLE	IF	CITATIONS
181	Retention time characterization and optimization of logic-compatible embedded DRAM cells. , 2012, , .		3
182	Phase Compensation of Cascaded Conductor-Backed CPW Periodic Cells. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2012, 2, 1455-1464.	2.5	0
183	A super-resolution CMOS image sensor for bio-microfluidic imaging. , 2012, , .		1
184	Design of quarter-wavelength resonator filters with coupling controllable paths. , 2012, , .		3
185	Low power implantable neural recording front-end. , 2012, , .		2
186	A Low-Power Single-Phase Clock Multiband Flexible Divider. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2012, 20, 376-380.	3.1	39
187	Integrated circuits design for neural recording sensor interface. , 2012, , .		0
188	A ROBUST 900 MHz RFID READER CHIP WITH RC-CALIBRATION. Journal of Circuits, Systems and Computers, 2012, 21, 1240021.	1.5	0
189	Design of a Ku-band Low-Phase-Noise VCO Using the Dual LC Tanks. IEEE Transactions on Circuits and Systems II: Express Briefs, 2012, 59, 262-266.	3.0	30
190	A Dual-Loop Clock and Data Recovery Circuit With Compact Quarter-Rate CMOS Linear Phase Detector. IEEE Transactions on Circuits and Systems I: Regular Papers, 2012, 59, 1156-1167.	5.4	19
191	Ultra-wide rejection band lowpass cell. Electronics Letters, 2012, 48, 99.	1.0	10
192	Recent progress in silicon-based millimeter-wave power amplifier. , 2012, , .		0
193	A 60GHz on-chip antenna in standard CMOS silicon Technology. , 2012, , .		14
194	A 7.9-mW 5.6-GHz Digitally Controlled Variable Gain Amplifier With Linearization. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 3482-3490.	4.6	17
195	MOSFET Drain Current Noise Modeling With Effective Gate Overdrive and Junction Noise. IEEE Electron Device Letters, 2012, 33, 1117-1119.	3.9	11
196	A low power low phase noise dual-band multiphase VCO. Microelectronics Journal, 2012, 43, 1016-1022.	2.0	7
197	A 9.87 nW 1 kS/s 8.7 ENOB SAR ADC for implantable epileptic seizure detection microsystems. , 2012, , .		2
198	Designs of a free-space white-LED mass-storage transceiver for SD-card file transfer. , 2012, , .		3

#	ARTICLE	IF	CITATIONS
199	Design Exploration of Hybrid CMOS and Memristor Circuit by New Modified Nodal Analysis. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2012, 20, 1012-1025.	3.1	72
200	Low-power high-speed dual-modulus prescaler for Gb/s applications. , 2012, , .		2
201	A 60GHz VCO with 25.8% tuning range by switching return-path in 65nm CMOS. , 2012, , .		9
202	Design of a low power 60GHz OOK receiver in 65nm CMOS technology. , 2012, , .		1
203	A 1.2 V 2.4 GHz low spur CMOS PLL synthesizer with a gain boosted charge pump for a batteryless transceiver. , 2012, , .		7
204	Dispersion modeling of solid core photonic crystal fiber. , 2012, , .		0
205	Transformer based multiple coupled LC tanks for on-chip VCO design and applications. , 2012, , .		3
206	A low power millimetre-wave VCO in 0.18 $\mu\text{m}$ SiGe BiCMOS technology. , 2012, , .		2
207	A 96 $\mu\text{m}$ 1V ultra-low power CMOS image sensor for biomedical application. , 2012, , .		4
208	A 160 nW 25 kS/s 9-bit SAR ADC for neural signal recording applications. , 2012, , .		10
209	A COMPACT MULTIMODE BANDPASS FILTER WITH EXTENDED STOPBAND BANDWIDTH. Progress in Electromagnetics Research Letters, 2012, 32, 177-186.	0.7	0
210	RADIAL LOADED TRANSFORMED RADIAL STUB FOR LPF STOPBAND EXTENSION. Progress in Electromagnetics Research Letters, 2012, 30, 125-132.	0.7	9
211	Research and development of microwave & millimeter-wave technology in singapore. , 2012, , .		2
212	A new field dependent mobility model for high frequency channel thermal noise of deep submicron RFCMOS. Solid-State Electronics, 2012, 68, 32-37.	1.4	9
213	Impact of velocity saturation and hot carrier effects on channel thermal noise model of deep sub-micron MOSFETs. Solid-State Electronics, 2012, 72, 8-11.	1.4	10
214	A 0.6 $\mu\text{m}$ high reverse $\epsilon$ isolation through feedback self $\epsilon$ cancellation for single $\epsilon$ stage noncascode CMOS LNA. Microwave and Optical Technology Letters, 2012, 54, 374-379.	1.4	1
215	A 1.8-V 3.6-mW 2.4-GHz Fully Integrated CMOS Frequency Synthesizer for the IEEE 802.15.4. International Federation for Information Processing, 2012, , 69-99.	0.4	5
216	Bandwidth-Related Optimization in High-Speed Frequency Dividers using SiGe Technology. Journal of Semiconductor Technology and Science, 2012, 12, 107-116.	0.4	4

#	ARTICLE	IF	CITATIONS
217	A 2.4 GHz ultra low-power high gain LNA utilizing $\pi$ -match and capacitive feedback input network. , 2011, , .		18
218	A low power wide tuning range VCO with coupled LC tanks. , 2011, , .		1
219	Wide center-tape balun for 60 GHz silicon RF ICs. , 2011, , .		1
220	Wide center-tape balun for 60 GHz silicon RF ICs. , 2011, , .		2
221	Low power high data rate GHz range receiver in 40nm CMOS technology. , 2011, , .		0
222	Design of a hysteresis lock detector for dual-loops clock and data recovery circuit. , 2011, , .		2
223	Design of Ring-Oscillator-Based Injection-Locked Frequency Dividers With Single-Phase Inputs. IEEE Microwave and Wireless Components Letters, 2011, 21, 559-561.	3.2	23
224	A double-quadrature down-conversion mixer in 0.18 $\mu$ m SiGe BiCMOS process. , 2011, , .		2
225	A CMOS low-power receiving signal strength indicator using weak-inversion limiting amplifiers. , 2011, , .		2
226	A Cascade-Parallel Based Noise De-Embedding Technique for RF Modeling of CMOS Device. IEEE Microwave and Wireless Components Letters, 2011, 21, 448-450.	3.2	5
227	A 3.1-8 GHz CMOS UWB front-end receiver. , 2011, , .		5
228	Power-Efficient Explicit-Pulsed Dual-Edge Triggered Sense-Amplifier Flip-Flops. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2011, 19, 1-9.	3.1	51
229	A BiCMOS implementation of noise canceling low noise amplifier for wideband applications. , 2011, , .		0
230	A CMOS power amplifier using power combination for 3.5GHz mobile WiMAX subscriber applications. , 2011, , .		0
231	Ultra-low power series input resonance differential common gate LNA. Electronics Letters, 2011, 47, 703.	1.0	12
232	A delta-sigma fractional-N frequency divider for a Phase Lock Loop in 60GHz transceiver. , 2011, , .		2
233	Ultra low-power high-speed flexible Probabilistic Adder for Error-Tolerant Applications. , 2011, , .		20
234	A 10GHz 2.8dB gain passive mixer in 0.18 $\mu$ m CMOS technology. , 2011, , .		0

#	ARTICLE	IF	CITATIONS
235	Ultra low power active 60 GHz Bi-CMOS down-conversion mixer. , 2011, , .		1
236	SiGe BiCMOS power amplifiers for 60GHz ISM band applications. , 2011, , .		3
237	A divide-by-two injection-locked frequency divider with 13-GHz locking range in 0.18- $\mu$ m CMOS technology. , 2011, , .		3
238	A Low-Loss Image-Reject Mixer Using Source Follower Isolation Method for DRM/DAB Tuner Applications. IEEE Transactions on Circuits and Systems II: Express Briefs, 2011, 58, 729-733.	3.0	6
239	An 8T Differential SRAM With Improved Noise Margin for Bit-Interleaving in 65 nm CMOS. IEEE Transactions on Circuits and Systems I: Regular Papers, 2011, 58, 1252-1263.	5.4	79
240	Design and Sensitivity Analysis of a New Current-Mode Sense Amplifier for Low-Power SRAM. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2011, 19, 196-204.	3.1	38
241	Low-power LC-tank-reused injection-locked frequency multiplier. , 2011, , .		0
242	New Ultra-Wide Stopband Low-Pass Filter Using Transformed Radial Stubs. IEEE Transactions on Microwave Theory and Techniques, 2011, 59, 604-611.	4.6	113
243	An Accurate Two-Port De-Embedding Technique for RF/Millimeter-Wave Noise Characterization and Modeling of Deep Submicrometer Transistors. IEEE Transactions on Microwave Theory and Techniques, 2011, 59, 479-487.	4.6	10
244	Compact Ultra-Wideband (UWB) Bandpass Filter With Ultra-Narrow Dual- and Quad-Notched Bands. IEEE Transactions on Microwave Theory and Techniques, 2011, 59, 1509-1519.	4.6	58
245	Characterization, design and modeling of on-chip interleaved transformers in CMOS RFICs. Analog Integrated Circuits and Signal Processing, 2011, 66, 67-79.	1.4	1
246	A fully integrated low power PAM multi-channel UWB transmitter. Analog Integrated Circuits and Signal Processing, 2011, 68, 77-84.	1.4	4
247	Design and analysis of a WLAN CMOS power amplifier using multiple-gated transistor technique. International Journal of RF and Microwave Computer-Aided Engineering, 2011, 21, 157-163.	1.2	1
248	High-frequency low-power LC divide-by-2/3 injection-locked frequency divider. Microwave and Optical Technology Letters, 2011, 53, 337-340.	1.4	6
249	A DC to 30GHz ultra-wideband CMOS T/R switch. Microwave and Optical Technology Letters, 2011, 53, 2072-2075.	1.4	8
250	Low-power 2.4/5.15GHz dual-band voltage-controlled oscillator. Microwave and Optical Technology Letters, 2011, 53, 2495-2497.	1.4	0
251	A low power 17% tuning range low phase noise VCOs using coupled LC tanks. , 2011, , .		0
252	A 60GHz BiCMOS self-demodulator with injection locked oscillator. , 2011, , .		2

#	ARTICLE	IF	CITATIONS
253	A stacking voltage-controlled oscillator and injection-locked frequency divider for low-power and 12-GHz operation. , 2011, , .		1
254	A low-power CAM with efficient power and delay trade-off. , 2011, , .		10
255	A compact 60 GHz LTCC microstrip bandpass filter with controllable transmission zeros. , 2011, , .		3
256	Novel hybrid type Automatic Amplitude Control loop VCO. , 2011, , .		3
257	A random number generator for low power cryptographic application. , 2010, , .		11
258	A comparative study of two techniques for improving power-handling capability of CMOS T/R switches. International Journal of RF and Microwave Computer-Aided Engineering, 2010, 20, 298-305.	1.2	0
259	A SPICE COMPATIBLE MODEL OF ON-WAFER COUPLED INTERCONNECTS FOR CMOS RFICs. Progress in Electromagnetics Research, 2010, 102, 287-299.	4.4	10
260	A 1.8-V 3.6-mW 2.4-GHz fully integrated CMOS frequency synthesizer for IEEE 802.15.4. , 2010, , .		2
261	Low-power high-speed multiplier for error-tolerant application. , 2010, , .		43
262	Dual-band bandpass filter using embedded spiral resonator and broadside-coupled meander slot-line. Electronics Letters, 2010, 46, 1135.	1.0	12
263	Compact Dual-Band Bandpass Filters Using Novel Embedded Spiral Resonator (ESR). IEEE Microwave and Wireless Components Letters, 2010, 20, 435-437.	3.2	56
264	A compact UWB bandpass filter with ultra narrow notched band and competitive attenuation slope. , 2010, , .		2
265	A Wideband Low Power Low-Noise Amplifier in CMOS Technology. IEEE Transactions on Circuits and Systems I: Regular Papers, 2010, 57, 773-782.	5.4	66
266	Enhanced low-power high-speed adder for error-tolerant application. , 2010, , .		106
267	Criterion to Evaluate Input-Offset Voltage of a Latch-Type Sense Amplifier. IEEE Transactions on Circuits and Systems I: Regular Papers, 2010, 57, 83-92.	5.4	13
268	Design of Low-Power High-Speed Truncation-Error-Tolerant Adder and Its Application in Digital Signal Processing. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2010, 18, 1225-1229.	3.1	262
269	Design and Analysis of Ultra Low Power True Single Phase Clock CMOS 2/3 Prescaler. IEEE Transactions on Circuits and Systems I: Regular Papers, 2010, 57, 72-82.	5.4	63
270	Design of a CMOS Broadband Transimpedance Amplifier With Active Feedback. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2010, 18, 461-472.	3.1	74



#	ARTICLE	IF	CITATIONS
271	Novel low cost compact size planar low pass filters with deep skirt selectivity and wide stopband rejection. , 2010, , .		7
272	A 1.8-V 6.5-GHz low power wide band single-phase clock CMOS 2/3 prescaler. , 2010, , .		6
273	An Energy-Aware CMOS Receiver Front End for Low-Power 2.4-GHz Applications. IEEE Transactions on Circuits and Systems I: Regular Papers, 2010, 57, 2675-2684.	5.4	35
274	A fully symmetrical 60GHz transceiver architecture for IEEE 802.15.3c application. , 2010, , .		2
275	Compact UWB Bandpass Filter With Ultra Narrow Notched Band. IEEE Microwave and Wireless Components Letters, 2010, 20, 145-147.	3.2	76
276	An 8T SRAM cell with column-based dynamic supply voltage for bit-interleaving. , 2010, , .		1
277	Design of probabilistic-based Markov Random Field logic gates in 65nm CMOS technology. , 2010, , .		8
278	Low IR drop and low power parallel CAM design using gated power transistor technique. , 2010, , .		0
279	A possible reality on battery-less low-power portable electronics. , 2010, , .		1
280	A 1-V CMOS ultralow-power receiver front end for the IEEE 802.15.4 standard using tuned passive mixer output pole. , 2010, , .		1
281	A low-voltage fully-integrated CMOS power amplifier for mobile WiMAX subscriber station. , 2009, , .		0
282	A novel de-embedding technique for On-Wafer characterization of RF CMOS. , 2009, , .		1
283	A CMOS Energy Efficient UWB transmitter module. , 2009, , .		1
284	Extended stopband band pass filter using new spurious suppression technique. Microwave and Optical Technology Letters, 2009, 51, 706-709.	1.4	0
285	A novel planar multimode bandpass filter with radial perturbation. Microwave and Optical Technology Letters, 2009, 51, 964-966.	1.4	7
286	An ultra-wideband bandpass filter using hybrid structure of microstrip and CPW. Microwave and Optical Technology Letters, 2009, 51, 2470-2473.	1.4	7
287	Impact of forward and reverse deep n-well biasing on the 1/f noise of 0.13 $\mu$ m n-channel MOSFETs in triple well technology. Solid-State Electronics, 2009, 53, 599-606.	1.4	1
288	A Wideband and High Rejection Multimode Bandpass Filter Using Stub Perturbation. IEEE Microwave and Wireless Components Letters, 2009, 19, 24-26.	3.2	57

#	ARTICLE	IF	CITATIONS
289	A Weak-Inversion Low-Power Active Mixer for 2.4 GHz ISM Band Applications. IEEE Microwave and Wireless Components Letters, 2009, 19, 719-721.	3.2	18
290	A 3.8 GHz Low-Noise CMOS Amplifier. IEEE Microwave and Wireless Components Letters, 2009, 19, 245-247.	3.2	41
291	Reciprocal noise canceling low power UWB LNA. , 2009, , .		0
292	A low power UWB direct conversion receiver with pulse detectors. , 2009, , .		2
293	A new unified model for channel thermal noise of deep sub-micron RFCMOS. , 2009, , .		1
294	A Scalable RFCMOS Noise Model. IEEE Transactions on Microwave Theory and Techniques, 2009, 57, 1009-1019.	4.6	18
295	A New 24/36-GHz Transceiver architecture for 60-GHz applications. , 2009, , .		0
296	A low power CMOS phase-switching prescaler for 1.8-2.4GHz wireless communications. Analog Integrated Circuits and Signal Processing, 2008, 56, 245-249.	1.4	1
297	Characterization and modeling of on-wafer single and multiple vias for CMOS RFICS. Microwave and Optical Technology Letters, 2008, 50, 713-715.	1.4	4
298	Enhancement of broadband performance for on-chip spiral inductors with inner-patterned ground. Microwave and Optical Technology Letters, 2008, 50, 1744-1746.	1.4	0
299	Impact of interconnect effects on broadband transimpedance amplifiers. Microwave and Optical Technology Letters, 2008, 50, 3017-3020.	1.4	0
300	High Self-Resonant and Area Efficient Monolithic Transformer Using Novel Intercoil-Crossing Structure for Silicon RFIC. IEEE Electron Device Letters, 2008, 29, 1376-1379.	3.9	3
301	Modeling and Layout Optimization of Differential Inductors for Silicon-Based RFIC Applications. IEEE Transactions on Electron Devices, 2008, 55, 1058-1066.	3.0	10
302	A Subthreshold Low-Noise Amplifier Optimized for Ultra-Low-Power Applications in the ISM Band. IEEE Transactions on Microwave Theory and Techniques, 2008, 56, 286-292.	4.6	59
303	16.6- and 28-GHz Fully Integrated CMOS RF Switches With Improved Body Floating. IEEE Transactions on Microwave Theory and Techniques, 2008, 56, 339-345.	4.6	48
304	Fully Symmetrical Monolithic Transformer (True 1:1) for Silicon RFIC. IEEE Transactions on Microwave Theory and Techniques, 2008, 56, 2301-2311.	4.6	36
305	An 8-bit 200-MSample/s Pipelined ADC With Mixed-Mode Front-End S/H Circuit. IEEE Transactions on Circuits and Systems I: Regular Papers, 2008, 55, 1430-1440.	5.4	44
306	Hybrid-Mode SRAM Sense Amplifiers: New Approach on Transistor Sizing. IEEE Transactions on Circuits and Systems II: Express Briefs, 2008, 55, 986-990.	3.0	23

#	ARTICLE	IF	CITATIONS
307	Sub-mW multi-GHz CMOS dual-modulus prescalers based on programmable injection-locked frequency dividers. , 2008, , .		8
308	Complex Shaped On-Wafer Interconnects Modeling for CMOS RFICs. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2008, 16, 922-926.	3.1	2
309	An Ultra-Compact Planar Bandpass Filter With Open-Ground Spiral for Wireless Application. IEEE Transactions on Advanced Packaging, 2008, 31, 285-291.	1.6	3
310	An area efficient high turn ratio monolithic transformer for silicon RFIC. , 2008, , .		0
311	Hardware realization of a medical diagnostic system based on Probabilistic CMOS (PCMOS) technology. , 2008, , .		0
312	SPICESoft: AUTOMATED TOOL FOR SENSITIVITY ANALYSIS, PERFORMANCE ANALYSIS, AND INVERSE PERFORMANCE ANALYSIS OF DIGITAL CIRCUITS. Journal of Circuits, Systems and Computers, 2008, 17, 221-238.	1.5	0
313	A CMOS Mixed-Mode Sample-and-Hold Circuit for Pipelined ADCs. , 2008, , 81-99.		0
314	6.1â€¦GHz 4.6â€¦mW CMOS divide-by-55/56 prescaler. Electronics Letters, 2008, 44, 1402.	1.0	5
315	INTEGRATED CIRCUIT DESIGN RESEARCH RANKING FOR WORLDWIDE UNIVERSITIES. Journal of Circuits, Systems and Computers, 2008, 17, 141-167.	1.5	1
316	A full current-mode sense amplifier for low-power SRAM applications. , 2008, , .		4
317	Body-bootstrapped-buffer circuit for CMOS static power reduction. , 2008, , .		0
318	Digitally controllable variable-gain amplifiers in 0.18-â€¦m CMOS technology for &#x003BC;-power applications. , 2007, , .		0
319	Fully integrated CMOS limiting amplifier with offset compensation network. Electronics Letters, 2007, 43, 1084.	1.0	4
320	0.9â€¦V current-mode sense amplifier using concurrent bit- and data-line tracking and sensing techniques. Electronics Letters, 2007, 43, 1421.	1.0	8
321	Design of Tunable Polyphase Filter Using MOSFET. , 2007, , .		0
322	Characterizing and Modeling Conductor-backed CPW Periodic Band Stop Filter with Miniaturized Size. IEEE MTT-S International Microwave Symposium Digest IEEE MTT-S International Microwave Symposium, 2007, , .	0.0	7
323	An Ultra-Compact Hairpin Band Pass Filter With Additional Zero Points. IEEE Microwave and Wireless Components Letters, 2007, 17, 262-264.	3.2	26
324	Analysis and Design of RC Polyphase Network for Quadrature Signal Generation in the 2.45GHz ISM Band. , 2007, , .		6

#	ARTICLE	IF	CITATIONS
325	A Low Power Fully Programmable 1MHz Resolution 2.4GHz CMOS PLL Frequency Synthesizer. , 2007, , .		9
326	A fully integrated 2.4-GHz receiver in a 0.18- $\mu\text{m}$ CMOS process for low-power body-area-network applications. , 2007, , .		3
327	Sub-1 V Low Power Wide Range Injection-Locked Frequency Divider. IEEE Microwave and Wireless Components Letters, 2007, 17, 528-530.	3.2	9
328	VLSI Architectures for Lifting-Based Discrete Wavelet Packet Transform. , 2007, , .		5
329	A Low-Voltage Low-Power High Linear and Wide-Band Mixer. , 2007, , .		6
330	PCMOS-based Hardware Implementation of Bayesian Network. , 2007, , .		5
331	Modeling of On-Wafer Interconnect System for CMOS RFICs. , 2007, , .		0
332	Characterization of On-Wafer Vias for CMOS RFICs. , 2007, , .		1
333	Low Power Transmitter Design for BAN. , 2007, , .		7
334	New Conditional Sampling Sense-Amplifier-Based Flip-Flop for High-Performance and Low-Power Application. , 2007, , .		3
335	Odd phase switching prescaler based on Injection Locked Frequency Divider. IEEE MTT-S International Microwave Symposium Digest IEEE MTT-S International Microwave Symposium, 2007, , .	0.0	0
336	Design of Driver Amplifiers for 2.4GHz Low Power Short-range Transceiver. , 2007, , .		1
337	A Novel Static Dual Edge-Trigger Flip-flop for High-Frequency Low-Power Application. , 2007, , .		5
338	Broad-Band Design Techniques for Transimpedance Amplifiers. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2007, 54, 590-600.	0.1	87
339	A 2.4 GHz ultra low power subthreshold CMOS low-noise amplifier. Microwave and Optical Technology Letters, 2007, 49, 743-744.	1.4	10
340	Distortion of pulsed signals in carbon nanotube interconnects. Microelectronics Journal, 2007, 38, 365-370.	2.0	3
341	RFCMOS Unit Width Optimization Technique. IEEE Transactions on Microwave Theory and Techniques, 2007, 55, 1844-1853.	4.6	11
342	Noise transfer characteristics and design techniques of a frequency synthesizer. Analog Integrated Circuits and Signal Processing, 2007, 52, 89-97.	1.4	2

#	ARTICLE	IF	CITATIONS
343	A 200-MHz CMOS Mixed-Mode Sample-and-Hold Circuit for Pipelined ADCs. , 2006, , .		3
344	Design of a Fully Integrated Switchable Transistor CMOS LNA for 2.1 / 2.4 GHz Application. , 2006, , .		3
345	A New Phase Noise Model for TSPC based divider. , 2006, , .		1
346	A 1.8-V 2.4/5.15-GHz dual-band LCVCO in 0.18- $\mu\text{m}$ CMOS technology. IEEE Microwave and Wireless Components Letters, 2006, 16, 194-196.	3.2	46
347	Wide bandwidth Stacked Patch Antenna on Fourteen Layers Microwave Board. , 2006, , .		3
348	A novel CMOS low-noise amplifier design for 3.1- to 10.6-GHz ultra-wide-band wireless receivers. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2006, 53, 1683-1692.	0.1	136
349	Impact of device scaling on the 1/f noise performance of deep submicrometer thin gate oxide CMOS devices. Solid-State Electronics, 2006, 50, 1219-1226.	1.4	12
350	A 1 V switchable CMOS LNA for 802.11A/B WLAN applications. Analog Integrated Circuits and Signal Processing, 2006, 48, 181-184.	1.4	11
351	Sensitivity Analysis of Coupled Interconnects for RFIC Applications. IEEE Transactions on Electromagnetic Compatibility, 2006, 48, 607-613.	2.2	12
352	A compact size coupling controllable filter with separate electric and magnetic coupling paths. IEEE Transactions on Microwave Theory and Techniques, 2006, 54, 1113-1119.	4.6	176
353	Design and Optimization of the Extended True Single-Phase Clock-Based Prescaler. IEEE Transactions on Microwave Theory and Techniques, 2006, 54, 3828-3835.	4.6	77
354	Scalable model-based design of a tapped-line filter with common ground via resonators. Microwave and Optical Technology Letters, 2006, 48, 96-99.	1.4	1
355	Equivalent circuit model of a stacked inductor for high-Q on-chip RF applications. IET Circuits, Devices and Systems, 2006, 153, 525.	0.6	5
356	Low-power high-performance explicit-pulsed flip-flop using static latch and dynamic pulse generator. IET Circuits, Devices and Systems, 2006, 153, 253.	0.6	8
357	Scalable Model of On-Wafer Interconnects for High-Speed CMOS ICs. IEEE Transactions on Advanced Packaging, 2006, 29, 770-776.	1.6	6
358	Investigation of interconnect effects in a transimpedance amplifier. , 2006, , .		0
359	Sensitivity of on-wafer interconnects to CMOS process parameters at radio frequency. , 2006, , .		5
360	CMOS EVEN HARMONIC SWITCHING MIXER FOR DIRECT CONVERSION RECEIVERS. Journal of Circuits, Systems and Computers, 2006, 15, 183-196.	1.5	2

#	ARTICLE	IF	CITATIONS
361	Design of an area-efficient CMOS multiple-valued current comparator circuit. IET Circuits, Devices and Systems, 2005, 152, 151.	0.6	3
362	Low power high-speed CMOS dual-modulus prescaler design with imbalanced phase-switching technique. IET Circuits, Devices and Systems, 2005, 152, 127.	0.6	13
363	Accurate and scalable RF interconnect model for silicon-based RFIC applications. IEEE Transactions on Microwave Theory and Techniques, 2005, 53, 3035-3044.	4.6	11
364	Physical Layout Design Optimization of Integrated Spiral Inductors for Silicon-Based RFIC Applications. IEEE Transactions on Electron Devices, 2005, 52, 2559-2567.	3.0	29
365	1.8-V 3.1-10.6-GHz CMOS low-noise amplifier for ultra-wideband applications. Microwave and Optical Technology Letters, 2005, 44, 299-302.	1.4	0
366	RF equivalent-circuit model of interconnect bends based on S-parameter measurements. Microwave and Optical Technology Letters, 2005, 45, 170-173.	1.4	7
367	Compact CMOS Baluns for the 4-10 GHz Band Applications. Analog Integrated Circuits and Signal Processing, 2005, 45, 5-13.	1.4	2
368	10 Gb/s Linear Full-Rate CMOS Phase Detector for Clock Data Recovery Circuit. Analog Integrated Circuits and Signal Processing, 2005, 45, 191-196.	1.4	0
369	Compact two-order bandpass filter with three finite zero points. Electronics Letters, 2005, 41, 846.	1.0	18
370	AN ULTRA LOW-POWER CURRENT-MODE SENSE AMPLIFIER FOR SRAM APPLICATIONS. Journal of Circuits, Systems and Computers, 2005, 14, 939-951.	1.5	14
371	Cost effective ferroelectric thick film phase shifter based on screen-printing technology. , 2005, , .		2
372	Experimentally investigating slow-wave transmission lines and filters based on conductor-backed CPW periodic cells. , 2005, , .		14
373	Non-sequential linear CMOS phase detector for CDR applications. IET Circuits, Devices and Systems, 2005, 152, 667.	0.6	5
374	Novel RF Process Monitoring Test Structure for Silicon Devices. IEEE Transactions on Semiconductor Manufacturing, 2005, 18, 246-254.	1.7	3
375	A miniaturized silicon-based ground Ring Guarded patch resonator and filter. IEEE Microwave and Wireless Components Letters, 2005, 15, 478-480.	3.2	2
376	A modified architecture used for input matching in CMOS low-noise amplifiers. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2005, 52, 784-788.	2.2	54
377	Design of a low power wide-band high resolution programmable frequency divider. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2005, 13, 1098-1103.	3.1	37
378	Fully integrated CMOS fractional-N frequency divider for wide-band mobile applications with spurs reduction. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2005, 52, 1042-1048.	0.1	21

#	ARTICLE	IF	CITATIONS
379	Equivalent circuit model of on-wafer CMOS interconnects for RFICs. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2005, 13, 1060-1071.	3.1	23
380	Parasitic-compensated quadrature LC oscillator. IET Circuits, Devices and Systems, 2004, 151, 45.	0.6	13
381	Simple and accurate extraction methodology for RF MOSFET valid up to 20 GHz. IET Circuits, Devices and Systems, 2004, 151, 587.	0.6	18
382	Impact of technology scaling on the $1/f$ noise of thin and thick gate oxide deep submicron NMOS transistors. IET Circuits, Devices and Systems, 2004, 151, 415.	0.6	23
383	Algorithm and architecture for a high density, low power scalar product macrocell. IEE Proceedings: Computers and Digital Techniques, 2004, 151, 161.	1.6	4
384	9.3-10.4-GHz-Band Cross-Coupled Complementary Oscillator With Low Phase-Noise Performance. IEEE Transactions on Microwave Theory and Techniques, 2004, 52, 1273-1278.	4.6	47
385	Novel 53/106 GHz Dual-Band MMW LC Oscillator Implemented in SiGe BiCMOS Technology. Journal of Infrared, Millimeter and Terahertz Waves, 2004, 25, 55-62.	0.6	2
386	Investigating the effects of the supply voltage on the tuning range of a 10-GHz VCO in 0.18- $\mu$ m CMOS technology. Microwave and Optical Technology Letters, 2004, 40, 448-451.	1.4	1
387	A fully integrated dual-band low-noise amplifier for bluetooth and wireless LAN applications. Microwave and Optical Technology Letters, 2004, 41, 297-301.	1.4	0
388	ESD robust and area-efficient circle-type layout design for CMOS output buffers. Journal of Electrostatics, 2004, 61, 189-196.	1.9	0
389	Effect of technology scaling on the $1/f$ noise of deep submicron PMOS transistors. Solid-State Electronics, 2004, 48, 1101-1109.	1.4	34
390	A comprehensive geometrical and biasing analysis for latchup in 0.18- $\mu$ m CoSi <sub>2</sub> STI CMOS structure. Solid-State Electronics, 2004, 48, 2109-2114.	1.4	1
391	1-10 GHz CMOS frequency divider with low power consumption. Electronics Letters, 2004, 40, 467.	1.0	10
392	High Frequency Thick Film BST Ferroelectric Phase Shifter. Integrated Ferroelectrics, 2004, 61, 65-70.	0.7	10
393	RF CMOS low-phase-noise LC oscillator through memory reduction tail transistor. IEEE Transactions on Circuits and Systems II: Express Briefs, 2004, 51, 85-90.	3.0	55
394	New wideband dualband CMOS LC voltage-controlled oscillator. IET Circuits, Devices and Systems, 2003, 150, 453.	0.6	15
395	A comprehensive study and modeling of centre-tap differentially driven single-turn integrated inductors for 10-GHz applications. Microwave and Optical Technology Letters, 2003, 38, 182-185.	1.4	2
396	A 52 GHz VCO with low-phase noise implemented in SiGe BiCMOS technology. Microwave and Optical Technology Letters, 2003, 39, 414-418.	1.4	4

#	ARTICLE	IF	CITATIONS
397	GHz programmable counter with low power consumption. Electronics Letters, 2003, 39, 1572.	1.0	13
398	An Integrated SiGe Dual-band Low Noise Amplifier for Bluetooth, HiperLAN and Wireless LAN Applications. , 2003, , .		4
399	Metallization proximity studies for copper spiral inductors on silicon. IEEE Transactions on Semiconductor Manufacturing, 2003, 16, 220-227.	1.7	8
400	A novel tap input coupling structure for a narrow bandpass filter using TM/sub 010/ mode of a microstrip circular-disk resonator. IEEE Transactions on Microwave Theory and Techniques, 2002, 50, 1230-1232.	4.6	17
401	High-performance low-power current sense amplifier using a cross-coupled current-mirror configuration. IET Circuits, Devices and Systems, 2002, 149, 308-314.	0.6	22
402	Investigation of the wideband operation capability of Gilbert cell mixers. Microwave and Optical Technology Letters, 2002, 33, 97-100.	1.4	2
403	New spur reduction fractional-N frequency divider. Microwave and Optical Technology Letters, 2002, 33, 355-358.	1.4	0
404	High temperature superconducting ferrite phase shifter with new latching structure. IEEE Transactions on Applied Superconductivity, 2001, 11, 430-433.	1.7	6
405	A high-speed twin-capacitor BiNMOS (TC-BiNMOS) logic circuit for single battery operation. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2001, 48, 399-405.	0.1	1
406	Small signal model and efficient parameter extraction technique for deep submicron MOSFETs for RF applications. IET Circuits, Devices and Systems, 2001, 148, 35.	0.6	4
407	Effective modeling of temperature-dependent body current for submicron devices under BiMOS hybrid-mode operation. Microelectronics Journal, 2001, 32, 205-214.	2.0	1
408	Latchup characterization of 0.18-micron STI cobalt silicided test structures. Microelectronics Journal, 2001, 32, 725-731.	2.0	3
409	Three-dimensional integrated coil inductor on silicon for high-frequency applications. Microwave and Optical Technology Letters, 2001, 31, 23-24.	1.4	0
410	New small-signal model for HEMTs and MESFETs. Microwave and Optical Technology Letters, 2001, 28, 375-378.	1.4	7
411	Effective channel length and external series resistance models of scaled LDD pMOSFETs operating in a Bi-MOS hybrid-mode environment. IEEE Transactions on Electron Devices, 2001, 48, 1001-1004.	3.0	3
412	Impact of 0.25 [micro sign]m dual gate oxide thickness CMOS process on flicker noise performance of multifingered deep-submicron MOS devices. IET Circuits, Devices and Systems, 2001, 148, 312.	0.6	4
413	Fully integrated 10 GHz CMOS VCO. Electronics Letters, 2001, 37, 1021.	1.0	15
414	Design and Optimization of Novel High Responsivity, Wideband Silicon Photodiode. Japanese Journal of Applied Physics, 2001, 40, 2738-2740.	1.5	1



#	ARTICLE	IF	CITATIONS
415	High-Q Si-based inductor shielded with double-layer polysilicon for RF applications. Microwave and Optical Technology Letters, 2000, 24, 366-367.	1.4	3
416	A new geometrical optimization technique for RF integrated inductors. Microwave and Optical Technology Letters, 2000, 26, 39-41.	1.4	4
417	A simple, unified, and scalable RF model for accumulation-mode varactors. Microwave and Optical Technology Letters, 2000, 26, 171-173.	1.4	0
418	Broadband Q Si-based hybrid inductor for RF applications. Microwave and Optical Technology Letters, 2000, 26, 216-219.	1.4	0
419	Channel and well design of quarter-micron high performance retrograde well pMOSFETs. Solid-State Electronics, 2000, 44, 1121-1125.	1.4	0
420	Modeling of the body current in a Bi-MOS hybrid-mode environment. Solid-State Electronics, 2000, 44, 2199-2205.	1.4	1
421	Photoinduced intramolecular charge-transfer state of p-dimethylaminobenzoic acid in CdS and TiO <sub>2</sub> colloid solutions. Journal of Photochemistry and Photobiology A: Chemistry, 2000, 132, 105-114.	3.9	16
422	1.5 V 1.8 GHz bandpass amplifier. IET Circuits, Devices and Systems, 2000, 147, 331.	0.6	9
423	Design of high performance double edge-triggered flip-flops. IET Circuits, Devices and Systems, 2000, 147, 283.	0.6	7
424	High performance double edge-triggered flip-flop using a merged feedback technique. IET Circuits, Devices and Systems, 2000, 147, 363.	0.6	1
425	Ultra-low-voltage bootstrapped CMOS driver for high performance applications. Electronics Letters, 2000, 36, 706.	1.0	17
426	A redundant-binary partial-product generator based on a five-bit recoding technique. International Journal of Electronics, 2000, 87, 413-423.	1.4	3
427	Physically-based RF model for metal-oxide-metal capacitors. Electronics Letters, 2000, 36, 425.	1.0	8
428	Sub-1V bootstrapped CMOS driver for giga-scale-integration era. Electronics Letters, 1999, 35, 392.	1.0	21
429	Study of quarter-micrometre retrograde well: Device characteristics against temperature. Electronics Letters, 1999, 35, 345.	1.0	1
430	A temperature-dependent DC model for quarter-micron LDD pMOSFET's operating in a Bi-MOS structure. IEEE Transactions on Electron Devices, 1999, 46, 1672-1684.	3.0	0
431	Comments on "Negative capacitance effect in semiconductor devices" [by M. Ershov et al., with reply]. IEEE Transactions on Electron Devices, 1999, 46, 2357-2358.	3.0	14
432	Novel 1-V full-swing high-speed BiCMOS circuit using positive feedback base-boost technique. IET Circuits, Devices and Systems, 1999, 146, 130.	0.6	0

#	ARTICLE	IF	CITATIONS
433	DC model for BiMOS structure and its adaptation to a circuit simulation environment. IET Circuits, Devices and Systems, 1999, 146, 83.	0.6	0
434	Low-power circuit implementation for partial-product addition using pass-transistor logic. IET Circuits, Devices and Systems, 1999, 146, 124.	0.6	16
435	Altering transistor positions: impact on the performance and power dissipation of dynamic latches and flip-flops. IET Circuits, Devices and Systems, 1999, 146, 279.	0.6	7
436	Establishment and characterization of 12 human colorectal-carcinoma cell lines. , 1999, 81, 902-910.		40
437	A low-power 16 $\times$ 16-b parallel multiplier utilizing pass-transistor logic. IEEE Journal of Solid-State Circuits, 1999, 34, 1395-1399.	5.4	27
438	Comparison of latchup immunity for silicided source/drain at different n+ implant energy. Microelectronics Reliability, 1998, 38, 1401-1405.	1.7	2
439	New current conveyor for high-speed low-power current sensing. IET Circuits, Devices and Systems, 1998, 145, 85.	0.6	11
440	A charge-trapping-based technique to design low-voltage BiCMOS logic circuits. IEEE Journal of Solid-State Circuits, 1998, 33, 164-168.	5.4	5
441	BiCMOS logic circuit for single-battery operation. Electronics Letters, 1998, 34, 2013.	1.0	0
442	1.5 V high-speed electrostatic discharge free BiCMOS digital circuit. Electronics Letters, 1998, 34, 1306.	1.0	2
443	Optimised 0.25 [ $\mu$ m] high performance retrograde well pMOS device for low-power applications. Electronics Letters, 1998, 34, 1702.	1.0	1
444	A precise transient model for delayed input BiCMOS digital circuits. International Journal of Electronics, 1997, 83, 441-454.	1.4	0
445	Delay time sensitivity analysis of multi-generation BiCMOS digital circuits. IET Circuits, Devices and Systems, 1997, 144, 60.	0.6	1
446	Experimentally-based analytical model of deep-submicron LDD pMOSFETs in a Bi-MOS hybrid-mode environment. IEEE Transactions on Electron Devices, 1997, 44, 1473-1482.	3.0	21
447	Novel low-voltage BiCMOS digital circuits employing a lateral p-n-p BJT in a p-MOS structure. IET Circuits, Devices and Systems, 1996, 143, 83.	0.6	3
448	1.1 V full-swing double bootstrapped BiCMOS logic gates. IET Circuits, Devices and Systems, 1996, 143, 41.	0.6	9
449	New complementary BiCMOS digital gates for low-voltage environments. Solid-State Electronics, 1996, 39, 681-687.	1.4	5
450	BiCMOS circuit optimisation technique linking channel width of MOS device to collector design of BJT. Electronics Letters, 1996, 32, 2300.	1.0	0

#	ARTICLE	IF	CITATIONS
451	Full-swing high speed CBiCMOS digital circuit for low-voltage applications. IET Circuits, Devices and Systems, 1995, 142, 8.	0.6	12
452	1.5 V high speed low power CMOS current sense amplifier. Electronics Letters, 1995, 31, 1991-1993.	1.0	7
453	A PC-based MPEG compressed data decoder. IEEE Transactions on Consumer Electronics, 1995, 41, 1169-1173.	3.6	1
454	An experimentally-based DC model for the Bi-MOS structure and its adaptation to a circuit simulation environment. , 0, , .		0
455	A novel 1-V full-swing BiCMOS circuit using a positive feedback base-boost technique. , 0, , .		1
456	Current compensation method for adjustment free stereo multiplex voltage control oscillator in FM receivers. , 0, , .		0
457	A unified small signal model and an efficient parameter extraction technique for deep submicron MOSFETs for RF applications. , 0, , .		0
458	Effects of polysilicon shield on spiral inductors for silicon-based RF IC's. , 0, , .		8
459	Impact of 0.25 $\mu$ m dual gate oxide CMOS process on the flicker noise characteristics of multi-fingered MOSFETs for wireless applications. , 0, , .		0
460	A cmos rf bandpass low noise amplifier for multi-band wireless communication applications. , 0, , .		2
461	An interconnect optimized floorplanning of a scalar product macrocell. , 0, , .		1
462	A new active polyphase filter for wideband image reject downconverter. , 0, , .		5
463	A 2GHz programmable counter with new re-loadable D flip-flop. , 0, , .		2
464	A broadband CMOS LNA for WLAN applications. , 0, , .		10
465	A new current conveyor amplifier for low-voltage high-speed application. , 0, , .		1
466	Design of a phase splitter for 3/sup rd/ ISM band. , 0, , .		2
467	Extremely high-Q stacked transformer-type inductors for RF applications. , 0, , .		3
468	An integrated dual-band low noise amplifier for GSM and wireless LAN applications. , 0, , .		5

#	ARTICLE	IF	CITATIONS
469	Broadband image reject down-converters. , 0, , .		1
470	An integrated SiGe RF bandpass low noise amplifier for multi-band wireless communication applications. , 0, , .		0
471	Analysis of LO leakage due to LO mismatch in CMOS gilbert mixer for direct conversion application. , 0, , .		0
472	Analysis of lo leakage in CMOS gilbert mixer by cadence spectrerf for direct conversion application. , 0, , .		1
473	An ultra low-power output feedback flip-flop. , 0, , .		0
474	Equivalent circuit model of on-wafer interconnects for CMOS RFICs. , 0, , .		7
475	A novel RFCMOS process monitoring test structure. , 0, , .		7
476	A novel silicon-based CBCPW-fed CBCPS ring resonator. , 0, , .		2
477	A new 5GHz CMOS dual-modulus prescaler. , 0, , .		3
478	A 5GHz to 6GHz integrated differential LNA. , 0, , .		1
479	2.45GHz RF Power Amplifier with T/R Switch. , 0, , .		0
480	Area Efficient Low-Power Static Explicit-Pulsed Flip-Flop with Local Feedback. , 0, , .		0
481	A Low-Power Static Dual Edge-Triggered Flip-Flop using an Output-Controlled Discharge Configuration. , 0, , .		24
482	A Novel Methodology for the Design of LC Tank VCO with Low Phase Noise. , 0, , .		16
483	On-Wafer Microwave De-Embedding Techniques. , 0, , .		3