

# Kiat Seng Yeo

## List of Publications by Year in descending order

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483  
papers

5,759  
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docs citations

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citing authors

#	ARTICLE	IF	CITATIONS
1	Design of Low-Power High-Speed Truncation-Error-Tolerant Adder and Its Application in Digital Signal Processing. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2010, 18, 1225-1229.	3.1	262
2	A compact size coupling controllable filter with separate electric and magnetic coupling paths. IEEE Transactions on Microwave Theory and Techniques, 2006, 54, 1113-1119.	4.6	176
3	A novel CMOS low-noise amplifier design for 3.1- to 10.6-GHz ultra-wide-band wireless receivers. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2006, 53, 1683-1692.	0.1	136
4	New Ultra-Wide Stopband Low-Pass Filter Using Transformed Radial Stubs. IEEE Transactions on Microwave Theory and Techniques, 2011, 59, 604-611.	4.6	113
5	Enhanced low-power high-speed adder for error-tolerant application. , 2010, , .		106
6	Novel Defected Ground Structure and Two-Side Loading Scheme for Miniaturized Dual-Band SIW Bandpass Filter Designs. IEEE Microwave and Wireless Components Letters, 2015, 25, 217-219.	3.2	106
7	Cell-Based Variable-Gain Amplifiers With Accurate dB-Linear Characteristic in 0.18 $\mu\text{m}$ CMOS Technology. IEEE Journal of Solid-State Circuits, 2015, 50, 586-596.	5.4	91
8	Broad-Band Design Techniques for Transimpedance Amplifiers. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2007, 54, 590-600.	0.1	87
9	An 8T Differential SRAM With Improved Noise Margin for Bit-Interleaving in 65 nm CMOS. IEEE Transactions on Circuits and Systems I: Regular Papers, 2011, 58, 1252-1263.	5.4	79
10	Design and Optimization of the Extended True Single-Phase Clock-Based Prescaler. IEEE Transactions on Microwave Theory and Techniques, 2006, 54, 3828-3835.	4.6	77
11	Compact UWB Bandpass Filter With Ultra Narrow Notched Band. IEEE Microwave and Wireless Components Letters, 2010, 20, 145-147.	3.2	76
12	Design of a CMOS Broadband Transimpedance Amplifier With Active Feedback. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2010, 18, 461-472.	3.1	74
13	Design Exploration of Hybrid CMOS and Memristor Circuit by New Modified Nodal Analysis. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2012, 20, 1012-1025.	3.1	72
14	Miniaturized 60-GHz On-Chip Multimode Quasi-Elliptical Bandpass Filter. IEEE Electron Device Letters, 2013, 34, 945-947.	3.9	70
15	A Wideband Low Power Low-Noise Amplifier in CMOS Technology. IEEE Transactions on Circuits and Systems I: Regular Papers, 2010, 57, 773-782.	5.4	66
16	Design and Analysis of Ultra Low Power True Single Phase Clock CMOS 2/3 Prescaler. IEEE Transactions on Circuits and Systems I: Regular Papers, 2010, 57, 72-82.	5.4	63
17	A Subthreshold Low-Noise Amplifier Optimized for Ultra-Low-Power Applications in the ISM Band. IEEE Transactions on Microwave Theory and Techniques, 2008, 56, 286-292.	4.6	59
18	A Compact High-Performance Patch Antenna Array for 60-GHz Applications. IEEE Antennas and Wireless Propagation Letters, 2016, 15, 313-316.	4.0	59

#	ARTICLE	IF	CITATIONS
19	Compact Ultra-Wideband (UWB) Bandpass Filter With Ultra-Narrow Dual- and Quad-Notched Bands. IEEE Transactions on Microwave Theory and Techniques, 2011, 59, 1509-1519.	4.6	58
20	A Wideband and High Rejection Multimode Bandpass Filter Using Stub Perturbation. IEEE Microwave and Wireless Components Letters, 2009, 19, 24-26.	3.2	57
21	Compact Dual-Band Bandpass Filters Using Novel Embedded Spiral Resonator (ESR). IEEE Microwave and Wireless Components Letters, 2010, 20, 435-437.	3.2	56
22	RF CMOS low-phase-noise LC oscillator through memory reduction tail transistor. IEEE Transactions on Circuits and Systems II: Express Briefs, 2004, 51, 85-90.	3.0	55
23	A modified architecture used for input matching in CMOS low-noise amplifiers. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2005, 52, 784-788.	2.2	54
24	A 57-to-64-GHz 0.094-mm <sup>2</sup> 5-bit Passive Phase Shifter in 65-nm CMOS. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2016, 24, 1917-1925.	3.1	54
25	A Low Phase Noise and Wide Tuning Range Millimeter-Wave VCO Using Switchable Coupled VCO-Cores. IEEE Transactions on Circuits and Systems I: Regular Papers, 2015, 62, 554-563.	5.4	52
26	Power-Efficient Explicit-Pulsed Dual-Edge Triggered Sense-Amplifier Flip-Flops. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2011, 19, 1-9.	3.1	51
27	A 2-D Distributed Power Combining by Metamaterial-Based Zero Phase Shifter for 60-GHz Power Amplifier in 65-nm CMOS. IEEE Transactions on Microwave Theory and Techniques, 2013, 61, 505-516.	4.6	51
28	Design and Analysis of Wide Frequency-Tuning-Range CMOS 60 GHz VCO by Switching Inductor Loaded Transformer. IEEE Transactions on Circuits and Systems I: Regular Papers, 2014, 61, 699-711.	5.4	49
29	16.6- and 28-GHz Fully Integrated CMOS RF Switches With Improved Body Floating. IEEE Transactions on Microwave Theory and Techniques, 2008, 56, 339-345.	4.6	48
30	9.3-10.4-GHz-Band Cross-Coupled Complementary Oscillator With Low Phase-Noise Performance. IEEE Transactions on Microwave Theory and Techniques, 2004, 52, 1273-1278.	4.6	47
31	A 1.8-V 2.4/5.15-GHz dual-band LCVCO in 0.18- $\mu\text{m}$ CMOS technology. IEEE Microwave and Wireless Components Letters, 2006, 16, 194-196.	3.2	46
32	Cross-Coupled Current Conveyor Based CMOS Transimpedance Amplifier for Broadband Data Transmission. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2013, 21, 1516-1525.	3.1	46
33	Ultra-Wideband Low-Loss Switch Design in High-Resistivity Trap-Rich SOI With Enhanced Channel Mobility. IEEE Transactions on Microwave Theory and Techniques, 2017, 65, 3937-3949.	4.6	45
34	An 8-bit 200-MSample/s Pipelined ADC With Mixed-Mode Front-End S/H Circuit. IEEE Transactions on Circuits and Systems I: Regular Papers, 2008, 55, 1430-1440.	5.4	44
35	Low-power high-speed multiplier for error-tolerant application. , 2010, , .		43
36	Temperature-Compensated dB-linear Digitally Controlled Variable Gain Amplifier With DC Offset Cancellation. IEEE Transactions on Microwave Theory and Techniques, 2013, 61, 2648-2661.	4.6	43

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37	A 65 nm CMOS Power Amplifier With Peak PAE above 18.9% From 57 to 66 GHz Using Synthesized Transformer-Based Matching Network. IEEE Transactions on Circuits and Systems I: Regular Papers, 2015, 62, 2533-2543.	5.4	43
38	A 3.8 GHz Low-Noise CMOS Amplifier. IEEE Microwave and Wireless Components Letters, 2009, 19, 245-247.	3.2	41
39	Design of a 60-GHz Quasi-Yagi Antenna With Novel Ladder-Like Directors for Gain and Bandwidth Enhancements. IEEE Antennas and Wireless Propagation Letters, 2016, 15, 682-685.	4.0	41
40	Establishment and characterization of 12 human colorectal-carcinoma cell lines. , 1999, 81, 902-910.		40
41	A Miniaturized Millimeter-Wave Standing-Wave Filtering Switch With High P1dB. IEEE Transactions on Microwave Theory and Techniques, 2013, 61, 1505-1515.	4.6	40
42	Design of High-Q Millimeter-Wave Oscillator by Differential Transmission Line Loaded With Metamaterial Resonator in 65-nm CMOS. IEEE Transactions on Microwave Theory and Techniques, 2013, 61, 1892-1902.	4.6	40
43	A Low-Power Single-Phase Clock Multiband Flexible Divider. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2012, 20, 376-380.	3.1	39
44	Design and Sensitivity Analysis of a New Current-Mode Sense Amplifier for Low-Power SRAM. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2011, 19, 196-204.	3.1	38
45	A High Speed Low Power CAM With a Parity Bit and Power-Gated ML Sensing. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2013, 21, 151-156.	3.1	38
46	Design of a low power wide-band high resolution programmable frequency divider. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2005, 13, 1098-1103.	3.1	37
47	0.77 fJ/bit/search Content Addressable Memory Using Small Match Line Swing and Automated Background Checking Scheme for Variation Tolerance. IEEE Journal of Solid-State Circuits, 2014, 49, 1487-1498.	5.4	37
48	Fully Symmetrical Monolithic Transformer (True 1:1) for Silicon RFIC. IEEE Transactions on Microwave Theory and Techniques, 2008, 56, 2301-2311.	4.6	36
49	Miniaturized 3-bit Phase Shifter for 60 GHz Phased-Array in 65 nm CMOS Technology. IEEE Microwave and Wireless Components Letters, 2014, 24, 50-52.	3.2	36
50	An Energy-Aware CMOS Receiver Front End for Low-Power 2.4-GHz Applications. IEEE Transactions on Circuits and Systems I: Regular Papers, 2010, 57, 2675-2684.	5.4	35
51	Effect of technology scaling on the 1/f noise of deep submicron PMOS transistors. Solid-State Electronics, 2004, 48, 1101-1109.	1.4	34
52	Design of a Ku-band Low-Phase-Noise VCO Using the Dual LC Tanks. IEEE Transactions on Circuits and Systems II: Express Briefs, 2012, 59, 262-266.	3.0	30
53	Physical Layout Design Optimization of Integrated Spiral Inductors for Silicon-Based RFIC Applications. IEEE Transactions on Electron Devices, 2005, 52, 2559-2567.	3.0	29
54	Development of a miniaturized stimulation device for electrical stimulation of cells. Journal of Biological Engineering, 2015, 9, 14.	4.7	29

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55	$\mu$ S-band High-PAE Wide-Tuning-Range VCO Using Triple-Coupled LC Tanks. IEEE Transactions on Circuits and Systems II: Express Briefs, 2013, 60, 736-740.	3.0	28
56	A low-power 16 $\times$ –16-b parallel multiplier utilizing pass-transistor logic. IEEE Journal of Solid-State Circuits, 1999, 34, 1395-1399.	5.4	27
57	Bidirectional Diode-Triggered Silicon-Controlled Rectifiers for Low-Voltage ESD Protection. IEEE Electron Device Letters, 2012, 33, 1360-1362.	3.9	27
58	An Ultra-Compact Hairpin Band Pass Filter With Additional Zero Points. IEEE Microwave and Wireless Components Letters, 2007, 17, 262-264.	3.2	26
59	A Compact 2.1–39 GHz Self-Biased Low-Noise Amplifier in 65 nm CMOS Technology. IEEE Microwave and Wireless Components Letters, 2013, 23, 662-664.	3.2	26
60	Coupled Dual LC Tanks Based ILFD With Low Injection Power and Compact Size. IEEE Microwave and Wireless Components Letters, 2014, 24, 105-107.	3.2	25
61	A 4 GHz 60 dB Variable Gain Amplifier With Tunable DC Offset Cancellation in 65 nm CMOS. IEEE Microwave and Wireless Components Letters, 2015, 25, 37-39.	3.2	25
62	A Compact 57–67 GHz Bidirectional LNAPA in 65-nm CMOS Technology. IEEE Microwave and Wireless Components Letters, 2016, 26, 628-630.	3.2	25
63	A Low-Power Static Dual Edge-Triggered Flip-Flop using an Output-Controlled Discharge Configuration. , 0, , .		24
64	A Reconfigurable K-/Ka-Band Power Amplifier With High PAE in 0.18- $\mu$ m SiGe BiCMOS for Multi-Band Applications. IEEE Transactions on Microwave Theory and Techniques, 2015, 63, 4395-4405.	4.6	24
65	A 220–285 GHz SPDT Switch in 65-nm CMOS Using Switchable Resonator Concept. IEEE Transactions on Terahertz Science and Technology, 2015, 5, 649-651.	3.1	24
66	A 30-GHz Power-Efficient PLL Frequency Synthesizer for 60-GHz Applications. IEEE Transactions on Microwave Theory and Techniques, 2017, 65, 4165-4175.	4.6	24
67	Design and Characterization of Micro-LED Matrix Display With Heterogeneous Integration of GaN and BCD Technologies. IEEE Transactions on Electron Devices, 2019, 66, 4221-4227.	3.0	24
68	Impact of technology scaling on the $1/f$ noise of thin and thick gate oxide deep submicron NMOS transistors. IET Circuits, Devices and Systems, 2004, 151, 415.	0.6	23
69	Equivalent circuit model of on-wafer CMOS interconnects for RFICs. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2005, 13, 1060-1071.	3.1	23
70	Hybrid-Mode SRAM Sense Amplifiers: New Approach on Transistor Sizing. IEEE Transactions on Circuits and Systems II: Express Briefs, 2008, 55, 986-990.	3.0	23
71	Design of Ring-Oscillator-Based Injection-Locked Frequency Dividers With Single-Phase Inputs. IEEE Microwave and Wireless Components Letters, 2011, 21, 559-561.	3.2	23
72	Internet of Things: Trends, challenges and applications. , 2014, , .		23

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73	Imparting electroactivity to polycaprolactone fibers with heparin-doped polypyrrole: Modulation of hemocompatibility and inflammatory responses. <i>Acta Biomaterialia</i> , 2015, 23, 240-249.	8.3	23
74	High-performance low-power current sense amplifier using a cross-coupled current-mirror configuration. <i>IET Circuits, Devices and Systems</i> , 2002, 149, 308-314.	0.6	22
75	Analysis and Design of Ultra-Wideband Low-Noise Amplifier With Input/Output Bandwidth Optimization and Single-Ended/Differential-Input Reconfigurability. <i>IEEE Transactions on Industrial Electronics</i> , 2014, 61, 5672-5680.	7.9	22
76	Monolithic Sub-Terahertz SPDT Switches With Low Insertion Loss and Enhanced Isolation. <i>IEEE Transactions on Terahertz Science and Technology</i> , 2018, 8, 192-200.	3.1	22
77	Experimentally-based analytical model of deep-submicron LDD pMOSFETs in a Bi-MOS hybrid-mode environment. <i>IEEE Transactions on Electron Devices</i> , 1997, 44, 1473-1482.	3.0	21
78	Sub-1V bootstrapped CMOS driver for giga-scale-integration era. <i>Electronics Letters</i> , 1999, 35, 392.	1.0	21
79	Fully integrated CMOS fractional-N frequency divider for wide-band mobile applications with spurs reduction. <i>IEEE Transactions on Circuits and Systems Part 1: Regular Papers</i> , 2005, 52, 1042-1048.	0.1	21
80	Class-D Amplifier Power Stage With PWM Feedback Loop. <i>IEEE Transactions on Power Electronics</i> , 2013, 28, 3870-3881.	7.9	21
81	Ultra low-power high-speed flexible Probabilistic Adder for Error-Tolerant Applications. , 2011, , .		20
82	Design of Reconfigurable dB-Linear Variable-Gain Amplifier and Switchable-Order $g_{m}$ -C Filter in 65-nm CMOS Technology. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2019, 67, 5148-5158.	4.6	20
83	A Wideband dB-Linear VGA With Temperature Compensation and Active Load. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2019, 66, 3279-3287.	5.4	20
84	A Dual-Loop Clock and Data Recovery Circuit With Compact Quarter-Rate CMOS Linear Phase Detector. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2012, 59, 1156-1167.	5.4	19
85	A Dividerless PLL With Low Power and Low Reference Spur by Aperture-Phase Detector and Phase-to-Analog Converter. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2013, 60, 37-50.	5.4	19
86	Decentralized and Lightweight Approach to Detect Eclipse Attacks on Proof of Work Blockchains. <i>IEEE Transactions on Network and Service Management</i> , 2021, 18, 1659-1672.	4.9	19
87	Simple and accurate extraction methodology for RF MOSFET valid up to 20 GHz. <i>IET Circuits, Devices and Systems</i> , 2004, 151, 587.	0.6	18
88	Compact two-order bandpass filter with three finite zero points. <i>Electronics Letters</i> , 2005, 41, 846.	1.0	18
89	A Weak-Inversion Low-Power Active Mixer for 2.4 GHz ISM Band Applications. <i>IEEE Microwave and Wireless Components Letters</i> , 2009, 19, 719-721.	3.2	18
90	A Scalable RFCMOS Noise Model. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2009, 57, 1009-1019.	4.6	18

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91	A 2.4 GHz ultra low-power high gain LNA utilizing $\pi$ -match and capacitive feedback input network. , 2011, , .		18
92	Design of a Wideband Variable-Gain Amplifier With Self-Compensated Transistor for Accurate dB-Linear Characteristic in 65 nm CMOS Technology. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 4187-4198.	5.4	18
93	Ultra-low-voltage bootstrapped CMOS driver for high performance applications. Electronics Letters, 2000, 36, 706.	1.0	17
94	A novel tap input coupling structure for a narrow bandpass filter using TM/sub 010/ mode of a microstrip circular-disk resonator. IEEE Transactions on Microwave Theory and Techniques, 2002, 50, 1230-1232.	4.6	17
95	A 7.9-mW 5.6-GHz Digitally Controlled Variable Gain Amplifier With Linearization. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 3482-3490.	4.6	17
96	DC-30 GHz DPDT Switch Matrix Design in High Resistivity Trap-Rich SOI. IEEE Transactions on Electron Devices, 2017, 64, 3548-3554.	3.0	17
97	A Monolithically Integrated Single-Input Load-Modulated Balanced Amplifier With Enhanced Efficiency at Power Back-Off. IEEE Journal of Solid-State Circuits, 2021, 56, 1553-1564.	5.4	17
98	Low-power circuit implementation for partial-product addition using pass-transistor logic. IET Circuits, Devices and Systems, 1999, 146, 124.	0.6	16
99	Photoinduced intramolecular charge-transfer state of p-dimethylaminobenzoic acid in CdS and TiO <sub>2</sub> colloid solutions. Journal of Photochemistry and Photobiology A: Chemistry, 2000, 132, 105-114.	3.9	16
100	A Novel Methodology for the Design of LC Tank VCO with Low Phase Noise. , 0, , .		16
101	A 35-mW 30-dB Gain Control Range Current Mode Linear-in-Decibel Programmable Gain Amplifier With Bandwidth Enhancement. IEEE Transactions on Microwave Theory and Techniques, 2014, 62, 3465-3475.	4.6	16
102	Fully integrated 10 GHz CMOS VCO. Electronics Letters, 2001, 37, 1021.	1.0	15
103	New wideband $\hat{\cdot}$ dualband CMOS LC voltage-controlled oscillator. IET Circuits, Devices and Systems, 2003, 150, 453.	0.6	15
104	Comments on "Negative capacitance effect in semiconductor devices" [by M. Ershov et al., with reply]. IEEE Transactions on Electron Devices, 1999, 46, 2357-2358.	3.0	14
105	AN ULTRA LOW-POWER CURRENT-MODE SENSE AMPLIFIER FOR SRAM APPLICATIONS. Journal of Circuits, Systems and Computers, 2005, 14, 939-951.	1.5	14
106	Experimentally investigating slow-wave transmission lines and filters based on conductor-backed CPW periodic cells. , 2005, , .		14
107	DGS embedded transformed radial stub for ultra-wide stopband lowpass filter. Electronics Letters, 2012, 48, 1473.	1.0	14
108	A 60GHz on-chip antenna in standard CMOS silicon Technology. , 2012, , .		14

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109	GHz programmable counter with low power consumption. Electronics Letters, 2003, 39, 1572.	1.0	13
110	Parasitic-compensated quadrature LC oscillator. IET Circuits, Devices and Systems, 2004, 151, 45.	0.6	13
111	Low power high-speed CMOS dual-modulus prescaler design with imbalanced phase-switching technique. IET Circuits, Devices and Systems, 2005, 152, 127.	0.6	13
112	Criterion to Evaluate Input-Offset Voltage of a Latch-Type Sense Amplifier. IEEE Transactions on Circuits and Systems I: Regular Papers, 2010, 57, 83-92.	5.4	13
113	2.3 A 130-to-180GHz 0.0035mm <sup>2</sup> SPDT switch with 3.3dB loss and 23.7dB isolation in 65nm bulk CMOS. , 2015, . .		13
114	A 60-GHz Coplanar Waveguide-Based Bidirectional LNA in SiGe BiCMOS. IEEE Microwave and Wireless Components Letters, 2017, 27, 742-744.	3.2	13
115	Full-swing high speed CBiCMOS digital circuit for low-voltage applications. IET Circuits, Devices and Systems, 1995, 142, 8.	0.6	12
116	Impact of device scaling on the 1/f noise performance of deep submicrometer thin gate oxide CMOS devices. Solid-State Electronics, 2006, 50, 1219-1226.	1.4	12
117	Sensitivity Analysis of Coupled Interconnects for RFIC Applications. IEEE Transactions on Electromagnetic Compatibility, 2006, 48, 607-613.	2.2	12
118	Dual-band bandpass filter using embedded spiral resonator and broadside-coupled meander slot-line. Electronics Letters, 2010, 46, 1135.	1.0	12
119	Ultra-low power series input resonance differential common gate LNA. Electronics Letters, 2011, 47, 703.	1.0	12
120	Miniaturized 40-60 GHz On-Chip Balun With Capacitive Loading Compensation. IEEE Electron Device Letters, 2014, 35, 434-436.	3.9	12
121	Substrate-Induced Noise Model and Parameter Extraction for High-Frequency Noise Modeling of Sub-Micron MOSFETs. IEEE Transactions on Microwave Theory and Techniques, 2014, 62, 1973-1985.	4.6	12
122	A 26.8 dB Gain 19.7 dBm CMOS Power Amplifier Using 4-way Hybrid Coupling Combiner. IEEE Microwave and Wireless Components Letters, 2015, 25, 43-45.	3.2	12
123	Predistortion Linearizer for Wideband AM/PM Cancellation With Left-Handed Delay Line. IEEE Microwave and Wireless Components Letters, 2017, 27, 794-796.	3.2	12
124	New current conveyor for high-speed low-power current sensing. IET Circuits, Devices and Systems, 1998, 145, 85.	0.6	11
125	Accurate and scalable RF interconnect model for silicon-based RFIC applications. IEEE Transactions on Microwave Theory and Techniques, 2005, 53, 3035-3044.	4.6	11
126	A 1 V switchable CMOS LNA for 802.11A/B WLAN applications. Analog Integrated Circuits and Signal Processing, 2006, 48, 181-184.	1.4	11



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127	RFCMOS Unit Width Optimization Technique. IEEE Transactions on Microwave Theory and Techniques, 2007, 55, 1844-1853.	4.6	11
128	A random number generator for low power cryptographic application. , 2010, , .		11
129	Sensing Margin Enhancement Techniques for Ultra-Low-Voltage SRAMs Utilizing a Bitline-Boosting Current and Equalized Bitline Leakage. IEEE Transactions on Circuits and Systems II: Express Briefs, 2012, 59, 868-872.	3.0	11
130	MOSFET Drain Current Noise Modeling With Effective Gate Overdrive and Junction Noise. IEEE Electron Device Letters, 2012, 33, 1117-1119.	3.9	11
131	THRU-Based Cascade De-embedding Technique for On-Wafer Characterization of RF CMOS Devices. IEEE Transactions on Electron Devices, 2013, 60, 2892-2899.	3.0	11
132	A broadband CMOS LNA for WLAN applications. , 0, , .		10
133	1â€...V 10â€...GHz CMOS frequency divider with low power consumption. Electronics Letters, 2004, 40, 467.	1.0	10
134	High Frequency Thick Film BST Ferroelectric Phase Shifter. Integrated Ferroelectrics, 2004, 61, 65-70.	0.7	10
135	A 2.4 GHz ultra low power subthreshold CMOS low-noise amplifier. Microwave and Optical Technology Letters, 2007, 49, 743-744.	1.4	10
136	Modeling and Layout Optimization of Differential Inductors for Silicon-Based RFIC Applications. IEEE Transactions on Electron Devices, 2008, 55, 1058-1066.	3.0	10
137	A SPICE COMPATIBLE MODEL OF ON-WAFER COUPLED INTERCONNECTS FOR CMOS RFICs. Progress in Electromagnetics Research, 2010, 102, 287-299.	4.4	10
138	An Accurate Two-Port De-Embedding Technique for RF/Millimeter-Wave Noise Characterization and Modeling of Deep Submicrometer Transistors. IEEE Transactions on Microwave Theory and Techniques, 2011, 59, 479-487.	4.6	10
139	A low-power CAM with efficient power and delay trade-off. , 2011, , .		10
140	A Cross-Coupled LPF Topology and Design for Millimeter-Wave RFIC Applications. IEEE Transactions on Electron Devices, 2012, 59, 2902-2909.	3.0	10
141	Ultra-wide rejection band lowpass cell. Electronics Letters, 2012, 48, 99.	1.0	10
142	A 160 nW 25 kS/s 9-bit SAR ADC for neural signal recording applications. , 2012, , .		10
143	Impact of velocity saturation and hot carrier effects on channel thermal noise model of deep sub-micron MOSFETs. Solid-State Electronics, 2012, 72, 8-11.	1.4	10
144	A New Millimeter-Wave Fixture Deembedding Method Based on Generalized Cascade Network Model. IEEE Electron Device Letters, 2013, 34, 447-449.	3.9	10

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145	MODELING AND LAYOUT OPTIMIZATION TECHNIQUES FOR SILICON-BASED SYMMETRICAL SPIRAL INDUCTORS. Progress in Electromagnetics Research, 2013, 143, 1-18.	4.4	10
146	High-Frequency Noise Modeling of MOSFETs for Ultra Low-Voltage RF Applications. IEEE Transactions on Microwave Theory and Techniques, 2015, 63, 141-154.	4.6	10
147	A Hybrid Pad-Line-Finger De-Embedding Technique for Broadband Modeling of CMOS Transistor. IEEE Microwave and Wireless Components Letters, 2016, 26, 507-509.	3.2	10
148	Review of high efficiency integrated LED lighting. , 2017, , .		10
149	1.1 V full-swing double bootstrapped BiCMOS logic gates. IET Circuits, Devices and Systems, 1996, 143, 41.	0.6	9
150	1.5 V 1.8 GHz bandpass amplifier. IET Circuits, Devices and Systems, 2000, 147, 331.	0.6	9
151	A Low Power Fully Programmable 1MHz Resolution 2.4GHz CMOS PLL Frequency Synthesizer. , 2007, , .		9
152	Sub-1 V Low Power Wide Range Injection-Locked Frequency Divider. IEEE Microwave and Wireless Components Letters, 2007, 17, 528-530.	3.2	9
153	A 60GHz VCO with 25.8% tuning range by switching return-path in 65nm CMOS. , 2012, , .		9
154	RADIAL LOADED TRANSFORMED RADIAL STUB FOR LPF STOPBAND EXTENSION. Progress in Electromagnetics Research Letters, 2012, 30, 125-132.	0.7	9
155	A new field dependent mobility model for high frequency channel thermal noise of deep submicron RFCMOS. Solid-State Electronics, 2012, 68, 32-37.	1.4	9
156	A compact dual-band meander-line antenna for biomedical applications. , 2013, , .		9
157	A 35 mW 30 dB gain control range current mode programmable gain amplifier with DC offset cancellation. , 2014, , .		9
158	Micro-LED arrays for display and communication: Device structure and driver architecture. , 2017, , .		9
159	A $\sqrt{V_S}$ Band Wide Locking Range Divide-by-4 Injection-Locked Frequency Divider. IEEE Microwave and Wireless Components Letters, 2018, 28, 1020-1022.	3.2	9
160	Physically-based RF model for metal-oxide-metal capacitors. Electronics Letters, 2000, 36, 425.	1.0	8
161	Effects of polysilicon shield on spiral inductors for silicon-based RF IC's. , 0, , .		8
162	Metallization proximity studies for copper spiral inductors on silicon. IEEE Transactions on Semiconductor Manufacturing, 2003, 16, 220-227.	1.7	8

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163	Low-power high-performance explicit-pulsed flip-flop using static latch and dynamic pulse generator. IET Circuits, Devices and Systems, 2006, 153, 253.	0.6	8
164	0.9 V current-mode sense amplifier using concurrent bit- and data-line tracking and sensing techniques. Electronics Letters, 2007, 43, 1421.	1.0	8
165	Sub-mW multi-GHz CMOS dual-modulus prescalers based on programmable injection-locked frequency dividers. , 2008, , .		8
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