

Andreas GrÃ¼bl

List of Publications by Year in descending order

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Version: 2024-02-01

18
papers

1,178
citations

933447

10
h-index

1281871

11
g-index

19
all docs

19
docs citations

19
times ranked

1057
citing authors

#	ARTICLE	IF	CITATIONS
1	A wafer-scale neuromorphic hardware system for large-scale neural modeling. , 2010, , .		449
2	Six Networks on a Universal Neuromorphic Computing Substrate. <i>Frontiers in Neuroscience</i> , 2013, 7, 11.	2.8	131
3	Neuromorphic hardware in the loop: Training a deep spiking network on the BrainScaleS wafer-scale system. , 2017, , .		99
4	Demonstrating Hybrid Learning in a Flexible Neuromorphic Hardware System. <i>IEEE Transactions on Biomedical Circuits and Systems</i> , 2017, 11, 128-142.	4.0	87
5	Demonstrating Advantages of Neuromorphic Computation: A Pilot Study. <i>Frontiers in Neuroscience</i> , 2019, 13, 260.	2.8	83
6	A comprehensive workflow for general-purpose neural modeling with highly configurable neuromorphic hardware systems. <i>Biological Cybernetics</i> , 2011, 104, 263-296.	1.3	72
7	An Accelerated LIF Neuronal Network Array for a Large-Scale Mixed-Signal Neuromorphic Architecture. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2018, 65, 4299-4312.	5.4	59
8	Live demonstration: A scaled-down version of the BrainScaleS wafer-scale neuromorphic system. , 2012, , .		41
9	A Mixed-Signal Structured AdEx Neuron for Accelerated Neuromorphic Cores. <i>IEEE Transactions on Biomedical Circuits and Systems</i> , 2018, 12, 1027-1037.	4.0	38
10	Surrogate gradients for analog neuromorphic computing. <i>Proceedings of the National Academy of Sciences of the United States of America</i> , 2022, 119, .	7.1	37
11	Verification and Design Methods for the BrainScaleS Neuromorphic Hardware System. <i>Journal of Signal Processing Systems</i> , 2020, 92, 1277-1292.	2.1	25
12	Accelerated Physical Emulation of Bayesian Inference in Spiking Neural Networks. <i>Frontiers in Neuroscience</i> , 2019, 13, 1201.	2.8	22
13	Effect of Heterogeneity on Decorrelation Mechanisms in Spiking Neural Networks: A Neuromorphic-Hardware Study. <i>Physical Review X</i> , 2016, 6, .	8.9	15
14	Interconnecting VLSI Spiking Neural Networks Using Isochronous Connections. , 2007, , 471-478.		10
15	The operating system of the neuromorphic BrainScaleS-1 system. <i>Neurocomputing</i> , 2022, 501, 790-810.	5.9	5
16	Full wafer redistribution and wafer embedding as key technologies for a multi-scale neuromorphic hardware cluster. , 2017, , .		4
17	Robustness from structure: Inference with hierarchical spiking networks on analog neuromorphic hardware. , 2017, , .		1
18	Demonstrating BrainScaleS-2 Inter-Chip Pulse-Communication using EXTOLL. , 2022, , .		0