Jinuk Kim

List of Publications by Year
in descending order

28 papers	543 citations	933447 10 h-index	752698 20 g-index
28 all docs	28 docs citations	28 times ranked	639 citing authors

#	Article	IF	Citations
1	Miniaturization for wearable EEG systems: recording hardware and data processing. Biomedical Engineering Letters, 2022, 12, 239-250.	4.1	3
2	Energy-Efficient High-Voltage Pulsers for Ultrasound Transducers. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 19-23.	3.0	11
3	Low-power integrated circuits for wearable electrophysiology. , 2021, , 163-199.		7
4	A biopolymer-based functional separator for stable Li metal batteries with an additive-free commercial electrolyte. Journal of Materials Chemistry A, 2021, 9, 7774-7781.	10.3	25
5	Energy-Efficient Integrated Circuit Solutions Toward Miniaturized Closed-Loop Neural Interface Systems. Frontiers in Neuroscience, 2021, 15, 667447.	2.8	9
6	Guest Editorial Special Issue on Selected Papers From IEEE ISCAS 2020. IEEE Transactions on Biomedical Circuits and Systems, 2021, 15, 366-368.	4.0	0
7	An Optically Addressed Nanowire-Based Retinal Prosthesis With Wireless Stimulation Waveform Control and Charge Telemetering. IEEE Journal of Solid-State Circuits, 2021, 56, 3263-3273.	5.4	10
8	A Load-Current-Regulating OLED Lamp Driver Using a Hybrid Step-Up Converter with 93.21% Efficiency at a High Conversion Ratio of 4.1. , 2021, , .		0
9	A review on recent approaches for designing the SEI layer on sodium metal anodes. Materials Advances, 2020, 1, 3143-3166.	5.4	42
10	An Energy-Efficient Three-Stage Amplifier Achieving a High Unity-Gain Bandwidth for Large Capacitive Loads Without Using a Compensation Zero. IEEE Solid-State Circuits Letters, 2020, 3, 530-533.	2.0	3
11	Design of Reconfigurable Time-to-Digital Converter Based on Cascaded Time Interpolators for Electrical Impedance Spectroscopy. Sensors, 2020, 20, 1889.	3.8	8
12	Digitally Adaptive High-Fidelity Analog Array Signal Processing Resilient to Capacitive Multiplying DAC Inter-Stage Gain Error. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 4095-4107.	5.4	2
13	RF power transmission and its considerations for ECoG implants. , 2019, , 121-144.		0
14	A 3 mm Ã— 3 mm Fully Integrated Wireless Power Receiver and Neural Interface System-on-Chip. IEEE Transactions on Biomedical Circuits and Systems, 2019, 13, 1736-1746.	4.0	34
15	A Fully Integrated RF-Powered Energy-Replenishing Current-Controlled Stimulator. IEEE Transactions on Biomedical Circuits and Systems, 2019, 13, 191-202.	4.0	18
16	A 500-MHz Bandwidth 7.5-mV_{pp} Ripple Power-Amplifier Supply Modulator for RF Polar Transmitters. IEEE Journal of Solid-State Circuits, 2018, 53, 1653-1665.	5.4	10
17	Sub-<inline-formula> <tex-math notation="LaTeX">mu </tex-math> </inline-formula>V_{rms}-Noise Sub-<inline-formula> <tex-math notation="LaTeX">mu </tex-math> </inline-formula>W/Channel ADC-Direct Neural Recording With 200-mV/ms Transient Recovery Through Predictive Digital Autoranging. IEEE Journal of Solid-State Circuits, 2018, 53, 3101-3110.	5.4	65
18	Design of miniaturized wireless power receivers for mm-sized implants. , 2017, , .		13

#	Article	IF	Citations
19	Silicon-Integrated High-Density Electrocortical Interfaces. Proceedings of the IEEE, 2017, 105, 11-33.	21.3	68
20	A 144-MHz Fully Integrated Resonant Regulating Rectifier With Hybrid Pulse Modulation for mm-Sized Implants. IEEE Journal of Solid-State Circuits, 2017, 52, 3043-3055.	5.4	67
21	Wireless powering of mm-scale fully-on-chip neural interfaces. , 2017, , .		16
22	Energy Recycling Telemetry IC With Simultaneous 11.5 mW Power and 6.78 Mb/s Backward Data Delivery Over a Single 13.56 MHz Inductive Link. IEEE Journal of Solid-State Circuits, 2016, 51, 2664-2678.	5.4	52
23	A 6.5- $mu ext{W}$ /MHz Charge Buffer With 7-fF Input Capacitance in 65-nm CMOS for Noncontact Electropotential Sensing. IEEE Transactions on Circuits and Systems II: Express Briefs, 2016, 63, 1161-1165.	3.0	4
24	A fully integrated 144 MHz wireless-power-receiver-on-chip with an adaptive buck-boost regulating rectifier and low-loss H-Tree signal distribution. , 2016, , .		15
25	A 1.3 mW 48 MHz 4 Channel MIMO Baseband Receiver With 65 dB Harmonic Rejection and 48.5 dB Spatial Signal Separation. IEEE Journal of Solid-State Circuits, 2016, 51, 832-844.	5.4	18
26	PSR Enhancement Through Super Gain Boosting and Differential Feed-Forward Noise Cancellation in a 65-nm CMOS LDO Regulator. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2014, 22, 2181-2191.	3.1	37
27	A CMOS LDO regulator with high PSR using Gain Boost-Up and Differential Feed Forward Noise Cancellation in 65nm process. , 2012, , .		4
28	Hybrid switching amplifier using a novel two-quadrant wideband buffer for dynamic power supply applications. Power Electronics Specialist Conference (PESC), IEEE, 2008, , .	0.0	2