Xuechen Zhang

List of Publications by Year
in descending order

21 papers	270 citations	1684188 5 h-index	1588992 8 g-index
21 all docs	21 docs citations	21 times ranked	206 citing authors

#	Article	IF	Citations
1	Flexpath: Type-Based Publish/Subscribe System for Large-Scale Science Analytics. , 2014, , .		61
2	IOrchestrator: Improving the Performance of Multi-node I/O Systems via Inter-Server Coordination. , 2010, , .		49
3	QoS support for end users of I/O-intensive applications using shared storage systems. , 2011, , .		33
4	InterferenceRemoval. , 2010, , .		29
5	UPS: Unified PMU-Data Storage System to Enhance T+D PMU Data Usability. IEEE Transactions on Smart Grid, 2020, 11, 739-748.	9.0	18
6	Understanding issue correlations. , 2015, , .		17
7	Making sense of performance in in-memory computing frameworks for scientific data analysis: A case study of the spark system. Journal of Parallel and Distributed Computing, 2018, 120, 369-382.	4.1	11
8	Large-scale adaptive mesh simulations through non-volatile byte-addressable memory. , 2017, , .		10
9	WOWMON: A Machine Learning-based Profiler for Self-adaptive Instrumentation of Scientific Workflows. Procedia Computer Science, 2016, 80, 1507-1518.	2.0	7
10	Optimizing Parallel I/O Accesses through Pattern-Directed and Layout-Aware Replication. IEEE Transactions on Computers, 2020, 69, 212-225.	3.4	6
11	YouChoose. ACM Transactions on Storage, 2011, 7, 1-18.	2.1	5
12	NVGraph: Enforcing Crash Consistency of Evolving Network Analytics in NVMM Systems. IEEE Transactions on Parallel and Distributed Systems, 2020, 31, 1255-1269.	5.6	5
13	IR+: Removing parallel I/O interference of MPI programs via data replication over heterogeneous storage devices. Parallel Computing, 2018, 76, 91-105.	2.1	4
14	Persistent Octrees for Parallel Mesh Refinement through Non-Volatile Byte-Addressable Memory. IEEE Transactions on Parallel and Distributed Systems, 2019, 30, 677-691.	5.6	4
15	Approximating the k-Minimum Distance Rumor Source Detection in Online Social Networks. , 2018, , .		3
16	FlashStager: Improving the Performance of SSD-Based Data Staging Systems via Write Redirection. , 2016, , .		2
17	Enforcing End-to-End I/O Policies for Scientific Workflows Using Software-Defined Storage Resource Enclaves. IEEE Transactions on Multi-Scale Computing Systems, 2018, 4, 662-675.	2.4	2
18	DeStager: feature guided in-situ data management in distributed deep memory hierarchies. Distributed and Parallel Databases, 2019, 37, 209-231.	1.6	2

#	Article	IF	Citations
19	Understanding Software Platforms for In-Memory Scientific Data Analysis: A Case Study of the Spark System. , 2016, , .		1
20	Compiler aided checkpointing using crash-consistent data structures in NVMM systems. , 2020, , .		1
21	NVSwap: Latency-Aware Paging using Non-Volatile Main Memory. , 2021, , .		0