Saransh Gupta

List of Publications by Year in descending order

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#	Article	IF	CITATIONS
1	Logic Design Within Memristive Memories Using Memristor-Aided loGIC (MAGIC). IEEE Nanotechnology Magazine, 2016, 15, 635-650.	2.0	244
2	FloatPIM. , 2019, , .		141
3	FELIX. , 2018, , .		97
4	Ultra-Efficient Processing In-Memory for Data Intensive Applications. , 2017, , .		69
5	DUAL: Acceleration of Clustering Algorithms using Digital-based Processing In-Memory. , 2020, , .		51
6	SearcHD: A Memory-Centric Hyperdimensional Computing With Stochastic Training. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2020, 39, 2422-2433.	2.7	43
7	NNPIM: A Processing In-Memory Architecture for Neural Network Acceleration. IEEE Transactions on Computers, 2019, 68, 1325-1337.	3.4	38
8	RMAC. , 2018, , .		29
9	FACH., 2019,,.		27
10	GRAM., 2019,,.		27
11	HDCluster: An Accurate Clustering Using Brain-Inspired High-Dimensional Computing. , 2019, , .		26
12	Efficient query processing in crossbar memory. , 2017, , .		20
13	Deep Learning Acceleration with Neuron-to-Memory Transformation. , 2020, , .		14
14	GAS., 2018,,.		12
15	DigitalPIM. , 2019, , .		12
16	NVQuery: Efficient Query Processing in Nonvolatile Memory. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2019, 38, 628-639.	2.7	11
17	Exploring Processing In-Memory for Different Technologies. , 2019, , .		9
18	GenPIM: Generalized processing in-memory to accelerate data intensive applications. , 2018, , .		6

2

SARANSH GUPTA

#	Article	IF	Citations
19	Invited: Accelerating Fully Homomorphic Encryption with Processing in Memory. , 2021, , .		6
20	HDnn-PIM: Efficient in Memory Design of Hyperdimensional Computing with Feature Extraction. , 2022, ,		6
21	Thermal-Aware Design and Management for Search-based In-Memory Acceleration. , 2019, , .		4
22	DP-Sim., 2021,,.		3
23	Stochastic-HD: Leveraging Stochastic Computing on Hyper-Dimensional Computing. , 2021, , .		2
24	UPIM: Unipolar Switching Logic for High Density Processing-in-Memory Applications. , 2019, , .		1
25	Implementing binary neural networks in memory with approximate accumulation. , 2020, , .		1
26	COSMO: Computing with Stochastic Numbers in Memory. ACM Journal on Emerging Technologies in Computing Systems, 2022, 18, 1-25.	2.3	1
27	Deep Learning Acceleration using Digital-Based Processing In-Memory. , 2020, , .		0
28	Massively Parallel Big Data Classification on a Programmable Processing In-Memory Architecture. , 2021, , .		0
29	Stochastic-HD: Leveraging Stochastic Computing on the Hyper-Dimensional Computing Pipeline. Frontiers in Neuroscience, 2022, 16, .	2.8	0