

Franz Kreupl

List of Publications by Year in descending order

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48
papers

2,974
citations

257450

24
h-index

377865

34
g-index

49
all docs

49
docs citations

49
times ranked

2903
citing authors

#	ARTICLE	IF	CITATIONS
1	Carbon nanotubes in interconnect applications. <i>Microelectronic Engineering</i> , 2002, 64, 399-408.	2.4	566
2	Reconfigurable Silicon Nanowire Transistors. <i>Nano Letters</i> , 2012, 12, 119-124.	9.1	343
3	Silicon-Nanowire Transistors with Intruded Nickel-Silicide Contacts. <i>Nano Letters</i> , 2006, 6, 2660-2666.	9.1	231
4	How do carbon nanotubes fit into the semiconductor roadmap?. <i>Applied Physics A: Materials Science and Processing</i> , 2005, 80, 1141-1151.	2.3	172
5	Chemical Vapor Deposition Growth of Single-Walled Carbon Nanotubes at 600 Å°C and a Simple Growth Model. <i>Journal of Physical Chemistry B</i> , 2004, 108, 1888-1893.	2.6	157
6	On the Applicability of Single-Walled Carbon Nanotubes as VLSI Interconnects. <i>IEEE Nanotechnology Magazine</i> , 2009, 8, 542-559.	2.0	156
7	High-Current Nanotube Transistors. <i>Nano Letters</i> , 2004, 4, 831-834.	9.1	143
8	Sub-20 nm Short Channel Carbon Nanotube Transistors. <i>Nano Letters</i> , 2005, 5, 147-150.	9.1	128
9	Electrochemical functionalization of multi-walled carbon nanotubes for solvation and purification. <i>Current Applied Physics</i> , 2002, 2, 107-111.	2.4	123
10	Towards the integration of carbon nanotubes in microelectronics. <i>Diamond and Related Materials</i> , 2004, 13, 1296-1300.	3.9	91
11	Carbon Nanotubes for Microelectronics?. <i>Small</i> , 2005, 1, 382-390.	10.0	90
12	Carbon nanotubes for microelectronics: status and future prospects. <i>Materials Science and Engineering C</i> , 2003, 23, 663-669.	7.3	80
13	Growth of Isolated Carbon Nanotubes with Lithographically Defined Diameter and Location. <i>Nano Letters</i> , 2003, 3, 257-259.	9.1	75
14	Ways towards the scaleable integration of carbon nanotubes into silicon based technology. <i>Diamond and Related Materials</i> , 2004, 13, 354-361.	3.9	65
15	Reconfigurable Nanowire Electronics-Enabling a Single CMOS Circuit Technology. <i>IEEE Nanotechnology Magazine</i> , 2014, 13, 1020-1028.	2.0	63
16	In-Situ Contacted Single-Walled Carbon Nanotubes and Contact Improvement by Electroless Deposition. <i>Nano Letters</i> , 2003, 3, 965-968.	9.1	60
17	The origin of the integral barrier height in inhomogeneous Au/Co/GaAs ₆₇ P ₃₃ -Schottky contacts: A ballistic electron emission microscopy study. <i>Journal of Applied Physics</i> , 1998, 83, 358-365.	2.5	44
18	Potential pinch-off effect in inhomogeneous Au/Co/GaAs ₆₇ P ₃₃ (100)-Schottky contacts. <i>Applied Physics Letters</i> , 1997, 70, 2559-2561.	3.3	37

#	ARTICLE	IF	CITATIONS
19	Silicon to nickel-silicide axial nanowire heterostructures for high performance electronics. <i>Physica Status Solidi (B): Basic Research</i> , 2007, 244, 4170-4175.	1.5	34
20	Carbon nanotubes finally deliver. <i>Nature</i> , 2012, 484, 321-322.	27.8	32
21	Hydrogen evolution activity of individual mono-, bi-, and few-layer MoS ₂ towards photocatalysis. <i>Applied Materials Today</i> , 2017, 8, 132-140.	4.3	32
22	High Performance X-Ray Transmission Windows Based on Graphenic Carbon. <i>IEEE Transactions on Nuclear Science</i> , 2015, 62, 588-593.	2.0	27
23	Silicon nanowires: catalytic growth and electrical characterization. <i>Physica Status Solidi (B): Basic Research</i> , 2006, 243, 3340-3345.	1.5	26
24	Carbon-based resistive memory. , 2008, , .		26
25	Low-Resistivity Long-Length Horizontal Carbon Nanotube Bundles for Interconnect Applicationsâ€”Part I: Process Development. <i>IEEE Transactions on Electron Devices</i> , 2013, 60, 2862-2869.	3.0	25
26	Nanoelectronics beyond silicon. <i>Microelectronic Engineering</i> , 2006, 83, 619-623.	2.4	18
27	The carbon-nanotube computer has arrived. <i>Nature</i> , 2013, 501, 495-496.	27.8	18
28	Low-Resistivity Long-Length Horizontal Carbon Nanotube Bundles for Interconnect Applicationsâ€”Part II: Characterization. <i>IEEE Transactions on Electron Devices</i> , 2013, 60, 2870-2876.	3.0	16
29	Design and properties of low-energy X-ray transmission windows based on graphenic carbon. <i>Physica Status Solidi (B): Basic Research</i> , 2015, 252, 2564-2573.	1.5	15
30	Tuning the Polarity of Si-Nanowire Transistors Without the Use of Doping. , 2008, , .		13
31	Carbon-based Materials as Key-enabler for â€œMore Than Mooreâ€”Materials Research Society Symposia Proceedings, 2011, 1303, 57.	0.1	13
32	Performance Improvement of Graphenic Carbon X-ray Transmission Windows. <i>MRS Advances</i> , 2016, 1, 1441-1446.	0.9	7
33	Energy of CDM Failure for ICs on Package-, Wafer-and Board-Level. , 2019, , .		7
34	Carbon-nanotube computer scaled up. <i>Nature</i> , 2019, 572, 588-589.	27.8	6
35	Graphenic carbon-silicon contacts for reliability improvement of metal-silicon junctions. , 2016, , .		5
36	A CMOS Temperature Stabilized 2-D Mechanical Stress Sensor With 11-bit Resolution. <i>IEEE Journal of Solid-State Circuits</i> , 2020, 55, 846-855.	5.4	5

#	ARTICLE	IF	CITATIONS
37	A Cost-Effective, Impedimetric Na ⁺ -Sensor in Fluids. , 2021, 5, 1-4.		5
38	Large-scale integration of carbon nanotubes into silicon-based microelectronics. , 2003, , .		2
39	A CMOS Temperature Stabilized 2-Dimensional Mechanical Stress Sensor with 11-bit Resolution. , 2019, , .		2
40	Carbon Nanotubes: Can they become a microelectronics technology?. AIP Conference Proceedings, 2004, , .	0.4	1
41	Advancing CMOS with carbon electronics. , 2014, , .		1
42	Advancing CMOS with carbon electronics. , 2014, , .		1
43	Catalytic CVD of SWCNTs at Low Temperatures and SWCNT Devices. AIP Conference Proceedings, 2004, , .	0.4	0
44	Session 7: Solid-state and nanoelectronic devices - spin devices, batteries and steep slope FETs. , 2008, , .		0
45	ã,«ãf¼ãfœãf³ãfŠãfŽãfãf¼ãf–ãfšã,³ãf³ãf”ãfãf¼ãfã,ãf¼ãf,è© ã¼œ. Nature Digest, 2013, 10, 30-31.	0.0	0
46	Overview of Carbon Nanotube Processing Methods. , 2017, , 81-100.		0
47	Highly Reliable Contacts to Silicon Enabled by Low Temperature Sputtered Graphenic Carbon. IEEE Journal of the Electron Devices Society, 2019, 7, 252-260.	2.1	0
48	Simulation of the Transient Potential Distribution On-Chip During a Fast ESD Event Based on a Parametric Measurement Analysis. , 2020, , .		0