

# P Karuppanan

## List of Publications by Year in descending order

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28  
papers

470  
citations

1040056

9  
h-index

794594

19  
g-index

30  
all docs

30  
docs citations

30  
times ranked

340  
citing authors

#	ARTICLE	IF	CITATIONS
1	High-gain patch antenna design using PRS and ground plane reflector for THz band applications. Optik, 2021, 232, 166559.	2.9	2
2	Investigation and design of microstrip patch antenna employed on PCs substrates in THz regime. Australian Journal of Electrical and Electronics Engineering, 2021, 18, 118-125.	1.2	5
3	Proximity-coupled high gain graphene patch antenna using holey dielectric superstrate for terahertz applications. Optik, 2021, 240, 166793.	2.9	9
4	Modelling, simulation and analysis of high step up DC-DC converter using coupled inductor and voltage multiplier cell using PSCAD. International Journal of Modelling and Simulation, 2020, 40, 29-36.	3.3	9
5	Enhanced radiation characteristics of graphene-based patch antenna array employing photonic crystals and dielectric grating for THz applications. Optik, 2020, 200, 163422.	2.9	17
6	Parasitic-coupled high-gain graphene antenna employed on PBG dielectric grating substrate for THz applications. Microwave and Optical Technology Letters, 2020, 62, 439-447.	1.4	9
7	Design of a SIW on-chip antenna using 0.18- $\mu$ m CMOS process technology at 0.4 THz. Optik, 2020, 223, 165509.	2.9	4
8	Design and analysis of Vivaldi antenna with enhanced radiation characteristics for mm-wave and THz applications. Optical and Quantum Electronics, 2019, 51, 1.	3.3	13
9	Design and Implementation of Current Harmonic Filter. , 2019, , .		0
10	Proximity feed multiband patch antenna array with SRR and PBG for THz applications. Optik, 2018, 175, 78-86.	2.9	16
11	Design and analysis of novel microstrip patch antenna on photonic crystal in THz. Physica B: Condensed Matter, 2018, 545, 107-112.	2.7	130
12	Study and Analysis of A Simple Self Cascode Regulated Cascode Amplifier. International Journal of Engineering, Transactions A: Basics, 2018, 31, .	0.4	0
13	Energy Grid Management, Optimization and Economic Analysis of Microgrid. Green Energy and Technology, 2017, , 289-325.	0.6	0
14	Analysis of low power feed through logic with leakage control technique. , 2017, , .		1
15	HCC-based interleaved boost converter with optimal switching frequency control of wind energy conversion system for DC microgrid application. Journal of Engineering, 2017, 2017, 495-505.	1.1	1
16	Grid-Connected and Off-Grid Solar Photovoltaic System. Green Energy and Technology, 2017, , 125-157.	0.6	21
17	A Fully Differential Operational Amplifier with Slew Rate Enhancer and Adaptive Bias for Ultra Low Power. Journal of Low Power Electronics, 2017, 13, 67-75.	0.6	8
18	Small signal modeling and stability analysis of N-phase interleaved boost converter. , 2016, , .		2

#	ARTICLE	IF	CITATIONS
19	An Improved PSO approach for optimal tuning of PI controller for shunt active power filter using FPGA with hardware co-simulation. , 2016, , .		4
20	Low power and high performance multi-V<inf>th</inf> dual mode logic design. , 2016, , .		3
21	Dynamic gate and substrate control charge pump circuits: a review. Analog Integrated Circuits and Signal Processing, 2015, 83, 257-270.	1.4	2
22	Active harmonic current compensation to enhance power quality. International Journal of Electrical Power and Energy Systems, 2014, 62, 144-151.	5.5	38
23	Digital non-linear controller-based active power filter for harmonic compensation. IETE Journal of Research, 2013, 59, 302.	2.6	10
24	A novel adaptive-fuzzy hysteresis current controller-based active power line conditioners for power quality enhancement. International Journal of Power Electronics, 2013, 5, 262.	0.2	6
25	PI and fuzzy logic controllers for shunt active power filter â€” A report. ISA Transactions, 2012, 51, 163-169.	5.7	80
26	Fryze power theory with adaptive-HCC based active power line conditioners. , 2011, , .		6
27	Sinusoidal extraction controller based on cascaded VSI for Active Power Filter. , 2011, , .		0
28	ANALYSIS OF LOW POWER AND SMALL SWING SELF-BIASING CMOS DESIGN. Far East Journal of Electronics and Communications, 0, , 245-261.	0.2	2