

Jongkil Park

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	Three-Terminal Ovonic Threshold Switch (3T-OTS) with Tunable Threshold Voltage for Versatile Artificial Sensory Neurons. <i>Nano Letters</i> , 2022, 22, 733-739.	9.1	10
2	SPICE Study of STDP Characteristics in a Drift and Diffusive Memristor-Based Synapse for Neuromorphic Computing. <i>IEEE Access</i> , 2022, 10, 6381-6392.	4.2	1
3	Emulating the short-term plasticity of a biological synapse with a ruthenium complex-based organic mixed ionic-electronic conductor. <i>Materials Advances</i> , 2022, 3, 2827-2837.	5.4	6
4	Bottom-Up Evolution of Diamond-Graphite Hybrid Two-Dimensional Nanostructure: Underlying Picture and Electrochemical Activity. <i>Small</i> , 2022, 18, 2105087.	10.0	4
5	Exploring dopant effects in stannic oxide nanoparticles for CO ₂ electro-reduction to formate. <i>Nature Communications</i> , 2022, 13, 2205.	12.8	61
6	A Comparison Study on Multilayered Barrier Oxide Structure in Charge Trap Flash for Synaptic Operation. <i>Crystals</i> , 2021, 11, 70.	2.2	5
7	Novel nano-plasmonic sensing platform based on vertical conductive bridge. <i>Scientific Reports</i> , 2021, 11, 3184.	3.3	1
8	Design of mid-infrared filter array based on plasmonic metal nanodiscs array and its application to on-chip spectrometer. <i>Scientific Reports</i> , 2021, 11, 12218.	3.3	12
9	A 2D material-based floating gate device with linear synaptic weight update. <i>Nanoscale</i> , 2020, 12, 24503-24509.	5.6	34
10	Enhanced analog synaptic behavior of SiNx/a-Si bilayer memristors through Ge implantation. <i>NPG Asia Materials</i> , 2020, 12, .	7.9	16
11	Presynaptic Spike-Driven Spike Timing-Dependent Plasticity With Address Event Representation for Large-Scale Neuromorphic Systems. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2020, 67, 1936-1947.	5.4	18
12	Reconfigurable Spike Routing Architectures for On-Chip Local Learning in Neuromorphic Systems. <i>Advanced Materials Technologies</i> , 2019, 4, 1800345.	5.8	16
13	A 128-Channel FPGA-Based Real-Time Spike-Sorting Bidirectional Closed-Loop Neural Interface System. <i>IEEE Transactions on Neural Systems and Rehabilitation Engineering</i> , 2017, 25, 2227-2238.	4.9	38
14	Fluoropolymer-Based Flexible Neural Prosthetic Electrodes for Reliable Neural Interfacing. <i>ACS Applied Materials & Interfaces</i> , 2017, 9, 43420-43428.	8.0	8
15	Hierarchical Address Event Routing for Reconfigurable Large-Scale Neuromorphic Systems. <i>IEEE Transactions on Neural Networks and Learning Systems</i> , 2017, 28, 2408-2422.	11.3	88
16	Event-based delay-controlled stimulator controller with priority queue for real-time closed-loop neural interface system. , 2017, , .		0
17	Energy Recycling Telemetry IC With Simultaneous 11.5 mW Power and 6.78 Mb/s Backward Data Delivery Over a Single 13.56 MHz Inductive Link. <i>IEEE Journal of Solid-State Circuits</i> , 2016, 51, 2664-2678.	5.4	52
18	A 65k-neuron 73-Mevents/s 22-pJ/event asynchronous micro-pipelined integrate-and-fire array transceiver. , 2014, , .		63