

Payam Heydari

List of Publications by Year in descending order

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61
papers

2,213
citations

331670

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315739

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61
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61
times ranked

2015
citing authors

#	ARTICLE	IF	CITATIONS
1	A CMOS Dual-Mode Brain-Computer Interface Chipset With 2-mV Precision Time-Based Charge Balancing and Stimulation-Side Artifact Suppression. IEEE Journal of Solid-State Circuits, 2022, 57, 1824-1840.	5.4	24
2	A Study of BER and EVM Degradation in Digital Modulation Schemes Due to PLL Jitter and Communication-Link Noise. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 3402-3415.	5.4	10
3	A Sub-Terahertz Wideband Stacked-Patch Antenna on a Flexible Printed Circuit for 6G Applications. IEEE Transactions on Antennas and Propagation, 2022, 70, 10047-10061.	5.1	12
4	An Analysis of CMRR Degradation in Multi-Channel Biosignal Recording Systems. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 151-155.	3.0	4
5	An mm-Wave Scalable PLL-Coupled Array for Phased-Array Applications in 65-nm CMOS. IEEE Transactions on Microwave Theory and Techniques, 2021, 69, 1439-1452.	4.6	19
6	A Teacher for All Seasons: How Behzad Razavi has Educated the Microelectronics Community. IEEE Solid-State Circuits Magazine, 2021, 13, 69-71.	0.4	0
7	Energy Efficient 100+ GHz Transceivers Enabling Beyond-5G Wireless Communications. IEEE Wireless Communications, 2021, 28, 144-151.	9.0	5
8	Transceivers for 6G Wireless Communications: Challenges and Design Solutions. , 2021, , .		5
9	A Comprehensive Analysis of Charge-Pump-Based Multi-Stage Multi-Output DC-DC Converters. , 2021, , .		2
10	An LO Leakage Suppression Technique for Blocker-Tolerant Wideband Receivers With High-<i>Q</i> Selectivity at RF Input. IEEE Journal of Solid-State Circuits, 2021, 56, 1682-1696.	5.4	9
11	CMOS Power-Amplifier Design Perspectives for 6G Wireless Communications. , 2021, , .		5
12	Terahertz Integrated Circuits and Systems for High-Speed Wireless Communications: Challenges and Design Perspectives. IEEE Open Journal of the Solid-State Circuits Society, 2021, 1, 18-36.	2.7	20
13	A Fully-Integrated 1 μ W/Channel Dual-Mode Neural Data Acquisition System for Implantable Brain-Machine Interfaces. , 2021, 2021, 5780-5783.		0
14	A CMOS Two-Element 170-GHz Fundamental-Frequency Transmitter With Direct RF-8PSK Modulation. IEEE Journal of Solid-State Circuits, 2020, 55, 282-297.	5.4	36
15	An Energy-Efficient CMOS Dual-Mode Array Architecture for High-Density ECoG-Based Brain-Machine Interfaces. IEEE Transactions on Biomedical Circuits and Systems, 2020, 14, 332-342.	4.0	16
16	Thermal Analysis of a Skull Implant in Brain-Computer Interfaces. , 2020, 2020, 3066-3069.		7
17	Optimal artifact suppression in simultaneous electrocorticography stimulation and recording for bi-directional brain-computer interface applications. Journal of Neural Engineering, 2020, 17, 026038.	3.5	6
18	A Study of Multi-Phase Injection on Accelerating Crystal Oscillator Start-Up. IEEE Transactions on Circuits and Systems II: Express Briefs, 2020, 67, 2868-2872.	3.0	5

#	ARTICLE	IF	CITATIONS
19	A 115-135-GHz 8PSK Receiver Using Multi-Phase RF-Correlation-Based Direct-Demodulation Method. IEEE Journal of Solid-State Circuits, 2019, 54, 2435-2448.	5.4	32
20	Dipole Cancellation as an Artifact Suppression Technique in Simultaneous Electrocardiography Stimulation and Recording. , 2019, , .		6
21	A 100-120GHz 20Gbps Bits-to-RF 16QAM Transmitter Using 1-bit Digital-to-Analog Interface. , 2019, , .		5
22	A benchtop system to assess the feasibility of a fully independent and implantable brain-machine interface. Journal of Neural Engineering, 2019, 16, 066043.	3.5	13
23	Analysis and Design of High-Order QAM Direct-Modulation Transmitter for High-Speed Point-to-Point mm-Wave Wireless Links. IEEE Journal of Solid-State Circuits, 2019, 54, 3161-3179.	5.4	39
24	Study and Design of a Fast Start-Up Crystal Oscillator Using Precise Dithered Injection and Active Inductance. IEEE Journal of Solid-State Circuits, 2019, 54, 2543-2554.	5.4	11
25	A CMOS MedRadio Transceiver With Supply-Modulated Power Saving Technique for an Implantable Brain-Machine Interface System. IEEE Journal of Solid-State Circuits, 2019, 54, 1541-1552.	5.4	23
26	A Millimeter-Wave Partially Overlapped Beamforming-MIMO Receiver: Theory, Design, and Implementation. IEEE Transactions on Microwave Theory and Techniques, 2019, 67, 1924-1936.	4.6	9
27	Introduction to the Special Issue on the 2019 IEEE International Solid-State Circuits Conference (ISSCC). IEEE Journal of Solid-State Circuits, 2019, 54, 3243-3246.	5.4	0
28	A CMOS V-Band PLL With a Harmonic Positive Feedback VCO Leveraging Operation in Triode Region for Phase-Noise Improvement. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 1818-1830.	5.4	11
29	Analysis and Design of a Wideband, Balun-Based, Differential Power Splitter at mm-Wave. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 1629-1633.	3.0	9
30	Electrocardiographic Encoding of Human Gait in the Leg Primary Motor Cortex. Cerebral Cortex, 2018, 28, 2752-2762.	2.9	44
31	A CMOS inductorless MedRadio OOK transceiver with a 42 μ W event-driven supply-modulated RX and a 14% efficiency TX for medical implants. , 2018, , .		7
32	A Study of Operating Condition and Design Methods to Achieve the Upper Limit of Power Gain in Amplifiers at Near- f_{\max} Frequencies. IEEE Transactions on Circuits and Systems I: Regular Papers, 2017, 64, 261-271.	5.4	45
33	Analysis and Design of a Millimeter-Wave Cavity-Backed Circularly Polarized Radiator Based on Fundamental Theory of Multi-Port Oscillators. IEEE Journal of Solid-State Circuits, 2017, 52, 3293-3311.	5.4	12
34	Neutralization Techniques for High-Frequency Amplifiers: An Overview. IEEE Solid-State Circuits Magazine, 2017, 9, 82-89.	0.4	9
35	Bottom-feed on-chip waveguide slot antenna for THz applications. , 2015, , .		3
36	An SQNR Improvement Technique Based on Magnitude Segmentation for Polar Quantizers. IEEE Transactions on Communications, 2014, 62, 3835-3841.	7.8	2

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37	Polar Quantizer for Wireless Receivers: Theory, Analysis, and CMOS Implementation. IEEE Transactions on Circuits and Systems I: Regular Papers, 2014, 61, 877-887.	5.4	20
38	A Silicon-Based 0.3 THz Frequency Synthesizer With Wide Locking Range. IEEE Journal of Solid-State Circuits, 2014, 49, 2951-2963.	5.4	66
39	A 94-GHz Extremely Thin Metasurface-Based BiCMOS On-Chip Antenna. IEEE Transactions on Antennas and Propagation, 2014, 62, 4439-4451.	5.1	42
40	Design and Implementation of a CMOS 4-Bit 12-GS/s Data Acquisition System-On-Chip. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2014, 22, 2164-2175.	3.1	3
41	A CMOS 210-GHz Fundamental Transceiver With OOK Modulation. IEEE Journal of Solid-State Circuits, 2014, 49, 564-580.	5.4	220
42	Millimeter-wave massive MIMO: the next wireless revolution?. , 2014, 52, 56-62.		657
43	An on-chip W-band bowtie slot antenna in silicon. , 2012, , .		3
44	A BiCMOS W-Band 2 \times 2 Focal-Plane Array With On-Chip Antenna. IEEE Journal of Solid-State Circuits, 2012, 47, 2355-2371.	5.4	78
45	Designs of fully on-chip antennas in (Bi)CMOS technology. , 2012, , .		4
46	W-Band Silicon-Based Frequency Synthesizers Using Injection-Locked and Harmonic Triplers. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 1307-1320.	4.6	86
47	A CMOS Distributed Amplifier With Distributed Active Input Balun Using GBW and Linearity Enhancing Techniques. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 1331-1341.	4.6	50
48	A 2-Gb/s 130-nm CMOS RF-Correlation-Based IR-UWB Transceiver Front-End. IEEE Transactions on Microwave Theory and Techniques, 2011, 59, 1117-1130.	4.6	29
49	A W-band CMOS Receiver Chipset for Millimeter-Wave Radiometer Systems. IEEE Journal of Solid-State Circuits, 2011, 46, 378-391.	5.4	92
50	Design and Analysis of a W-Band SiGe Direct-Detection-Based Passive Imaging Receiver. IEEE Journal of Solid-State Circuits, 2011, 46, 2240-2252.	5.4	94
51	An 85-95.2 GHz transformer-based injection-locked frequency tripler in 65nm CMOS. , 2010, , .		24
52	Code-modulated path-sharing multi-antenna receivers: theory and analysis. IEEE Transactions on Wireless Communications, 2009, 8, 2193-2201.	9.2	22
53	A CMOS Code-Modulated Path-Sharing Multi-Antenna Receiver Front-End. IEEE Journal of Solid-State Circuits, 2009, 44, 1321-1335.	5.4	29
54	A Multiband Inductor-Reuse CMOS Low-Noise Amplifier. IEEE Transactions on Circuits and Systems II: Express Briefs, 2008, 55, 209-213.	3.0	22

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55	A Universal Code-Modulated Path-Sharing Multi-Antenna Receiver. , 2008, , .		5
56	A Nonlinear Model for Phase Noise and Jitter in LC Oscillators. , 2007, , .		3
57	Design and Analysis of a Performance-Optimized CMOS UWB Distributed LNA. IEEE Journal of Solid-State Circuits, 2007, 42, 1892-1905.	5.4	119
58	Theoretical Analysis of Novel Multi-Order <i>LC</i> Oscillators. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2007, 54, 287-291.	2.2	23
59	A Novel Ultra-Low Power (ULP) Low Noise Amplifier using Differential Inductor Feedback. , 2006, , .		31
60	A Distributed RF Front-End for UWB Receivers. , 2006, , .		10
61	On the Dynamics of Regenerative Frequency Dividers. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2006, 53, 1413-1417.	2.2	16