

Lingjun Kong

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	Engineering Bimetal Synergistic Electrocatalysts Based on Metal-Organic Frameworks for Efficient Oxygen Evolution. <i>Small</i> , 2019, 15, e1903410.	10.0	126
2	Co ₂ N _x /nitrogen-doped reduced graphene oxide for enzymeless glucose detection. <i>Chemical Communications</i> , 2014, 50, 4921-4923.	4.1	41
3	EXIT-Chart-Based LDPC Code Design for 2D ISI Channels. <i>IEEE Transactions on Magnetics</i> , 2013, 49, 2823-2826.	2.1	31
4	Non-Binary Protograph-Based LDPC Codes for 2-D-ISI Magnetic Recording Channels. <i>IEEE Transactions on Magnetics</i> , 2017, 53, 1-5.	2.1	12
5	Coding and signal processing for ultra-high density magnetic recording channels. , 2014, , .		10
6	Parallel Differential Chaotic Shift Keying With Code Index Modulation for Wireless Communication. <i>IEEE Transactions on Communications</i> , 2022, 70, 5113-5127.	7.8	10
7	A Detector-Aware LDPC Code Optimization for Ultra-High Density Magnetic Recording Channels. <i>IEEE Transactions on Magnetics</i> , 2017, 53, 1-4.	2.1	8
8	Protograph QC-LDPC and Rate-Adaptive Polar Codes Design for MLC NAND Flash Memories. <i>IEEE Access</i> , 2019, 7, 37131-37140.	4.2	7
9	Embedded Marker Code for Channels Corrupted by Insertions, Deletions, and AWGN. <i>IEEE Transactions on Magnetics</i> , 2013, 49, 2535-2538.	2.1	5
10	Coding and Detection for Channels With Written-In Errors and Inter-Symbol Interference. <i>IEEE Transactions on Magnetics</i> , 2014, 50, 1-6.	2.1	5
11	Finite-length extrinsic information transfer analysis and design of protograph low-density parity-check codes for ultra-high-density magnetic recording channels. <i>IET Communications</i> , 2016, 10, 1303-1311.	2.2	4
12	Page-Based Dynamic Partitioning Scheduling for LDPC Decoding in MLC NAND Flash Memory. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2019, 66, 2082-2086.	3.0	4
13	Adaptive 2-D Scheduling-Based Nonbinary Majority-Logic Decoding for NAND Flash Memory. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2020, 67, 1349-1353.	3.0	4
14	Polar Code Design for Ultra-High Density Magnetic Recording Channels. , 2018, , .		3
15	Performance Analysis and Optimization of Spatially Coupled Protograph-Based Low-Density Parity-Check Codes for Two-Dimensional Magnetic Recording Systems. <i>IEEE Transactions on Magnetics</i> , 2020, 56, 1-7.	2.1	2
16	Protograph QC-LDPC codes design for multi-level cell flash memories. , 2017, , .		1
17	Design and Analysis of Spatially Coupled Protograph LDPC Codes for Two-Dimensional Magnetic Recording Systems. , 2018, , .		1
18	Rate-adaptive Polar Codes Design for MLC NAND Flash Memory. , 2018, , .		1

#	ARTICLE	IF	CITATIONS
19	DNN-aided read-voltage threshold optimization for MLC flash memory with finite block length. IET Communications, 2022, 16, 120-130.	2.2	1
20	A Key Point-Based License Plate Detection with Pyramid Network Structure. , 2021, , .		0
21	Mutual Information Approximation Based Polar Code Design for 4Tb/in^2 2D-ISI Channels. IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences, 2021, E104.A, 1075-1079.	0.3	0
22	Structural Analysis of Nonbinary Cyclic and Quasi-Cyclic LDPC Codes with $\hat{\pm}$ -Multiplied Parity-Check Matrices. IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences, 2020, E103.A, 1299-1303.	0.3	0