

# Adesh Kumar

## List of Publications by Year in descending order

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74  
papers

908  
citations

516710

16  
h-index

501196

28  
g-index

80  
all docs

80  
docs citations

80  
times ranked

450  
citing authors

#	ARTICLE	IF	CITATIONS
1	Renewable energy adoption: Design, development, and assessment of solar tree for the mountainous region. <i>International Journal of Energy Research</i> , 2022, 46, 743-759.	4.5	15
2	Application of Data Mining in Smart Grid Technology. , 2022, , 869-882.		0
3	FPGA integrated IEEE 802.15.4 ZigBee wireless sensor nodes performance for industrial plant monitoring and automation. <i>Nuclear Engineering and Technology</i> , 2022, 54, 2444-2452.	2.3	13
4	ASIC Implementation of Programmable Timer Subsystems for WSN-SOC with WISHBONE Architecture on a Single Chip. <i>The National Academy of Sciences, India</i> , 2022, 45, 231-234.	1.3	4
5	Smart Communication and Security by Key Distribution in Multicast Environment. <i>Wireless Communications and Mobile Computing</i> , 2022, 2022, 1-14.	1.2	2
6	Solar tree-based water pumping for assured irrigation in sustainable Indian agriculture environment. <i>Sustainable Production and Consumption</i> , 2022, 33, 15-27.	11.0	27
7	Nature Based Self-Learning Mechanism and Simulation of Automatic Control Smart Hybrid Antilock Braking System. <i>Wireless Personal Communications</i> , 2021, 116, 3291-3308.	2.7	16
8	FPGA application for wireless monitoring in power plant. <i>Nuclear Engineering and Technology</i> , 2021, 53, 1167-1175.	2.3	19
9	Application of Data Mining in Smart Grid Technology. <i>Advances in Information Quality and Management</i> , 2021, , 815-827.	0.2	1
10	Zigbee Internode Communication and FPGA Synthesis Using Mesh, Star and Cluster Tree Topological Chip. <i>Wireless Personal Communications</i> , 2021, 119, 1321.	2.7	14
11	Performance analysis of DSDV and OLSR wireless sensor network routing protocols using FPGA hardware and machine learning. <i>Multimedia Tools and Applications</i> , 2021, 80, 22301-22319.	3.9	32
12	Estimation of surface roughness for transparent superhydrophobic coating through image processing and machine learning. <i>Molecular Crystals and Liquid Crystals</i> , 2021, 726, 90-104.	0.9	11
13	Machine Learning Approach for Brain Tumor Detection and Segmentation. <i>International Journal of Organizational and Collective Intelligence</i> , 2021, 11, 68-84.	0.3	13
14	Nonlinear dynamics of controlled release mechanism under boundary friction. <i>Results in Engineering</i> , 2021, 11, 100265.	5.1	11
15	Smart grid and nuclear power plant security by integrating cryptographic hardware chip. <i>Nuclear Engineering and Technology</i> , 2021, 53, 3327-3334.	2.3	14
16	WPAN and IoT Enabled Automation to Authenticate Ignition of Vehicle in Perspective of Smart Cities. <i>Sensors</i> , 2021, 21, 7031.	3.8	4
17	Smart grid security with AES hardware chip. <i>International Journal of Information Technology (Singapore)</i> , 2020, 12, 49-55.	2.7	5
18	Recent progress in nano-oxides and CNTs based corrosion resistant superhydrophobic coatings: A critical review. <i>Progress in Organic Coatings</i> , 2020, 140, 105512.	3.9	58

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19	Automotive Power Window Communication with DTC Algorithm and Hardware-in-the Loop Testing. Wireless Personal Communications, 2020, 114, 3351-3366.	2.7	19
20	A review on fundamentals, constraints and fabrication techniques of superhydrophobic coatings. Progress in Organic Coatings, 2020, 142, 105557.	3.9	187
21	Cybersecurity for Smart Grid: Threats, Solutions and Standardization. Green Energy and Technology, 2020, , 17-29.	0.6	5
22	EMI Concerns, Measurements and Standards for Smart Grid. Green Energy and Technology, 2020, , 123-136.	0.6	6
23	Symmetric Cryptography and Hardware Chip Implementation on FPGA. Advances in Intelligent Systems and Computing, 2020, , 945-955.	0.6	1
24	Solar-Assisted Cloud-Enabled Intelligent Mini Refrigerator. Advances in Intelligent Systems and Computing, 2020, , 765-772.	0.6	0
25	Implementation of HMI Using VISUAL BASIC .NET for Induction Hardening Process. Advances in Intelligent Systems and Computing, 2020, , 913-920.	0.6	5
26	Bionics Based Camouflage Clothing for Soldiers. Advances in Intelligent Systems and Computing, 2020, , 415-423.	0.6	0
27	Design and implementation of I2C interface on FPGA for space borne AIS receiver in embedded system. International Journal of Information Technology (Singapore), 2019, 11, 331-340.	2.7	2
28	3D Multilayer Mesh NoC Communication and FPGA Synthesis. Wireless Personal Communications, 2019, 106, 1855-1873.	2.7	13
29	Designing of FIR Filter Using FPGA: A Review. Lecture Notes in Electrical Engineering, 2019, , 493-505.	0.4	2
30	Smart Grid Security with Cryptographic Chip Integration. EAI Endorsed Transactions on Energy Web, 2019, 6, 157037.	0.4	6
31	Memristive Phase Change Memory. International Journal of Engineering and Advanced Technology, 2019, 9, 4169-4173.	0.3	0
32	Integration of Cognitive Radio with Heterogeneous Smart Grid Communication Architecture. Advances in Intelligent Systems and Computing, 2018, , 981-989.	0.6	4
33	Bionic Functionality of Prosthetic Hand. Advances in Intelligent Systems and Computing, 2018, , 1177-1190.	0.6	0
34	GSM-Based Prepaid Meter. Advances in Intelligent Systems and Computing, 2018, , 1467-1479.	0.6	0
35	Fat Tree NoC Design and Synthesis. Advances in Intelligent Systems and Computing, 2018, , 1749-1756.	0.6	28
36	Modeling and Simulation of ECG Signal for Heartbeat Application. Advances in Intelligent Systems and Computing, 2018, , 503-511.	0.6	3

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37	Automatic Street Lighting System. Advances in Intelligent Systems and Computing, 2018, , 879-887.	0.6	4
38	VGA Application in Text Display Using FPGA. Advances in Intelligent Systems and Computing, 2018, , 1575-1585.	0.6	0
39	Machine Learning Based Resource Utilization and Pre-estimation for Network on Chip (NoC) Communication. Wireless Personal Communications, 2018, 102, 2211-2231.	2.7	29
40	Antenna Path Loss Propagation in the Dehradun Region at 1800MHz in L-Band. Lecture Notes in Electrical Engineering, 2018, , 171-179.	0.4	0
41	Development of Wireless Data Acquisition and Control System for Smart Microgrid. Lecture Notes in Electrical Engineering, 2018, , 667-673.	0.4	5
42	Bionics Based Emergency Contact for Treatment of Epilepsy. , 2018, , .		2
43	IoT-Based Implementation of Field Area Network Using Smart Grid Communication Infrastructure. Smart Cities, 2018, 1, 176-189.	9.4	26
44	Cross Layer Optimization and Simulation of Smart Grid Home Area Network. Modelling and Simulation in Engineering, 2018, 2018, 1-14.	0.7	2
45	A facile approach to develop modified nano-silica embedded polystyrene based transparent superhydrophobic coating. Materials Letters, 2018, 233, 340-343.	2.6	32
46	Bluetooth-Controlled Robot Using Windows Phone Application. Advances in Intelligent Systems and Computing, 2018, , 1199-1205.	0.6	0
47	Application of Multi Layer Artificial Neural Network in the Diagnosis System: A Systematic Review. IAES International Journal of Artificial Intelligence, 2018, 7, 138.	0.8	14
48	Storage Optimization of Automated Storage and Retrieval Systems Using Breadth-First Search Algorithm. Lecture Notes in Electrical Engineering, 2017, , 229-238.	0.4	7
49	IC Packaging: 3D IC Technology and Methods. Lecture Notes in Electrical Engineering, 2017, , 303-317.	0.4	2
50	FM Receiver Design Using Programmable PLL. Wireless Personal Communications, 2017, 97, 773-787.	2.7	4
51	Blueprint of a CMOS Charge Pump for Phase-Locked Loop Synthesizers with High Efficiency. Lecture Notes in Electrical Engineering, 2017, , 249-257.	0.4	0
52	Five Stage Telecommunication Switching Design and Synthesis. Wireless Personal Communications, 2017, 97, 3043-3059.	2.7	3
53	Visual Recurrence Analysis of Chaotic and Regular Motion of a Multiple Pendulum System. Arabian Journal for Science and Engineering, 2017, 42, 2711-2716.	3.0	3
54	Efficiency of Modified Phosphatic Fertilizer in Transplanted Rice (Oryza sativa L.) Under Partially Reclaimed Salt Affected Soil of Uttar Pradesh. The National Academy of Sciences, India, 2017, 40, 5-7.	1.3	1

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55	Microcontroller and FPGA-Based Analysis of 8×48 LED Matrix Display with Keyboard Interface. <i>Advances in Intelligent Systems and Computing</i> , 2017, , 887-894.	0.6	1
56	Four-Stage Telecommunication Switching Design and Synthesis. <i>Advances in Intelligent Systems and Computing</i> , 2017, , 1079-1085.	0.6	1
57	Channel Capacity in MIMO OFDM System. <i>Advances in Intelligent Systems and Computing</i> , 2017, , 1113-1120.	0.6	1
58	Design and FPGA Implementation of 32-Point FFT Processor. <i>Advances in Intelligent Systems and Computing</i> , 2017, , 285-292.	0.6	1
59	Design and FPGA Implementation of DSSS for Near-Far Effect in MANET. <i>Advances in Intelligent Systems and Computing</i> , 2017, , 425-434.	0.6	1
60	Stabilization of Underactuated Mechanical System Using LQR Technique. <i>Advances in Intelligent Systems and Computing</i> , 2017, , 601-608.	0.6	2
61	Scalable Design and Synthesis of 3D Mesh Network on Chip. <i>Advances in Intelligent Systems and Computing</i> , 2017, , 661-666.	0.6	23
62	Wireless Sensor Network Based Smart Grid Communications: Cyber Attacks, Intrusion Detection System and Topology Control. <i>Electronics (Switzerland)</i> , 2017, 6, 5.	3.1	78
63	Design Goal Based Implementation of Energy Efficient Greek Unicode Reader for Natural Language Processing. <i>International Journal of Smart Home</i> , 2016, 10, 181-190.	0.4	2
64	Design and FPGA Implementation of Intelligent Helmet Hardware Chip for Safe Drive. <i>International Journal of Engineering and Technology</i> , 2016, 8, 2831-2836.	0.1	0
65	Secured Network on Chip (NoC) Architecture and Routing with Modified TACIT Cryptographic Technique. <i>Procedia Computer Science</i> , 2015, 48, 158-165.	2.0	14
66	Rotator on Chip (RoC) Design Based on Ring Topological NoC. <i>Procedia Computer Science</i> , 2015, 45, 540-548.	2.0	7
67	Design and FPGA Synthesis of Three Stage Telecommunication Switching in HDL Environment. <i>Procedia Computer Science</i> , 2015, 48, 454-460.	2.0	6
68	Comparative Design and Analysis of Mesh, Torus and Ring NoC. <i>Procedia Computer Science</i> , 2015, 48, 330-337.	2.0	27
69	Design and FPGA Implementation of DWT, Image Text Extraction Technique. <i>Procedia Computer Science</i> , 2015, 57, 1015-1025.	2.0	20
70	Effect of Height of the Substrate and Width of the Patch on the Performance Characteristics of Microstrip Antenna. <i>International Journal of Electrical and Computer Engineering</i> , 2015, 5, 1441.	0.7	22
71	Network on chip for DTMF Decoder and TDM switching in telecommunication network with HDL environment. , 2013, , .		1
72	Design and hardware development of power window control mechanism using microcontroller. , 2013, , .		9

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73	Network on Chip for 3D Mesh Structure with Enhanced Security Algorithm in HDL Environment. International Journal of Computer Applications, 2012, 59, 6-12.	0.2	6
74	Hardware chip performance analysis of different FFT architecture. International Journal of Electronics, 0, , 1-17.	1.4	6