

Aydin Ilker Karsilayan

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/1770420/publications.pdf>

Version: 2024-02-01

18
papers

99
citations

1684188
5
h-index

1474206
9
g-index

18
all docs

18
docs citations

18
times ranked

113
citing authors

#	ARTICLE	IF	CITATIONS
1	Self-Powered Rectifier for Energy Harvesting Applications. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2011, 1, 308-320.	3.6	22
2	An LC Quadrature VCO Using Capacitive Source Degeneration Coupling to Eliminate Bi-Modal Oscillation. IEEE Transactions on Circuits and Systems I: Regular Papers, 2012, 59, 1871-1879.	5.4	22
3	A 3â€“6-GHz Highly Linear I-Channel Receiver With Over +3.0-dBm In-Band P_{1dB} and 200-MHz Baseband Bandwidth Suitable for 5G Wireless and Cognitive Radio Applications. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 3134-3147.	5.4	16
4	Sensitivity of Multiband ZP-OFDM Ultra-Wide-Band and Receivers to Synchronization Errors. IEEE Transactions on Signal Processing, 2007, 55, 729-734.	5.3	11
5	THD+Noise Estimation in Class-D Amplifiers. , 2007, , .		6
6	A large-signal blocker robust transimpedance amplifier for coexisting radio receivers in 45nm CMOS. , 2011, , .		5
7	A 12-Bit 125-MS/s 2.5-Bit/Cycle SAR-Based Pipeline ADC Employing a Self-Biased Gain Boosting Amplifier. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 3618-3629.	5.4	5
8	A current-mode flash ADC for low-power continuous-time sigma delta modulators. , 2013, , .		4
9	Adaptive Narrowband Interference Suppression inÂMultiband OFDM Receivers. Circuits, Systems, and Signal Processing, 2008, 27, 475-489.	2.0	2
10	Blocker-Tolerant and High-Sensitivity \$Delta Sigma \$ Correlation Digitizer for Radar and Coherent Receiver Applications. IEEE Transactions on Microwave Theory and Techniques, 2017, 65, 3453-3463.	4.6	2
11	An Interference-Tolerant Synchronization Scheme for Wireless Communication Systems Based on Direct Sequence Spread Spectrum. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 415-427.	5.4	2
12	A 200MSPS time-interleaved 12-bit ADC system with digital calibration. , 2017, , .		1
13	An Efficient Sinusoid-Like Pseudo Random Sequence Modulator/Demodulator System With Reduced Adjacent Channel Leakage and High Rejection to Random and Systematic Interference. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 3790-3803.	5.4	1
14	Impact of Narrowband Interference on Multi-Band OFDM Ultra Wideband Communication Systems. , 2006, , .		0
15	Blocker and jitter tolerant wideband ΣΔ modulators. , 2012, , .		0
16	Blocker tolerant wideband continuous time sigma-delta modulator for wireless applications. , 2014, , .		0
17	Automatic tuning scheme for second order bandstop filters based on phase comparison. Analog Integrated Circuits and Signal Processing, 2020, 102, 331-340.	1.4	0
18	Blocker-Tolerant Wideband Continuous-Time âˆ† ADC Using Embedded Minimally-Invasive Filtering. , 2020, , .		0