

Jeng-Hau Lin

List of Publications by Year in descending order

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4
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2682572

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docs citations

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48
citing authors

#	ARTICLE	IF	CITATIONS
1	Vulnerability of Hardware Neural Networks to Dynamic Operation Point Variations. IEEE Design and Test, 2020, 37, 75-84.	1.2	2
2	Accelerating Local Binary Pattern Networks with Software-Programmable FPGAs. , 2019, , .		5
3	Binarized Convolutional Neural Networks with Separable Filters for Efficient Hardware Acceleration. , 2017, , .		17
4	Simulation Algorithms With Exponential Integration for Time-Domain Analysis of Large-Scale Power Delivery Networks. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2016, 35, 1681-1694.	2.7	19