

# Miki Trifunovic

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/1552326/publications.pdf>

Version: 2024-02-01

10  
papers

96  
citations

1684188

5  
h-index

1474206

9  
g-index

10  
all docs

10  
docs citations

10  
times ranked

153  
citing authors

#	ARTICLE	IF	CITATIONS
1	Solution-processed polycrystalline silicon on paper. Applied Physics Letters, 2015, 106, .	3.3	34
2	Single-grain Si thin-film transistors on flexible polyimide substrate fabricated from doctor-blade coated liquid-Si. Applied Physics Letters, 2013, 102, .	3.3	23
3	Solution-based polycrystalline silicon transistors produced on a paper substrate. Npj Flexible Electronics, 2017, 1, .	10.7	11
4	Solution-Based Fabrication of Polycrystalline Si Thin-Film Transistors from Recycled Polysilanes. ACS Sustainable Chemistry and Engineering, 2017, 5, 5642-5645.	6.7	10
5	Wafer-level direct bonding of optimized superconducting NbN for 3D chip integration. Physica C: Superconductivity and Its Applications, 2021, 582, 1353823.	1.2	8
6	Single-Grain Si Thin-Film Transistors for Monolithic 3D-ICs and Flexible Electronics. IEICE Transactions on Electronics, 2014, E97.C, 227-237.	0.6	4
7	Analysis of polydihydrosilane crystallization by excimer laser annealing. Thin Solid Films, 2017, 638, 73-80.	1.8	3
8	(Invited) Single-Grain Si Tfts Fabricated on a Precursor from Doctor-Blade Coated Liquid-Si. ECS Transactions, 2014, 64, 31-37.	0.5	1
9	29.1: Solution-Processed Poly-Si TFTs at Paper Compatible Temperatures. Digest of Technical Papers SID International Symposium, 2015, 46, 415-418.	0.3	1
10	Polycrystalline silicon TFTs on a paper substrate using solution-processed silicon. , 2016, , .		1