

# John Wawrzynek

## List of Publications by Year in descending order

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Version: 2024-02-01

18  
papers

742  
citations

1307594

7  
h-index

1474206

9  
g-index

18  
all docs

18  
docs citations

18  
times ranked

568  
citing authors

#	ARTICLE	IF	CITATIONS
1	A view of the parallel computing landscape. Communications of the ACM, 2009, 52, 56-67.	4.5	412
2	RAMP: Research Accelerator for Multiple Processors. IEEE Micro, 2007, 27, 46-57.	1.8	112
3	Stream Computations Organized for Reconfigurable Execution (SCORE). Lecture Notes in Computer Science, 2000, , 605-614.	1.3	66
4	Stream computations organized for reconfigurable execution. Microprocessors and Microsystems, 2006, 30, 334-354.	2.8	59
5	OpenRCL: Low-Power High-Performance Computing with Reconfigurable Devices. , 2010, , .		26
6	Analysis of quasi-static scheduling techniques in a virtualized reconfigurable machine. , 2002, , .		12
7	Exploring Many-Core Design Templates for FPGAs and ASICs. International Journal of Reconfigurable Computing, 2012, 2012, 1-15.	0.2	10
8	Stochastic, spatial routing for hypergraphs, trees, and meshes. , 2003, , .		10
9	The SFRA. , 2004, , .		9
10	JPEG Quality Transcoding Using Neural Networks Trained with a Perceptual Error Measure. Neural Computation, 1999, 11, 267-296.	2.2	8
11	Stochastic spatial routing for reconfigurable networks. Microprocessors and Microsystems, 2006, 30, 301-318.	2.8	7
12	HyVE: Hybrid Vertex-Edge Memory Hierarchy for Energy-Efficient Graph Processing. IEEE Transactions on Computers, 2019, 68, 1131-1146.	3.4	6
13	ASTRO: Synthesizing application-specific reconfigurable hardware traces to exploit memory-level parallelism. Microprocessors and Microsystems, 2015, 39, 553-564.	2.8	4
14	Extracting memory-level parallelism through reconfigurable hardware traces. , 2013, , .		1
15	Discriminatively Fortified Computing with Reconfigurable Digital Fabric. , 2011, , .		0
16	Receiver Adaptive Beamforming and Interference of Indoor Environments in mmWave. , 2018, , .		0
17	NewGraph: Balanced Large-Scale Graph Processing on FPGAs with Low Preprocessing Overheads. , 2018, , .		0
18	Stream Computations Organized for Reconfigurable Execution. , 2008, , 203-218.		0