

# Wing-Shan Tam

## List of Publications by Year in descending order

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28  
papers

121  
citations

1684188

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h-index

1474206

9  
g-index

29  
all docs

29  
docs citations

29  
times ranked

91  
citing authors

#	ARTICLE	IF	CITATIONS
1	Dynamic Analysis of Two-Phase Switched-Capacitor DC-DC Converters. IEEE Transactions on Power Electronics, 2014, 29, 302-317.	7.9	21
2	An overview of charge pumping circuits for flash memory applications. , 2011, , .		12
3	On the design of power- and area-efficient Dickson charge pump circuits. Analog Integrated Circuits and Signal Processing, 2014, 78, 373-389.	1.4	11
4	Transient Sensitivity of Sectorial Split-Drain Magnetic Field-Effect Transistor. IEEE Transactions on Magnetics, 2013, 49, 4048-4051.	2.1	8
5	Parasitic capacitance effect on the performance of two-phase switched-capacitor DC-DC converters. IET Power Electronics, 2015, 8, 1195-1208.	2.1	8
6	High-Performance Resistorless Sub-1V Bandgap Reference Circuit Based on Piecewise Compensation Technique. , 2007, , .		7
7	Design strategy for 2-phase switched capacitor charge pump. , 2008, , .		7
8	Generating sub-1V reference voltages from a resistorless CMOS bandgap reference circuit by using a piecewise curvature temperature compensation technique. Microelectronics Reliability, 2010, 50, 1054-1061.	1.7	7
9	Design strategy for two-phase switched capacitor step-up charge pump. , 2009, , .		5
10	Split-Drain Magnetic Field-Effect Transistor Channel Charge Trapping and Stress Induced Sensitivity Deterioration. IEEE Transactions on Magnetics, 2014, 50, 1-4.	2.1	5
11	Snapback breakdown ESD device based on zener diodes on silicon-on-insulator technology. Microelectronics Reliability, 2014, 54, 1163-1168.	1.7	5
12	A novel gate boosting circuit for 2-phase high voltage CMOS charge pump. , 2009, , .		3
13	Double edge-triggered half-static clock-gated D-type flip-flop. , 2010, , .		3
14	Modelling of maximum power efficiency of charge pump circuits. Electronics Letters, 2014, 50, 1233-1234.	1.0	3
15	Analysis of ESD discharge current distribution and area optimization of VDMOS gate protection structure. Microelectronics Reliability, 2010, 50, 622-626.	1.7	2
16	AN ENERGY EFFICIENT HALF-STATIC CLOCK-GATING D-TYPE FLIP-FLOP. Journal of Circuits, Systems and Computers, 2010, 19, 635-654.	1.5	2
17	Thermal stability of sectorial split-drain magnetic field-effect transistors. Microelectronics Reliability, 2014, 54, 1115-1118.	1.7	2
18	Sensitivity distortion of split-drain MAGFET under alternating magnetic field. , 2015, , .		2

#	ARTICLE	IF	CITATIONS
19	A low power CMOS magnetic field sensor consisting of a MAGFET and a pulse width modulated readout circuit. , 2017, , .		2
20	Double Edge-Triggered Half-Static Clock-Gating D-Type Flip-Flop. Solid State Electronics Letters, 2021, 3, 1-4.	1.0	2
21	A Wideband three-stage rail-to-rail power amplifier driving large capacitive load. , 2008, , .		1
22	Area efficient 2 <sup>&amp;lt;sup&gt;n&lt;/sup&gt;</sup> switched capacitor charge pump. , 2009, , .		1
23	A low-voltage charge pump with wide current driving capability. , 2010, , .		1
24	Modeling of terminal ring structures for high-voltage power MOSFETs. Microelectronics Reliability, 2012, 52, 1645-1650.	1.7	1
25	Design and analysis of 600 V power MOSFET with multiple field limiting ring. , 2008, , .		0
26	Current mode track and hold circuit with 50MS/sec speed and 8-bit resolution. , 2008, , .		0
27	A low power temperature insensitive voltage supervisory circuit in metal gate technology. , 2009, , .		0
28	CMOS low power split-drain MAGFET based magnetic field strength sensor. Microelectronics Journal, 2020, 100, 104759.	2.0	0