

# Colin Yu Lin

## List of Publications by Year in descending order

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10  
papers

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1937685

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citing authors

#	ARTICLE	IF	CITATIONS
1	FPGA High-level Synthesis versus Overlay. Computer Architecture News, 2017, 44, 92-97.	2.5	5
2	Low Cost 1D DCT Core for Multiple Video Codec. Chinese Journal of Electronics, 2016, 25, 1052-1057.	1.5	3
3	A Computationally Efficient Reconfigurable FIR Filter Architecture Based on Coefficient Occurrence Probability. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2016, 35, 1297-1308.	2.7	14
4	Design space exploration for sparse matrix-matrix multiplication on FPGAs. International Journal of Circuit Theory and Applications, 2013, 41, 205-219.	2.0	19
5	Timing-constrained minimum area/power FPGA memory mapping. , 2013, , .		3
6	Energy-efficient dataflow computations on FPGAs using application-specific coarse-grain architecture synthesis. Computer Architecture News, 2012, 40, 58-63.	2.5	10
7	A Model for Matrix Multiplication Performance on FPGAs. , 2011, , .		15
8	Design space exploration for sparse matrix-matrix multiplication on FPGAs. , 2010, , .		14
9	Operation scheduling for FPGA-based reconfigurable computers. , 2009, , .		3
10	Automatic system architecture synthesis for FPGA-based reconfigurable computers. , 2009, , .		2