Jiayuan Meng

List of Publications by Year
in descending order

13 papers	**2,967** citations	**4** h-index	**5** g-index
13 all docs	**13** docs citations	**13** times ranked	**1779** citing authors

#	Article	IF	Citations
1	Workflow performance improvement using model-based scheduling over multiple clusters and clouds. Future Generation Computer Systems, 2016, 54, 206-218.	7.5	29
2	SKOPE. , 2014, , .		15
3	SESH Framework: A Space Exploration Framework for GPU Application and Hardware Codesign. Lecture Notes in Computer Science, 2014, , 182-202.	1.3	2
4	Dataflow-driven GPU performance projection for multi-kernel transformations. , 2012, , .		10
5	A reconfigurable simulator for large-scale heterogeneous multicore architectures. , 2011, , .		5
6	A Performance Study for Iterative Stencil Loops on GPUs with Ghost Zone Optimizations. International Journal of Parallel Programming, 2011, 39, 115-142.	1.5	37
7	Dynamic warp subdivision for integrated branch and memory divergence tolerance. Computer Architecture News, 2010, 38, 235-246.	2.5	39
8	Exploiting inter-thread temporal locality for chip multithreading. , 2010, , .		14
9	Best-effort parallel execution framework for Recognition and mining applications. , 2009, , .		46
10	Performance modeling and automatic ghost zone optimization for iterative stencil loops on GPUs. , 2009, , .		103
11	Rodinia: A benchmark suite for heterogeneous computing. , 2009, , .		2,150
12	Avoiding cache thrashing due to private data placement in last-level cache for manycore scaling. , 2009, , .		21
13	A performance study of general-purpose applications on graphics processors using CUDA. Journal of Parallel and Distributed Computing, 2008, 68, 1370-1380.	4.1	496