

Eun Jung Kim

List of Publications by Year in descending order

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37
papers

327
citations

1478505

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37
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docs citations

37
times ranked

201
citing authors

#	ARTICLE	IF	CITATIONS
1	Recursive partitioning multicast: A bandwidth-efficient routing for Networks-on-Chip. , 2009, , .		49
2	Pseudo-Circuit: Accelerating Communication for On-Chip Interconnection Networks. , 2010, , .		31
3	A Hybrid Buffer Design with STT-MRAM for On-Chip Interconnects. , 2012, , .		28
4	Communication-Aware Globally-Coordinated On-Chip Networks. IEEE Transactions on Parallel and Distributed Systems, 2012, 23, 242-254.	5.6	26
5	Adaptive data compression for high-performance low-power on-chip networks. , 2008, , .		23
6	A Domain-Specific On-Chip Network Design for Large Scale Cache Systems. , 2007, , .		22
7	Active-Routing: Compute on the Way for Near-Data Processing. , 2019, , .		16
8	Mediaworm: a qos capable router architecture for clusters. IEEE Transactions on Parallel and Distributed Systems, 2002, 13, 1261-1274.	5.6	11
9	A heuristic for peak power constrained design of network-on-chip (NoC) based multimode systems. , 0, , .		11
10	Remote Control: A Simple Deadlock Avoidance Scheme for Modular Systems-on-Chip. IEEE Transactions on Computers, 2021, 70, 1928-1941.	3.4	11
11	A Comprehensive Framework for Enhancing Security in InfiniBand Architecture. IEEE Transactions on Parallel and Distributed Systems, 2007, 18, 1393-1406.	5.6	10
12	ROAL: A Randomly Ordered Activation and Layering Protocol for Ensuring K-Coverage in Wireless Sensor Networks. , 2007, , .		10
13	Adaptive Prefetching Scheme Using Web Log Mining in Cluster-Based Web Systems. , 2009, , .		10
14	Effective Dynamic Thermal Management for MPEG-4 decoding. , 2007, , .		9
15	Integrated admission and congestion control for QoS support in clusters. , 0, , .		7
16	I2SEMS: Interconnects-Independent Security Enhanced Shared Memory Multiprocessor Systems. Parallel Architecture and Compilation Techniques (PACT), Proceedings of the International Conference on, 2007, , .	0.0	7
17	Design and Analysis of On-Chip Networks for Large-Scale Cache Systems. IEEE Transactions on Computers, 2010, 59, 332-344.	3.4	7
18	Fly-Over: A Light-Weight Distributed Power-Gating Mechanism for Energy-Efficient Networks-on-Chip. , 2017, , .		7

#	ARTICLE	IF	CITATIONS
19	Performance analysis of a QoS capable cluster interconnect. Performance Evaluation, 2005, 60, 275-302.	1.2	4
20	An overview of security issues in cluster interconnects. , 2006, , .		4
21	Fast Secure Communications in Shared Memory Multiprocessor Systems. IEEE Transactions on Parallel and Distributed Systems, 2011, 22, 1714-1721.	5.6	4
22	Security Enhancement in InfiniBand Architecture. , 0, , .		3
23	Integration of admission, congestion, and peak power control in QoS-aware clusters. Journal of Parallel and Distributed Computing, 2010, 70, 1087-1099.	4.1	3
24	Computing En-Route for Near-Data Processing. IEEE Transactions on Computers, 2021, 70, 906-921.	3.4	3
25	Calculation of deadline missing probability in a QoS capable cluster interconnect. , 0, , .		2
26	QoS provisioning in clusters. Computer Architecture News, 2001, 29, 120-129.	2.5	2
27	WSN03-6: Assuring K-Coverage in the Presence of Mobility in Wireless Sensor Networks. IEEE Global Telecommunications Conference (GLOBECOM), 2006, , .	0.0	2
28	PID controlled thermal management in photonic network-on-chip. , 2015, , .		2
29	Instant attack stopper in InfiniBand architecture. , 2005, , .		1
30	Temperature-aware scheduler based on thermal behavior grouping in multicore systems. , 2009, , .		1
31	Scalable and Efficient Bounds Checking for Large-Scale CMP Environments. , 2011, , .		1
32	QoS provisioning in clusters: an investigation of router and NIC design. , 0, , .		0
33	A source identification scheme against DDoS attacks in cluster interconnects. , 0, , .		0
34	Exploring IBA Design Space for Improved Performance. IEEE Transactions on Parallel and Distributed Systems, 2007, 18, 498-510.	5.6	0
35	A session key caching and prefetching scheme for secure communication in cluster systems. Journal of Parallel and Distributed Computing, 2010, 70, 732-742.	4.1	0
36	A Case for Handshake in Nanophotonic Interconnects. , 2013, , .		0

#	ARTICLE	IF	CITATIONS
37	Dual Pattern Compression Using Data-Preprocessing for Large-Scale GPU Architectures. , 2019, , .		0