Alain J Martin

List of Publications by Year
in descending order

15 papers	850 citations	11 h-index	15 g-index
19 all docs	19 docs citations	19 times ranked	248 citing authors

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#	Article	IF	Citations
1	A Compact Transregional Model for Digital CMOS Circuits Operating Near Threshold. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2014, 22, 2041-2053.	3.1	51
2	A Necessary and Sufficient Timing Assumption for Speed-Independent Circuits. , 2009, , .		19
3	Can Asynchronous Techniques Help the SoC Designer?. , 2006, , .		1
4	Et2: A Metric for Time and Energy Efficiency of Computation. Plenum Series in Computer Science, 2002, , 293-315.	0.1	50
5	Towards an energy complexity of computation. Information Processing Letters, 2001, 77, 181-187.	0.6	69
6	Design of a delay-insensitive multiply-accumulate unit. The Integration VLSI Journal, 1993, 15, 291-311.	2.1	9
7	Asynchronous datapaths and the design of an asynchronous adder. Formal Methods in System Design, 1992, 1, 117-137.	0.8	86
8	Distributed sorting. Science of Computer Programming, 1990, 15, 119-133.	1.9	11
9	The design of an asynchronous microprocessor. Computer Architecture News, 1989, 17, 99-110.	2.5	85
10	Design of Synchronization Algorithms. , 1989, , 447-478.		3
11	Compiling communicating processes into delay-insensitive VLSI circuits. Distributed Computing, 1986, 1, 226-234.	0.8	283
12	Distributed mutual exclusion on a ring of processes. Science of Computer Programming, 1985, 5, 265-276.	1.9	45
13	Fair mutual exclusion with unfair P and V operations. Information Processing Letters, 1985, 21, 97-100.	0.6	12
14	The probe: An addition to communication primitives. Information Processing Letters, 1985, 20, 125-130.	0.6	47
15	An axiomatic definition of synchronization primitives. Acta Informatica, 1981, 16, 219.	0.5	19