

# Quoc An Vu

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/10460953/publications.pdf>

Version: 2024-02-01

21  
papers

2,036  
citations

516710

16  
h-index

713466

21  
g-index

21  
all docs

21  
docs citations

21  
times ranked

3727  
citing authors

#	ARTICLE	IF	CITATIONS
1	Seamless Stitching of Graphene Domains on Polished Copper (111) Foil. <i>Advanced Materials</i> , 2015, 27, 1376-1382.	21.0	314
2	Transferred wrinkled Al <sub>2</sub> O <sub>3</sub> for highly stretchable and transparent graphene carbon nanotube transistors. <i>Nature Materials</i> , 2013, 12, 403-409.	27.5	295
3	Two-terminal floating-gate memory with van der Waals heterostructures for ultrahigh on/off ratio. <i>Nature Communications</i> , 2016, 7, 12725.	12.8	271
4	Tuning Carrier Tunneling in van der Waals Heterostructures for Ultrahigh Detectivity. <i>Nano Letters</i> , 2017, 17, 453-459.	9.1	178
5	Two-terminal Multibit Optical Memory via van der Waals Heterostructure. <i>Advanced Materials</i> , 2019, 31, e1807075.	21.0	168
6	Unusually efficient photocurrent extraction in monolayer van der Waals heterostructure by tunnelling through discretized barriers. <i>Nature Communications</i> , 2016, 7, 13278.	12.8	120
7	A High On/Off Ratio Floating Gate Memristor Array on a Flexible Substrate via CVD Grown Large Area 2D Layer Stacking. <i>Advanced Materials</i> , 2017, 29, 1703363.	21.0	116
8	Near-zero hysteresis and near-ideal subthreshold swing in h-BN encapsulated single-layer MoS <sub>2</sub> field-effect transistors. <i>2D Materials</i> , 2018, 5, 031001.	4.4	104
9	Transfer assembly for two-dimensional van der Waals heterostructures. <i>2D Materials</i> , 2020, 7, 022005.	4.4	87
10	Wafer-Scale Single-Crystalline AB-Stacked Bilayer Graphene. <i>Advanced Materials</i> , 2016, 28, 8177-8183.	21.0	79
11	Tunable Negative Differential Resistance in van der Waals Heterostructures at Room Temperature by Tailoring the Interface. <i>ACS Nano</i> , 2019, 13, 8193-8201.	14.6	69
12	Chemically Modulated Band Gap in Bilayer Graphene Memory Transistors with High On/Off Ratio. <i>ACS Nano</i> , 2015, 9, 9034-9042.	14.6	56
13	Tunneling Photocurrent Assisted by Interlayer Excitons in Staggered van der Waals Hetero-Bilayers. <i>Advanced Materials</i> , 2017, 29, 1701512.	21.0	51
14	Nondestructive Characterization of Graphene Defects. <i>Advanced Functional Materials</i> , 2013, 23, 5183-5189.	14.9	44
15	High-Performance Photoinduced Memory with Ultrafast Charge Transfer Based on MoS <sub>2</sub> /SWCNTs Network Van Der Waals Heterostructure. <i>Small</i> , 2019, 15, e1804661.	10.0	42
16	Efficient Gate Modulation in a Screening-Engineered MoS <sub>2</sub> /Single-Walled Carbon Nanotube Network Heterojunction Vertical Field-Effect Transistor. <i>ACS Applied Materials &amp; Interfaces</i> , 2019, 11, 25516-25523.	8.0	20
17	Electronics and Optoelectronics Based on Two-Dimensional Materials. <i>Journal of the Korean Physical Society</i> , 2018, 73, 1-15.	0.7	16
18	Sorting centimetre-long single-walled carbon nanotubes. <i>Scientific Reports</i> , 2016, 6, 30836.	3.3	3

#	ARTICLE	IF	CITATIONS
19	Memristors: A High On/Off Ratio Floating Gate Memristor Array on a Flexible Substrate via CVD-Grown Large Area 2D Layer Stacking (Adv. Mater. 44/2017). Advanced Materials, 2017, 29, .	21.0	1
20	Devices layer up for stability. Nature Electronics, 2018, 1, 98-99.	26.0	1
21	Tuning the inhomogeneous charge transport in ZnO interfaces for ultrahigh on/off ratio top-gated field-effect-transistor arrays. Nano Research, 2020, 13, 3033-3040.	10.4	1